## Silicon Carbide Schottky Diode

650 V, 20 A

# FFSP2065A

#### Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

#### Features

- Max Junction Temperature 175°C
- Avalanche Rated 95 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- This Device is Pb–Free, Halogen Free/BFR Free and RoHS Compliant

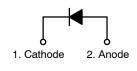
#### Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

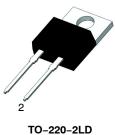


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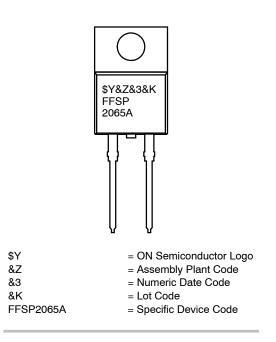


Schottky Diode



TO-220-2LD CASE 340BB

### MARKING DIAGRAM



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## FFSP2065A

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage		650	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 1)		95	mJ
١ <sub>F</sub>	Continuous Rectified Forward Current @ T <sub>C</sub> < 147°C		20	А
	Continuous Rectified Forward Current @ T <sub>C</sub> <	25	А	
I <sub>F,Max</sub>	Non-Repetitive Peak Forward Surge Current	T <sub>C</sub> = 25°C, 10 μs	1225	А
		T <sub>C</sub> = 150°C, 10 μs	1000	А
I <sub>F,SM</sub>	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	105	А
I <sub>F,RM</sub>	Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	58	А
P <sub>TOT</sub>	Power Dissipation	T <sub>C</sub> = 25°C	187	W
		T <sub>C</sub> = 150°C	31	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		–55 to +175	°C
	TO247 Mounting Torque, M3 Screw	60	Ncm	

### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1.  $E_{AS}$  of 95 mJ is based on starting  $T_J = 25^{\circ}$ C, L = 0.5 mH, I<sub>AS</sub> = 19.5 A, V = 50 V.

### **THERMAL CHARACTERISTICS**

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max (Note 2)	0.8	°C/W

2. Pulse: Test Pulse width = 300  $\mu$ s, Duty Cycle = 2%.

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Мах	Unit
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 20 A, T <sub>C</sub> = 25°C	-	1.50	1.75	V
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 125°C	-	1.6	2.0	1
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 175°C	-	1.72	2.4	1
I <sub>R</sub>	Reverse Current	$V_{R} = 650 \text{ V}, \text{ T}_{C} = 25^{\circ}\text{C}$	-	-	200	μΑ
		$V_{R} = 650 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$	-	-	400	1
		V <sub>R</sub> = 650 V, T <sub>C</sub> = 175°C	-	-	600	1
Q <sub>C</sub>	Total Capacitive Charge	V = 400 V	-	64	-	nC
С	Total Capacitance	V <sub>R</sub> = 1 V, f = 100 kHz	-	1085	-	pF
		V <sub>R</sub> = 200 V, f = 100 kHz	-	117	-	]
		V <sub>R</sub> = 400 V, f = 100 kHz	_	88	_	]

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

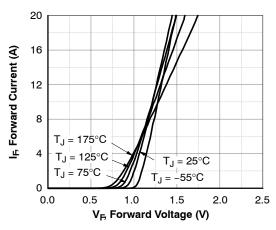
#### **ORDERING INFORMATION**

Part Number	Top Marking	Package	Packing Method	Quantity
FFSP2065A	FFSP2065A	TO-220-2LD	Tube	50 Units

## FFSP2065A

### **TYPICAL CHARACTERISTICS**

(T<sub>J</sub> = 25°C unless otherwise noted)



**Figure 1. Forward Characteristics** 

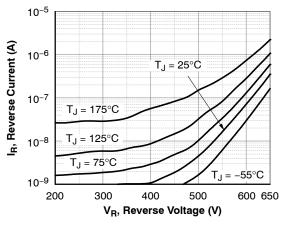
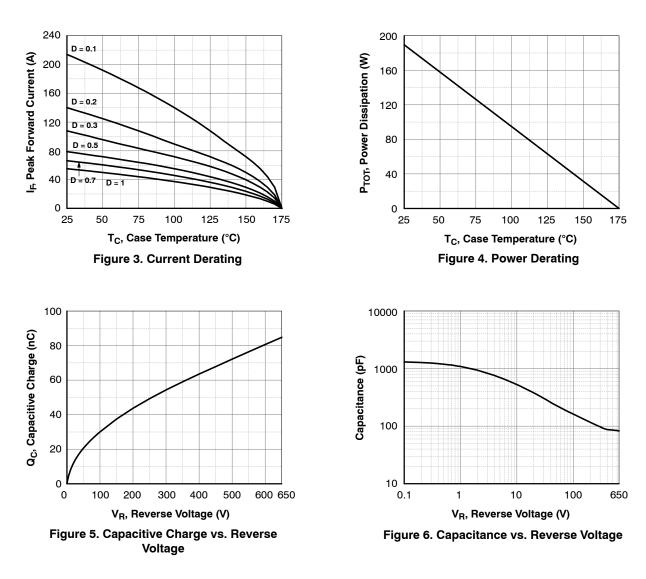


Figure 2. Reverse Characteristics



### FFSP2065A

#### TYPICAL CHARACTERISTICS (Continued)

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$ 

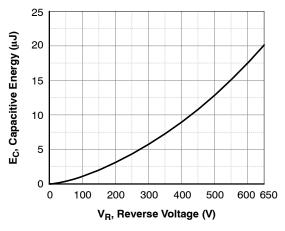


Figure 7. Capacitance Stored Energy

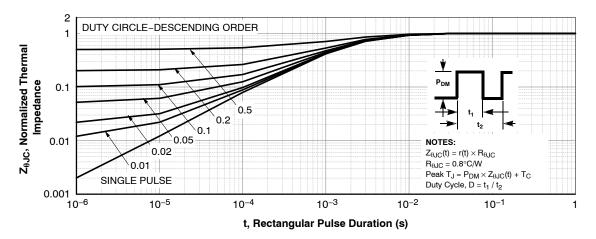
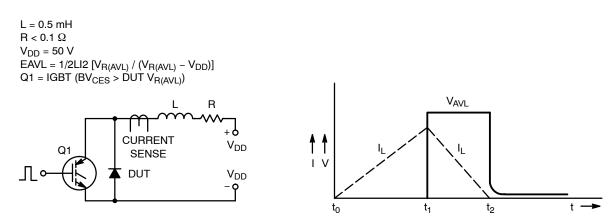


Figure 8. Junction-to-Case Transient Thermal Response Curve

#### **TEST CIRCUIT AND WAVEFORMS**

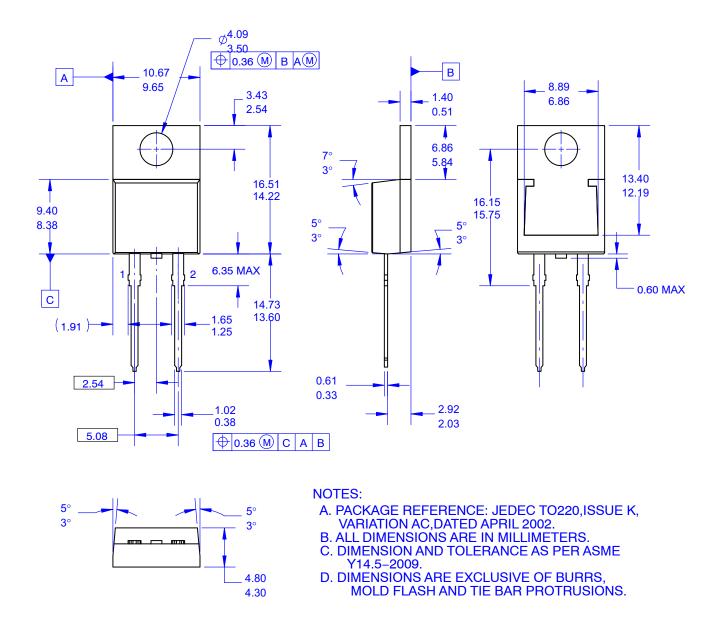






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