

# Silicon Carbide Schottky Diode

1200 V, 10 A

## FFSP10120A

### Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

### Features

- Max Junction Temperature 175°C
- Avalanche Rated 100 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- This Device is Pb-Free, Halogen Free/BFR Free and RoHS Compliant

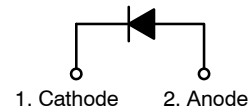
### Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

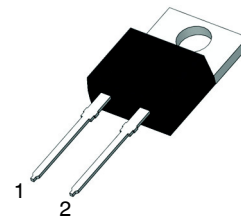


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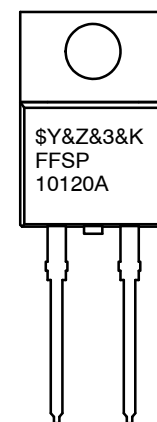


Schottky Diode



TO-220-2LD  
CASE 340BB

### MARKING DIAGRAM



|            |                         |
|------------|-------------------------|
| \$Y        | = ON Semiconductor Logo |
| &Z         | = Assembly Plant Code   |
| &3         | = Numeric Date Code     |
| &K         | = Lot Code              |
| FFSP10120A | = Specific Device Code  |

### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

# FFSP10120A

## ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

| Symbol                            | Parameter                                                     | Value                                    | Unit |   |
|-----------------------------------|---------------------------------------------------------------|------------------------------------------|------|---|
| V <sub>RRM</sub>                  | Peak Repetitive Reverse Voltage                               | 1200                                     | V    |   |
| E <sub>AS</sub>                   | Single Pulse Avalanche Energy (Note 1)                        | 100                                      | mJ   |   |
| I <sub>F</sub>                    | Continuous Rectified Forward Current @ T <sub>C</sub> < 148°C | 10                                       | A    |   |
| I <sub>F,Max</sub>                | Non-Repetitive Peak Forward Surge Current                     | T <sub>C</sub> = 25°C, 10 μs             | 850  | A |
|                                   |                                                               | T <sub>C</sub> = 150°C, 10 μs            | 800  | A |
| I <sub>F,SM</sub>                 | Non-Repetitive Forward Surge Current                          | Half-Sine Pulse, t <sub>p</sub> = 8.3 ms | 90   | A |
| I <sub>F,RM</sub>                 | Repetitive Forward Surge Current                              | Half-Sine Pulse, t <sub>p</sub> = 8.3 ms | 35   | A |
| P <sub>TOT</sub>                  | Power Dissipation                                             | T <sub>C</sub> = 25°C                    | 168  | W |
|                                   |                                                               | T <sub>C</sub> = 150°C                   | 28   | W |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range                       | -55 to +175                              | °C   |   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. E<sub>AS</sub> of 100 mJ is based on starting T<sub>J</sub> = 25°C, L = 0.5 mH, I<sub>AS</sub> = 20 A, V = 150 V.

## THERMAL CHARACTERISTICS

| Symbol           | Parameter                                 | Value | Unit |
|------------------|-------------------------------------------|-------|------|
| R <sub>θJC</sub> | Thermal Resistance, Junction to Case, Max | 0.89  | °C/W |

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

| Symbol         | Parameter               | Test Condition                                  | Min | Typ  | Max  | Unit |
|----------------|-------------------------|-------------------------------------------------|-----|------|------|------|
| V <sub>F</sub> | Forward Voltage         | I <sub>F</sub> = 10 A, T <sub>C</sub> = 25°C    | -   | 1.45 | 1.75 | V    |
|                |                         | I <sub>F</sub> = 10 A, T <sub>C</sub> = 125°C   | -   | 1.7  | 2.0  |      |
|                |                         | I <sub>F</sub> = 10 A, T <sub>C</sub> = 175°C   | -   | 2.0  | 2.4  |      |
| I <sub>R</sub> | Reverse Current         | V <sub>R</sub> = 1200 V, T <sub>C</sub> = 25°C  | -   | -    | 200  | μA   |
|                |                         | V <sub>R</sub> = 1200 V, T <sub>C</sub> = 125°C | -   | -    | 300  |      |
|                |                         | V <sub>R</sub> = 1200 V, T <sub>C</sub> = 175°C | -   | -    | 400  |      |
| Q <sub>C</sub> | Total Capacitive Charge | V = 800 V                                       | -   | 62   | -    | nC   |
| C              | Total Capacitance       | V <sub>R</sub> = 1 V, f = 100 kHz               | -   | 612  | -    | pF   |
|                |                         | V <sub>R</sub> = 400 V, f = 100 kHz             | -   | 58   | -    |      |
|                |                         | V <sub>R</sub> = 800 V, f = 100 kHz             | -   | 47   | -    |      |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## ORDERING INFORMATION

| Part Number | Top Marking | Package    | Packing Method | Quantity |
|-------------|-------------|------------|----------------|----------|
| FFSP10120A  | FFSP10120A  | TO-220-2LD | Tube           | 50 Units |

TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

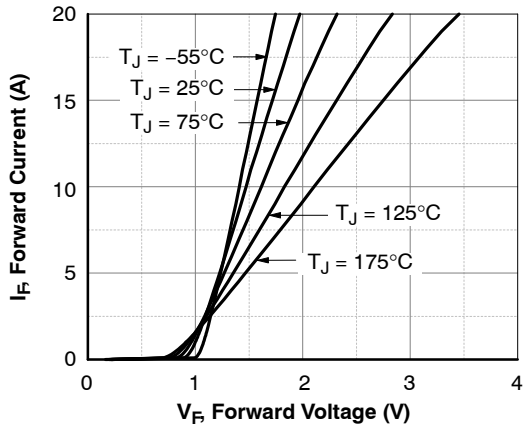


Figure 1. Forward Characteristics

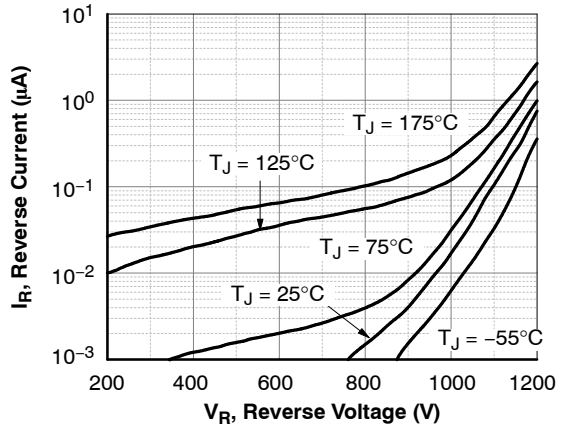


Figure 2. Reverse Characteristics

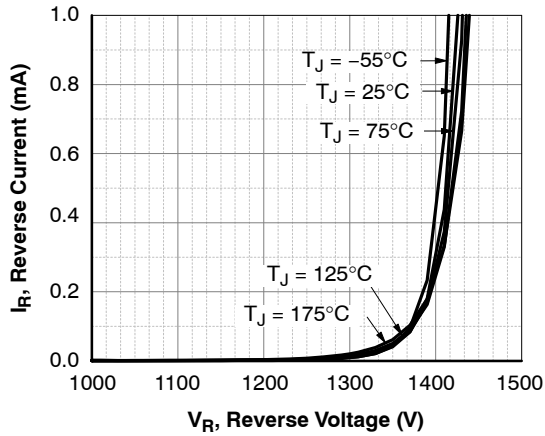


Figure 3. Reverse Characteristics

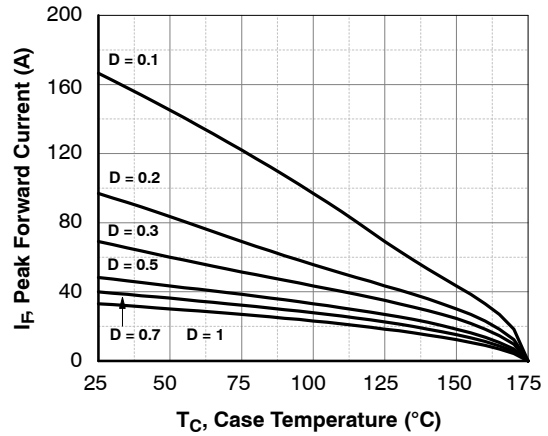


Figure 4. Current Derating

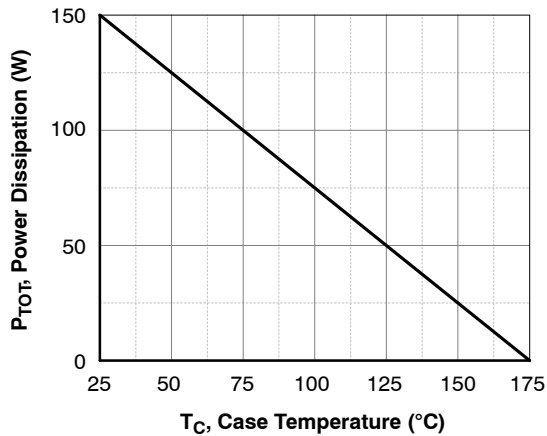


Figure 5. Power Derating

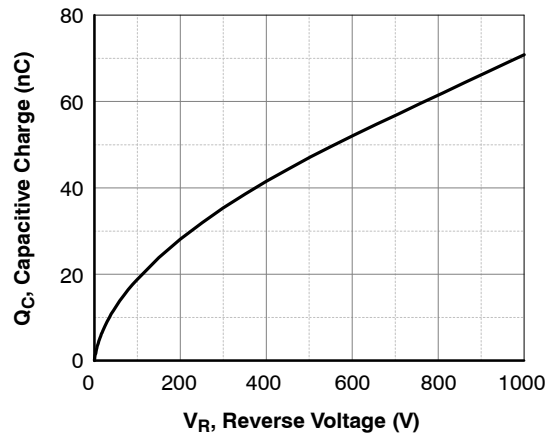


Figure 6. Capacitive Charge vs. Reverse Voltage

# FFSP10120A

## TYPICAL CHARACTERISTICS (Continued)

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

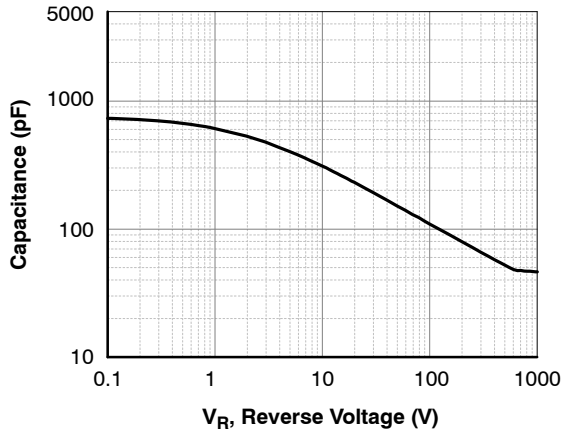


Figure 7. Capacitance vs. Reverse Voltage

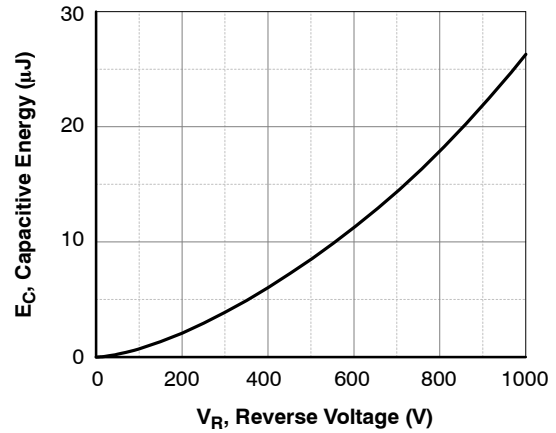


Figure 8. Capacitance Stored Energy

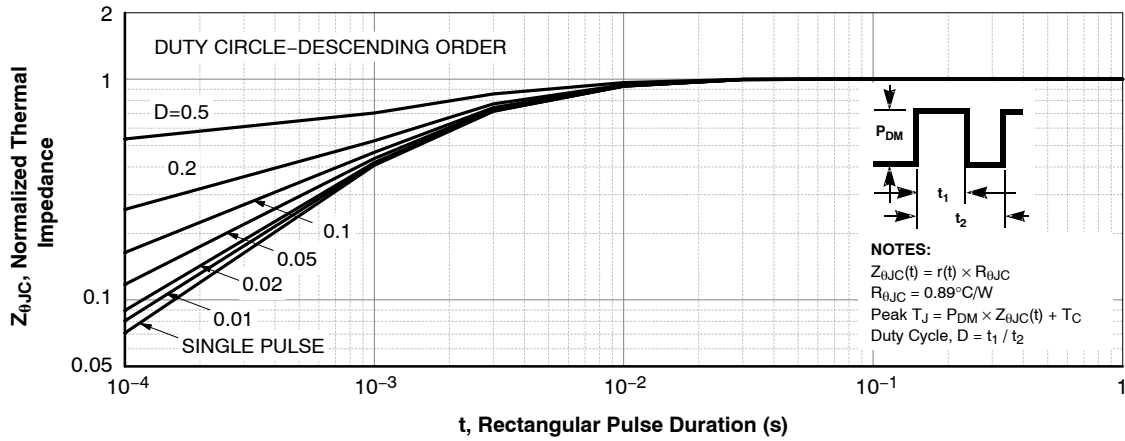


Figure 9. Junction-to-Case Transient Thermal Response Curve

## TEST CIRCUIT AND WAVEFORMS

$L = 0.5 \text{ mH}$   
 $R < 0.1 \ \Omega$   
 $V_{DD} = 50 \text{ V}$   
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)} / (V_{R(AVL)} - V_{DD})]$   
 $Q1 = \text{IGBT (} BV_{CES} > \text{DUT } V_{R(AVL)} \text{)}$

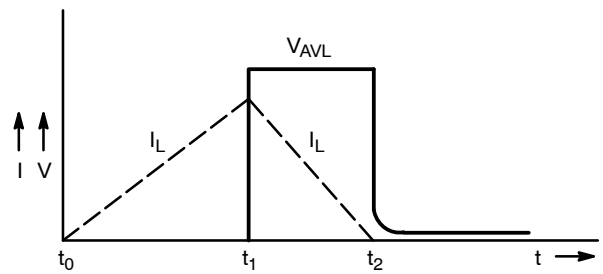
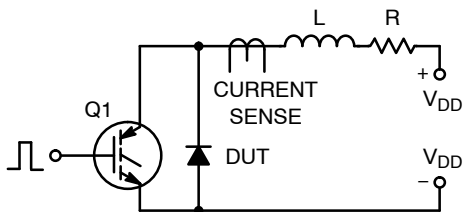


Figure 10. Unclamped Inductive Switching Test Circuit & Waveform

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

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TO-220-2LD  
CASE 340BB  
ISSUE O

DATE 31 AUG 2016



### NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC, DATED APRIL 2002.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

|                         |                    |                                                                                                                                                                                     |
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