

glass passivated Bridge Rectifier

Features

- Glass Passivated Die Construction
- Low Forward Voltage Drop
- High Current Capability
- High Surge Current Capability
- Designed for Surface Mount Application
- Plastic Material UL Flammability 94V-O





Mechanical Date

• Case: ABS, Molded Plastic

 Terminals: Plated Leads Solderable per MIL-STD-202, Method 208

Polarity: As Marked on CaseMounting Position: AnyMarking:Type Number

Major Ratings and Characteristics

<u> </u>	
Ι _ο	0.8A,1.0A
V_{RRM}	200 V to 1000 V
I _{FSM}	30 A
I _R	5 μΑ
V_{F}	1.00V
T _j max.	150 °C

Maximum Ratings & Thermal Characteristics (T_A = 25 °C unless otherwise noted)

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Items	Symbol	ABS2	ABS4	ABS6	ABS8	ABS10	UNIT
Peak Repetitive Reverse Voltage DC Blocking Voltage	V_{RRM} V_{DC}	200	400	600	800	1000	V
RMS Reverse Voltage	V _{R(RMS)}	140	280	420	560	700	V
Average Rectified Output Current (1) Average Rectified Output Current (2)	Io	0.8 1.0					Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load(JEDEC Method)	I _{FSM}	30				Α	
Current Squared Time	l ² t	3.7				A ² S	
Thermal resistance from junction to lead ⁽¹⁾	$R_{\theta JL}$	25			°C/W		
Thermal resistance from junction to ambient ⁽¹⁾	$R_{\theta JA}$	80				°C/W	
Thermal resistance from junction to ambient ⁽²⁾	$R_{\theta JA}$	62.5				°C/W	
Operating junction temperature range	TJ	-55 to +150				$^{\circ}$	
Storage temperature range	T _{STG}	-55 to +125			$^{\circ}$		

Note 1: Mounted on glass epoxy PC board with 1.3mm² solder pad.

Note 2: Mounted on aluminum substrate PC board with 1.3mm² solder pad.

Electrical Characteristics (T_A = 25 °C unless otherwise noted)

Items	Test conditions		Symbol	Min	Туре	Max	UNIT
Instantaneous forward voltage per leg	I _F =1.0A ⁽³⁾		V_{F}	-	-	1.00	V
Reverse current	V _R =V _{DC}	T _j =25℃	I _R	-	-	5	
		T _j =125℃		-	-	500	μA

Note: 3.Pulse test:300µs pulse width,1% duty cycle.



Characteristic Curves (T_A=25 ℃ unless otherwise noted)

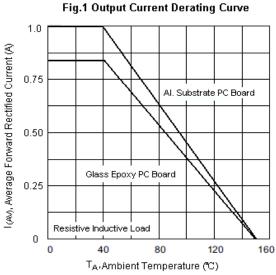
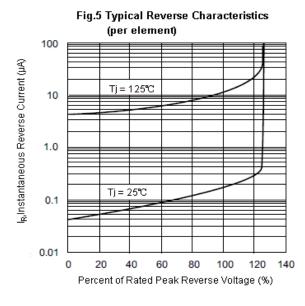
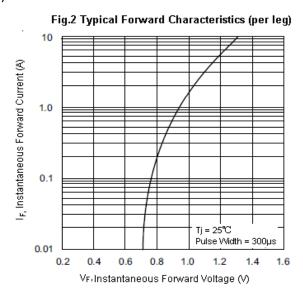


Fig.3 Maximum Peak Forward Surge Current (per leg) 35 30 Irsm , Peak Forward Surge Current (A) 20 10 TA = 25°C Single Half Sine-Wave Pulse Width = 8.3 ms (JEDEC Method) 0 1.0 10 100 Number Of Cycles at 60 Hz





100 T_j = 25°C t = 1.0MHz

10 T_j = 25°C
t = 1.0MHz

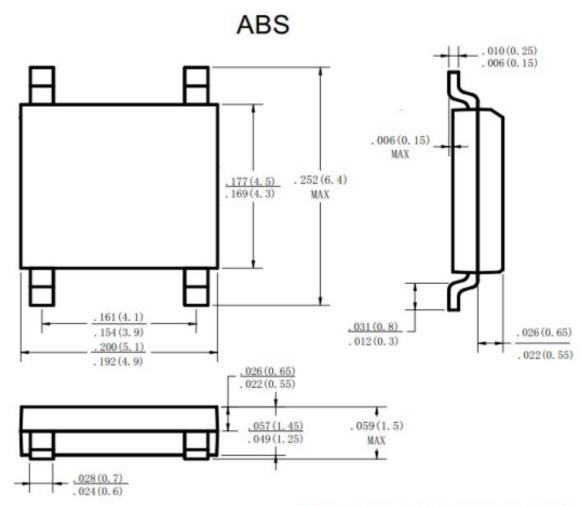
1 1 10 100

V_R, Reverse Voltage (V)

Fig.4 Typical Junction Capacitance



Package Outline



Dimensions in inches and (millimeters)

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Rev: 01.06.2018 3/3 www.leiditech.com