

## SK54L THRU SK520L

### 5.0 AMP SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS

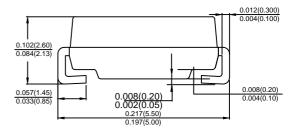
#### **Features**

- High current capacity,low V<sub>F</sub>
- · Low Power Loss, High Efficiency
- · Ideally Suited for Automatic Assembly
- · For Use in Low Voltage Application
- Plastic Case Material has UL Flammability Classification Rating 94V-0

### **Mechanical Data**

- · Case: Molded plastic SMB
- Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- · Polarity: Color band dentes cathode end
- · Mounting Position: Any
- · Making: Type Number
- Solder Dip: 260 °C /10Sec whole body

# 0.083(2.11) 0.075(1.91) 0.187(4.75) 0.187(4.75) 0.183(4.15)



Dimensions in inches and (millimeters)

## **Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	SK54L	SK545L	SK55L	SK56L	SK58L	SK510L	SK515L	SK520L	Unit
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	40	45	50	60	80	100	150	200	V
Maximum RMS Voltage	V <sub>RMS</sub>	28	32	35	42	56	70	105	140	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	40	45	50	60	80	100	150	200	V
Average Rectified Output Current @T∟ =90°C	lf(AV)	5.0								А
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	<b>I</b> FSM	120								Α
Forward Voltage @IF=5.0A (Note 1)	V <sub>FM</sub>	0.45			0.5	0	.6	0.8	35	V
Peak Reverse Current @T <sub>A</sub> =25°C	0.2 0.05								mA	
At Rated DC Blocking Voltage @TA =100°C	<b>I</b> R	10				5				mΑ
I <sup>2</sup> t Rating for fusing (t <8.3ms)	l <sup>2</sup> t	59.76								A <sup>2</sup> s
Typical Junction Capacitance (Note 2)	СJ	300					170	70		pF
Typical Thermal Resistance (Note 3)	Rθ JA	75								°C/W
Operating Temperature Range	Тл	-55 to+150								$^{\circ}$
Storage Temperature Range	Тѕтс	-55 to +150								$^{\circ}\mathbb{C}$

Note: 1.Pulse Test with PW=300usec,1%Duty Cycle.

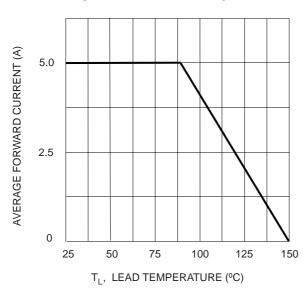
- 2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C
- 3. Thermal Resistance from Junction to lead mounted on P.C.B. with 0.3" x 0.3" (8.0 mm x 8.0 mm) copper pad areas.

version:04 1 of 3 www.dyelec.com



# SK54L THRU SK520L

Fig. 1 Forward Current Derating Curve



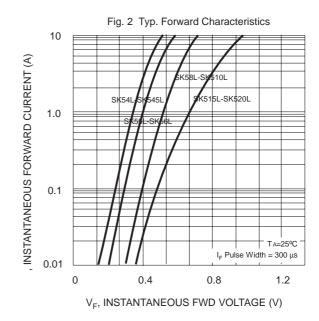


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

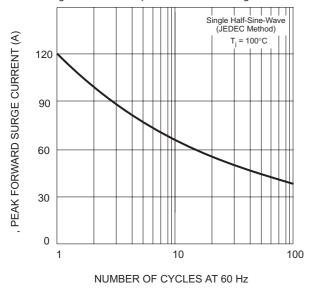
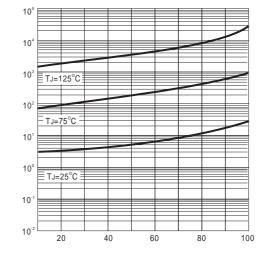
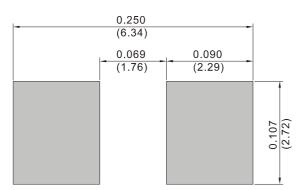


FIG.4TYPICALREVERSE CHRACTERISTIC



### FIG.5 MOUNTING PAD LAYOUT



PERCENT OF RATED PEAK REVERSE VOLTAGE ,  $\!\%$ 

REVERSE CURRENT (uA)



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version:04 3 of 3 www.dyelec.com