<u>MOSFET</u> – Power, N-Channel, SUPERFET[®] III, FRFET[®]

650 V, 75 A, 27.4 mΩ

NTH4L027N65S3F

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- 700 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 23 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 259 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 1972 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

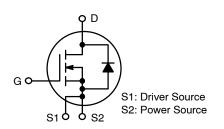
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar



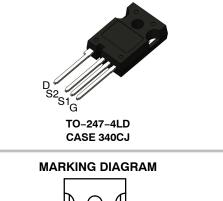
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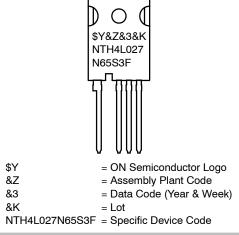
www.onsemi.com

V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	27.4 m Ω @ 10 V	75 A	



POWER MOSFET





ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter		Value	Unit	
V _{DSS}	Drain to Source Voltage		650	V	
V _{GSS}	Gate to Source Voltage	– DC	±30	V	
		– AC (f > 1 Hz)	±30	-	
Ι _D	Drain Current	– Continuous (T _C = 25°C)	75	А	
		– Continuous (T _C = 100°C)	60		
I _{DM}	Drain Current	– Pulsed (Note 1)	187.5	A	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	le Pulsed Avalanche Energy (Note 2)		mJ	
I _{AS}	Avalanche Current (Note 2)	Avalanche Current (Note 2)		A	
E _{AR}	Repetitive Avalanche Energy (Note 1)		5.95	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		50		
PD	Power Dissipation	(T _C = 25°C)	595	W	
		– Derate Above 25°C	4.76	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8"	from Case for 5 seconds	300	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality shows be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. $I_{AS} = 15 \text{ A}, \text{ R}_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$. 3. $I_{SD} \leq 37.5 \text{ A}, \text{ di/dt} \leq 200 \text{ A/}\mu\text{s}, \text{ V}_{DD} \leq 400 \text{ V}$, starting $T_{J} = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.21	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4L027N65S3F	NTH4L027N65S3F	TO-247-4LD	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS	·	•			
BV _{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650			V
		V_{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700			V
$\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 15$ mA, Referenced to $25^{\circ}C$		0.61		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
		V_{DS} = 520 V, T_{C} = 125°C		361		
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30$ V, $V_{DS} = 0$ V			±100	nA

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 3 \text{ mA}$	3.0		5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V_{GS} = 10 V, I _D = 35 A		23	27.4	mΩ
9fs	Forward Transconductance	V_{DS} = 20 V, I _D = 37.5 A		56		S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 1 MHz	7690	pF
C _{oss}	Output Capacitance		200	pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	1972	pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	352	pF
Q _{g(tot)}	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 37.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	259	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	72	nC
Q _{gd}	Gate to Drain "Miller" Charge]	99	nC
ESR	Equivalent Series Resistance	f = 1 MHz	1.2	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 37.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	51	ns
t _r	Turn-On Rise Time	$R_g = 2 \Omega$ (Note 4)	26	ns
t _{d(off)}	Turn-Off Delay Time		122	ns
t _f	Turn-Off Fall Time		6.0	ns

SOURCE-DRAIN DIODE CHARACTERISTICS

۱ _S	Maximum Continuous Source to Drain Diode Forward Current			75	А
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current			187.5	А
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 37.5 A$		1.3	V
t _{rr}	Reverse Recovery Time	$V_{DD} = 400 \text{ V}, I_{SD} = 37.5 \text{ A},$	168		ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/µs	1014		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

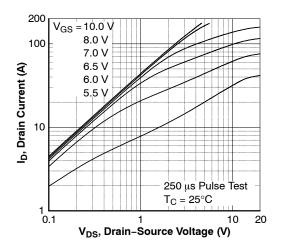
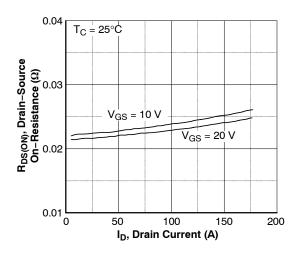
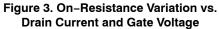


Figure 1. On-Region Characteristics





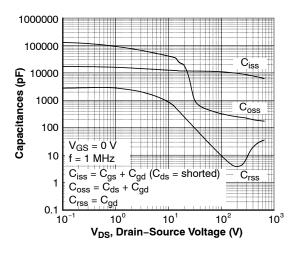


Figure 5. Capacitance Characteristics

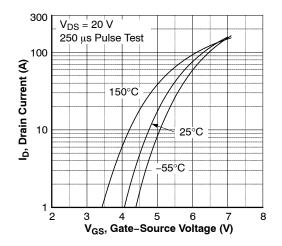
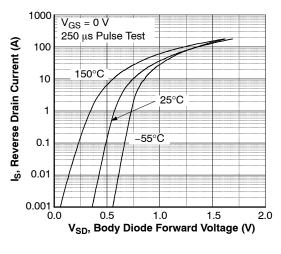


Figure 2. Transfer Characteristics





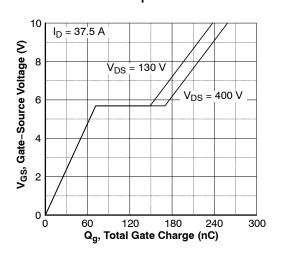
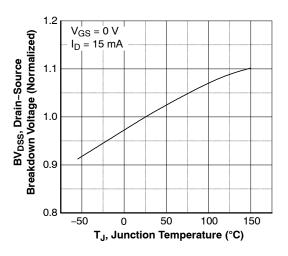
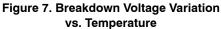


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)





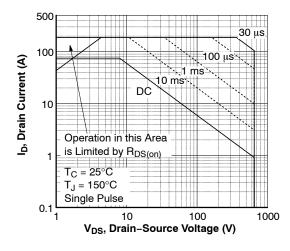


Figure 9. Maximum Safe Operating Area

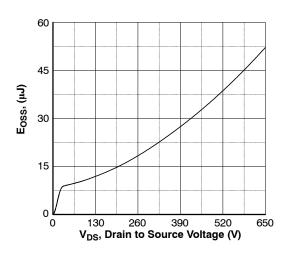


Figure 11. E_{OSS} vs. Drain to Source Voltage

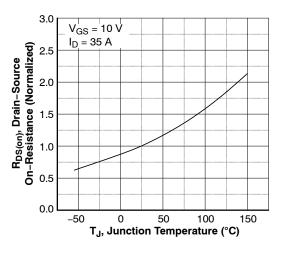


Figure 8. On–Resistance Variation vs. Temperature

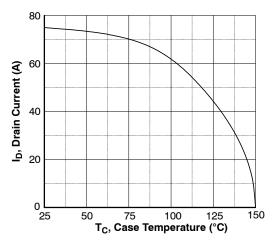


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

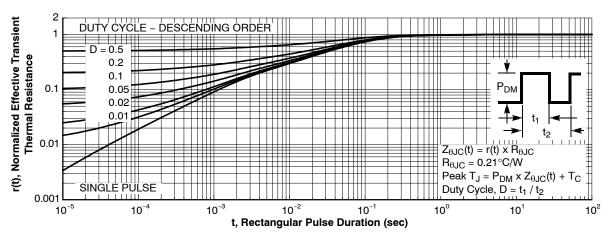
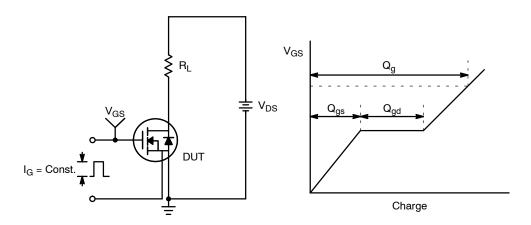


Figure 12. Transient Thermal Response Curve





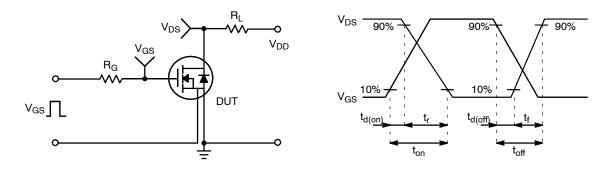
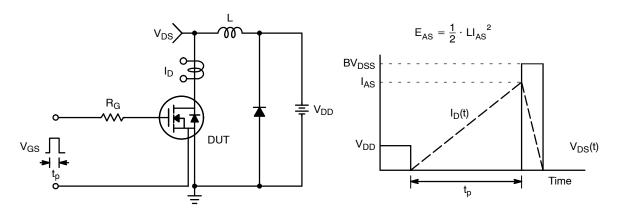


Figure 14. Resistive Switching Test Circuit & Waveforms





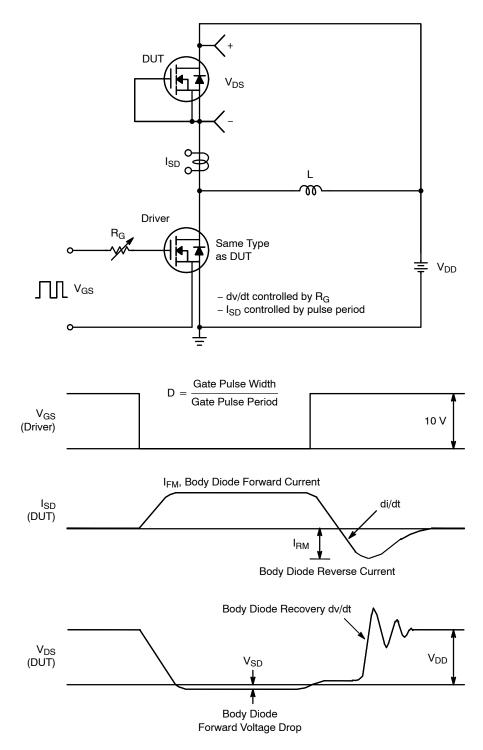


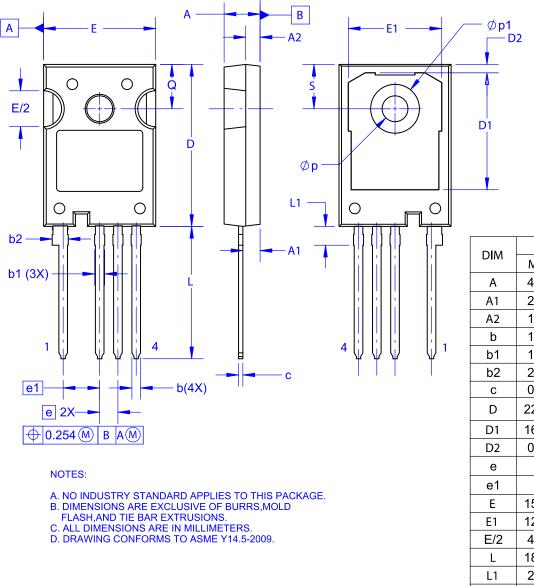
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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TO-247-4LD CASE 340CJ ISSUE A

DATE 16 SEP 2019



	MIL	MILLIMETERS				
DIM	MIN	NOM	MAX			
А	4.80	5.00	5.20			
A1	2.10	2.40	2.70			
A2	1.80	2.00	2.20			
b	1.07	1.20	1.33			
b1	1.20	1.40	1.60			
b2	2.02	2.22	2.42			
С	0.50	0.60	0.70			
D	22.34	22.54	22.74			
D1	16.00	16.25	16.50			
D2	0.97	1.17	1.37			
е	2	2.54 BSC	2			
e1	Ę	5.08 BSC	2			
Е	15.40	15.60	15.80			
E1	12.80	13.00	13.20			
E/2	4.80	5.00	5.20			
L	18.22	18.42	18.62			
L1	2.42	2.62	2.82			
р	3.40	3.60	3.80			
p1	6.60	6.80	7.00			
Q	5.97	6.17	6.37			
S	5.97	6.17	6.37			

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