

**Vishay Siliconix** 

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# N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
	0.033 at V <sub>GS</sub> = 4.5 V	4.9		
20	0.040 at V <sub>GS</sub> = 2.5 V	4.4		
	0.051 at V <sub>GS</sub> = 1.8 V	3.9		

### **FEATURES**

Halogen-free According to IEC 61249-2-21
Available

3 kΩ

N-Channel

- TrenchFET<sup>®</sup> Power MOSFET
- ESD Protected: 3000 V

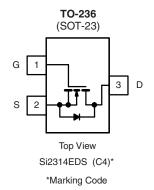
#### **APPLICATIONS**

• LI-Ion Battery Protection

GO



RoHS COMPLIANT HALOGEN FREE Available



Ordering Information: Si2314EDS-T1-E3 (Lead (Pb)-free) Si2314EDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	20		V
Gate-Source Voltage		V <sub>GS</sub>	± 12		
	T <sub>A</sub> = 25 °C	I	4.9	3.77	
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T <sub>A</sub> = 70 °C	– I <sub>D</sub>	3.9	3.0	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	15		A
Avalanche Current <sup>b</sup>	L = 0.1 mH	I <sub>AS</sub>	15		
Single Avalanche Energy		E <sub>AS</sub>	1	1.25	mJ
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	1.0		А
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.25	0.75	W
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.80	0.48	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum lumation to Ambienta	t ≤ 5 s	R <sub>thJA</sub>	75	100	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	120	166	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	40	50	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

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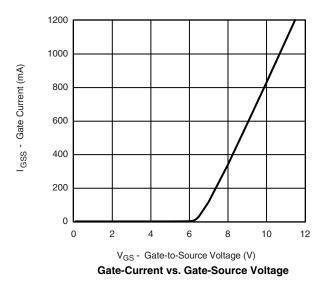
SPECIFICATIONS $T_A = 25^{\circ}$	C, unless o	otherwise noted					
			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, I <sub>D</sub> = 250 µA	20			v	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.45		0.95	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 1.5		
Zara Cata Valtaga Drain Current	1	$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			75		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge$ 10 V, $V_{GS}$ = 4.5 V	15			А	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		0.027	0.033	0.040 Ω	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$		0.033	0.040		
	_	$V_{GS} = 1.8 \text{ V}, I_D = 4.0 \text{ A}$		0.042	0.051		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		40		S	
Diode Forward Voltage	V <sub>SD</sub>	$I_{S} = 1.0 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			11.0	14.0	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.0 \text{ A}$		1.5			
Gate-Drain Charge	Q <sub>gd</sub>			2.1			
Switching			•		•		
Turn-On Delay Time	t <sub>d(on)</sub>			0.53	0.8		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		1.4	2.2		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ 1.0 A, $\text{V}_\text{GEN}$ = 4.5 V, $\text{R}_\text{g}$ = 6 $\Omega$		13.5	20	μs	
Fall Time	t <sub>f</sub>			5.9	9		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, dI/dt = 100 A/μs		13	25	ns	

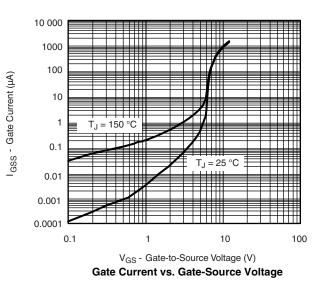
Notes:

a. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

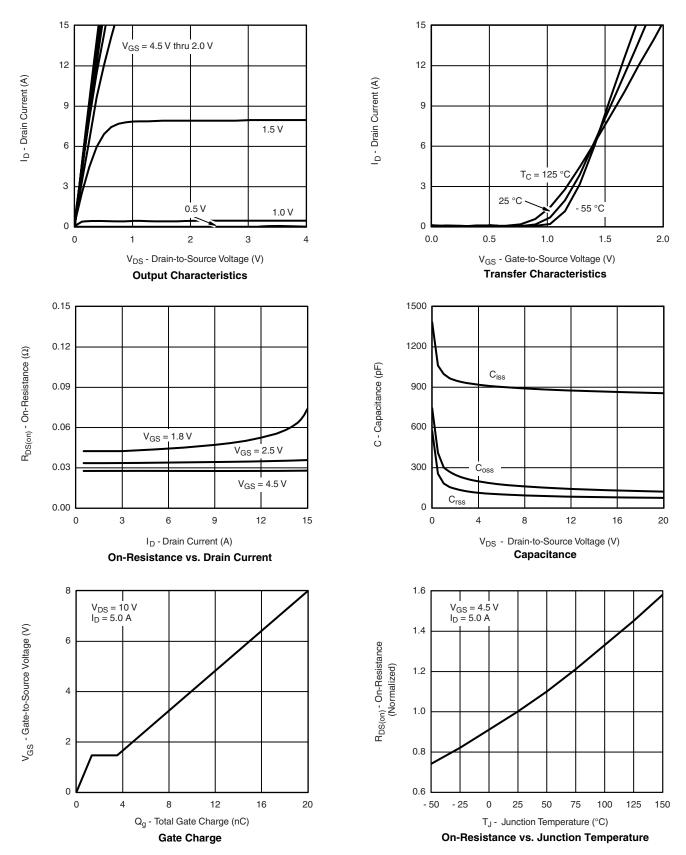






## Si2314EDS Vishay Siliconix

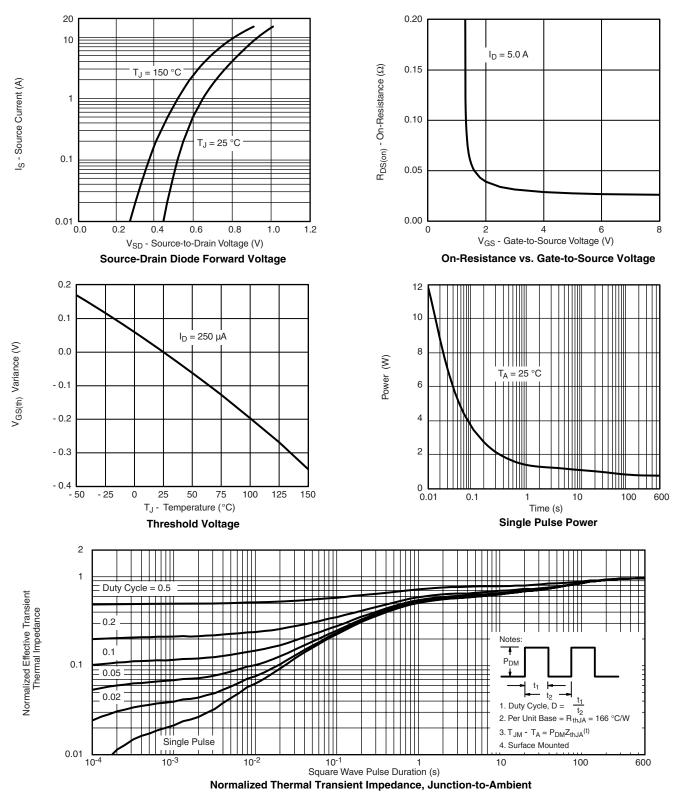
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



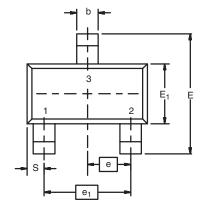
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71611">www.vishay.com/ppg?71611</a>.



# Package Information

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## SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020	) Ref	
q	3°	8°	3°	8°	



# Application Note 826

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## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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