



## General Features

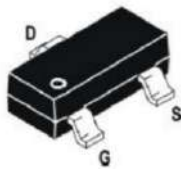
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package
- Available in SOT23 Package

## Product Summary

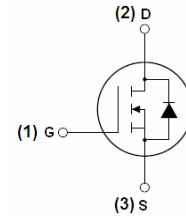
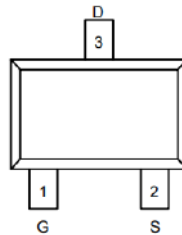
V <sub>DS</sub>	30	V
R <sub>DS(on)</sub> , Typ. @ V <sub>GS</sub> =10 V	29	mΩ
I <sub>D</sub>	5.8	A

## Applications

- PWM applications
- Load switch
- Power management



SOT23 Top View



N-channel

### Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current	I <sub>D</sub>	T <sub>A</sub> =25°C	A
		T <sub>A</sub> =70°C	
Pulsed Drain Current <sup>C</sup>	I <sub>DM</sub>	30	
Power Dissipation <sup>B</sup>	P <sub>D</sub>	T <sub>A</sub> =25°C	W
		T <sub>A</sub> =70°C	
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

### Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>A</sup>	R <sub>θJA</sub>	70	90	°C/W
Maximum Junction-to-Ambient <sup>A,D</sup>		100	125	
Maximum Junction-to-Lead	R <sub>θJL</sub>	63	80	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}$ , $V_{GS}=0\text{V}$	30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24\text{V}$ , $V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1 5	$\mu\text{A}$
$I_{GSS}$	Gate-Body leakage current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 12\text{V}$			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	0.7	1.1	1.4	V
$I_{D(ON)}$	On state drain current	$V_{GS}=4.5\text{V}$ , $V_{DS}=5\text{V}$	30			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=5.8\text{A}$ $T_J=125^\circ\text{C}$		29	32 39	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=5\text{A}$		32	36	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=4\text{A}$		40	52	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=5\text{V}$ , $I_D=5\text{A}$		15		S
$V_{SD}$	Diode Forward Voltage	$I_S=1\text{A}$ , $V_{GS}=0\text{V}$		0.77	1	V
$I_S$	Maximum Body-Diode Continuous Current				2	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0\text{V}$ , $V_{DS}=15\text{V}$ , $f=1\text{MHz}$		823		pF
$C_{oss}$	Output Capacitance			99		pF
$C_{riss}$	Reverse Transfer Capacitance			77		pF
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $f=1\text{MHz}$	2	4	6	$\Omega$
<b>SWITCHING PARAMETERS</b>						
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=10\text{V}$ , $V_{DS}=15\text{V}$ , $R_L=2.6\Omega$ , $R_{GEN}=3\Omega$		3.3		ns
$t_r$	Turn-On Rise Time			4.8		ns
$t_{D(off)}$	Turn-Off DelayTime			26.3		ns
$t_f$	Turn-Off Fall Time			4.1		ns

A. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.

B. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=150^\circ\text{C}$ , using  $\leq 10\text{s}$  junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^\circ\text{C}$ .

D. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to lead  $R_{\theta JL}$  and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using  $<300\mu\text{s}$  pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, assuming a maximum junction temperature of  $T_{J(MAX)}=150^\circ\text{C}$ . The SOA curve provides a single pulse rating.



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

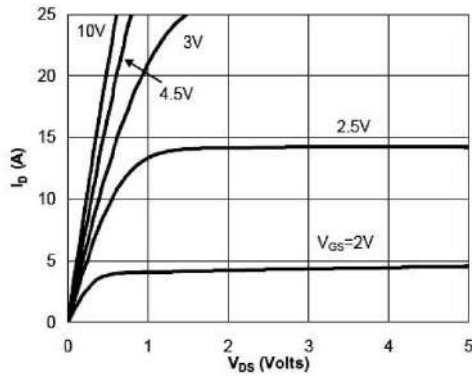


Fig 1: On-Region Characteristics (Note E)

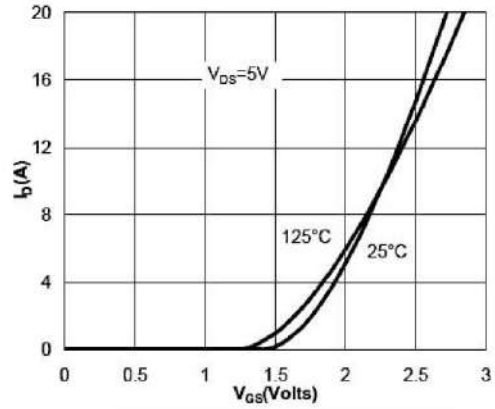


Figure 2: Transfer Characteristics (Note E)

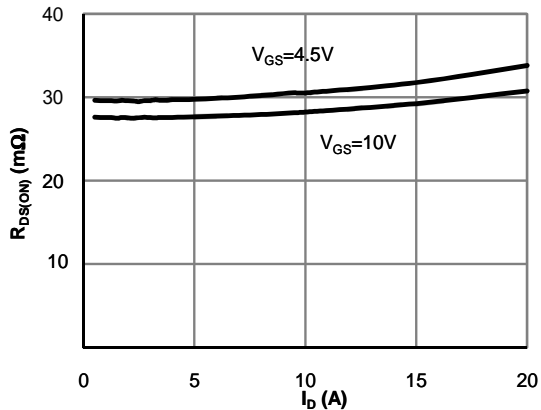


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

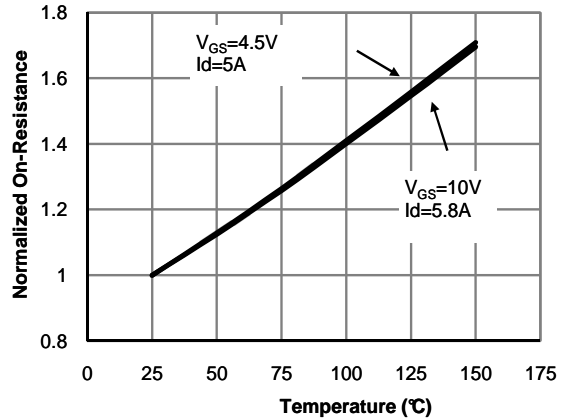


Figure 4: On-Resistance vs. Junction Temperature (Note E)

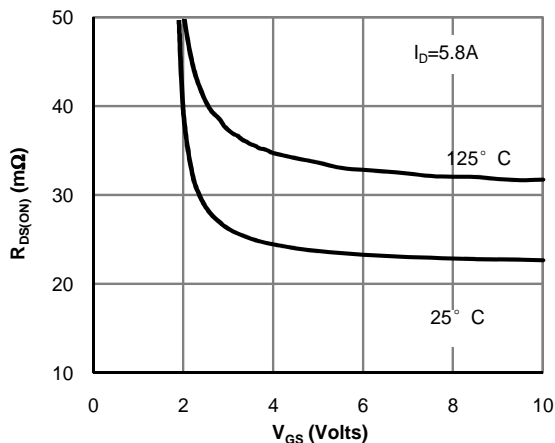


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

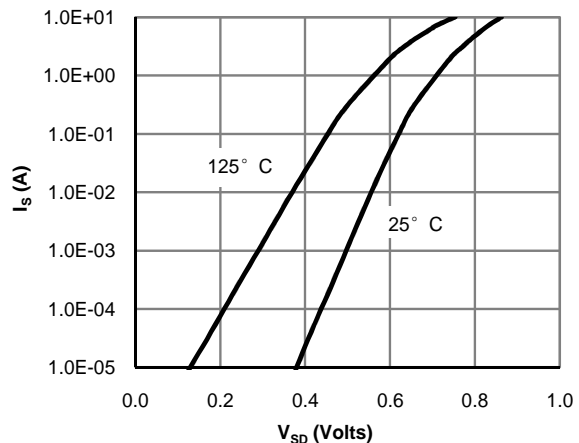


Figure 6: Body-Diode Characteristics (Note E)



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

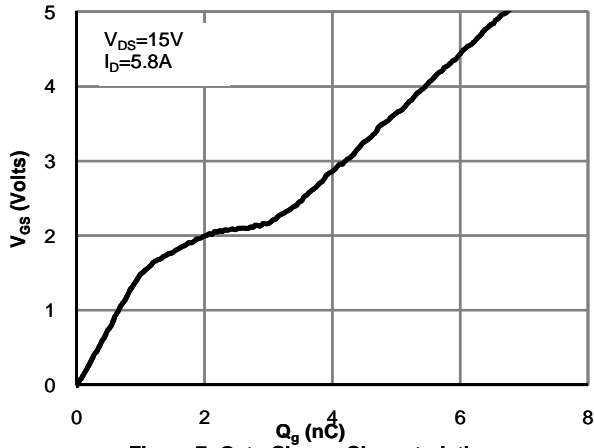


Figure 7: Gate-Charge Characteristics

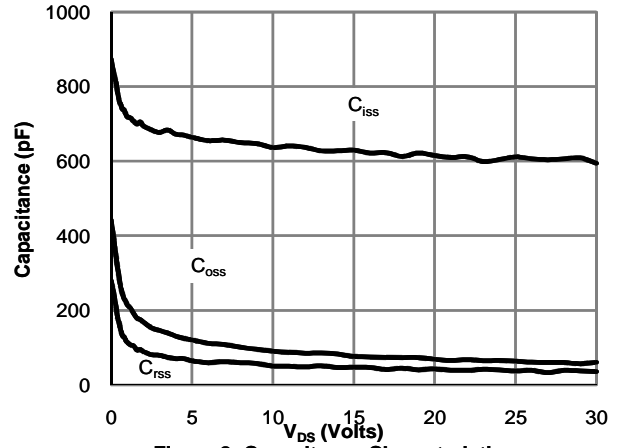


Figure 8: Capacitance Characteristics

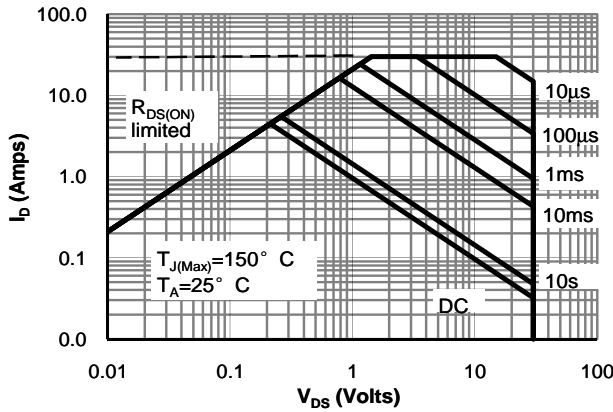


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

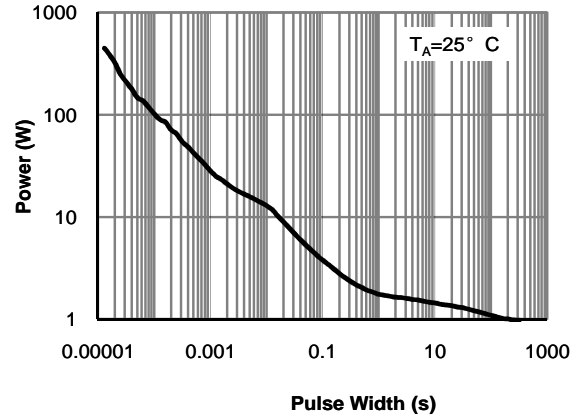


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

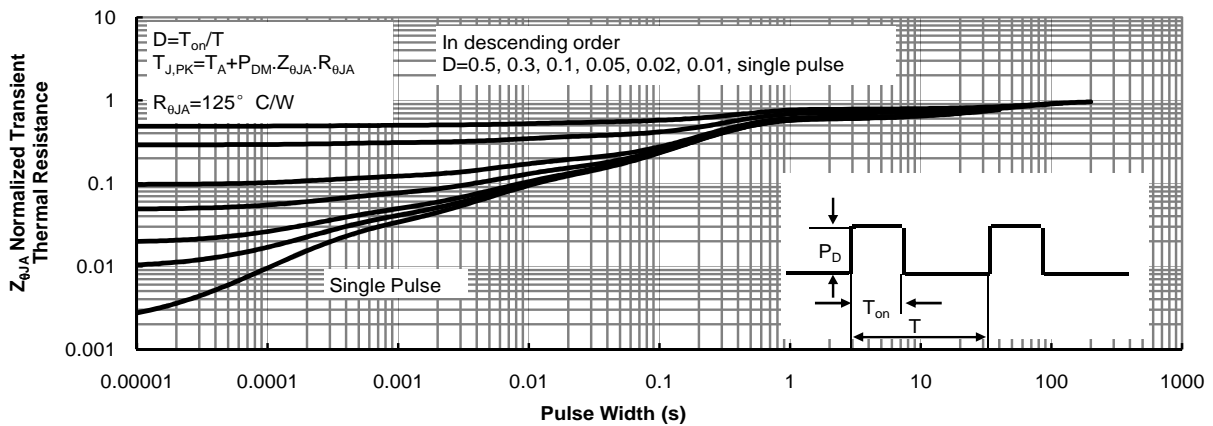
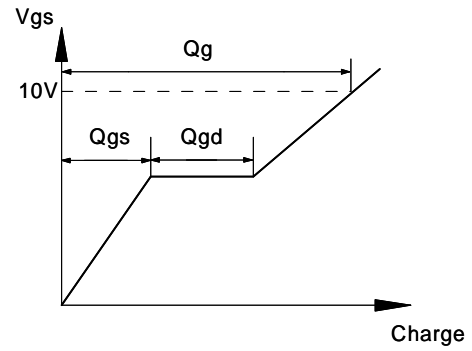
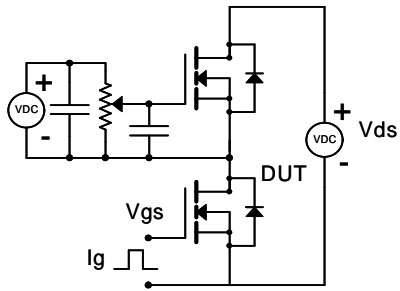
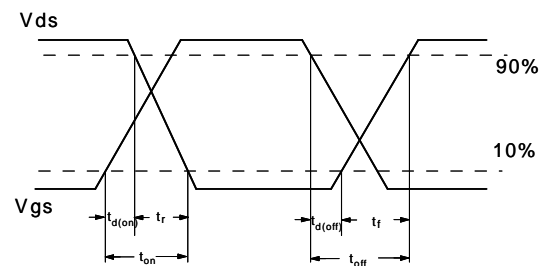
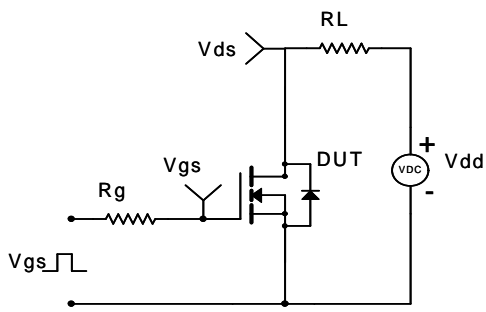


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

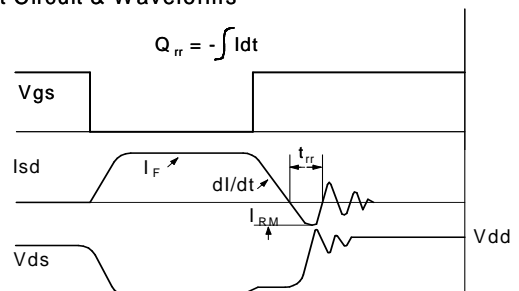
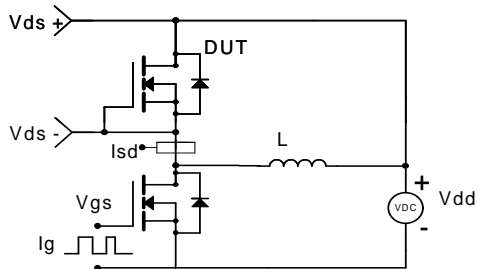
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



### Diode Recovery Test Circuit & Waveforms

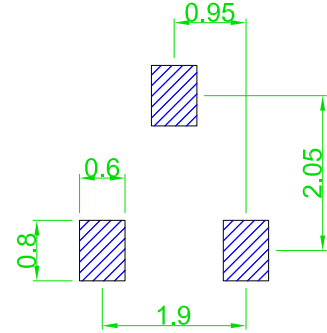
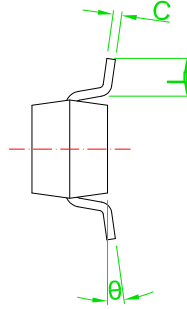
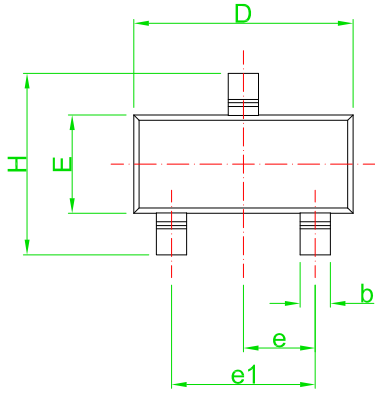


## Ordering and Marking Information

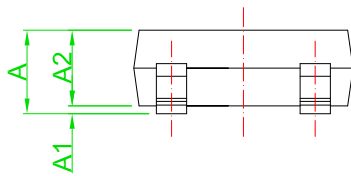
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM3400ZA-R	3400	SOT23	Tape&Reel	3000

PACKAGE	MARKING
SOT23	<div style="border: 1px solid black; width: 60px; height: 40px; margin: 0 auto; display: flex; align-items: center; justify-content: center;"> <b>3400</b> </div>

## SOT-23 PACKAGE INFORMATION

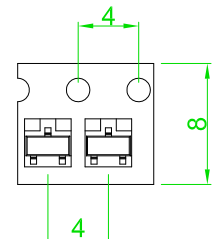


Recommended Land Pattern



## SOT23

Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	0.90	1.15	0.035	0.045
A1	0.00	0.10	0.000	0.004
A2	0.90	1.05	0.035	0.041
b	0.30	0.55	0.012	0.022
C	0.08	0.15	0.003	0.006
D	2.80	3.00	0.110	0.118
E	1.20	1.40	0.047	0.055
e	0.95 TYP		0.037 TYP	
e1	1.80	2.00	0.071	0.079
H	2.25	2.55	0.089	0.100
L	0.30	0.50	0.012	0.020
$\theta$	0°	8°	0°	8°



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