NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2030 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

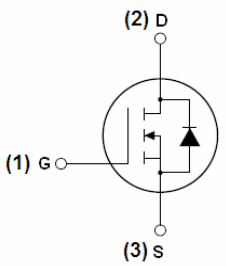
- V_{DS} =20V,I_D =30A
  $R_{DS(ON)}$ <12mΩ @ V_{GS}=10V (Typ:10.5mΩ)
  $R_{DS(ON)}$ <13mΩ @ V_{GS}=4.5V (Typ:11mΩ)
  $R_{DS(ON)}$ <18mΩ @ V_{GS}=2.5V (Typ:14mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Power switching application
- Load switching
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram

Marking and pin assignment

TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE2030	NCE2030	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±12	V
Drain Current-Continuous	I_D	30	A
Drain Current-Continuous(T_C=100℃)	I_D (100℃)	21	A
Pulsed Drain Current	I_{DM}	100	A
Maximum Power Dissipation	P_D	40	W
Single pulse avalanche energy (Note 5)	E_{AS}	150	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 175	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case (Note 2)	$R_{\theta JC}$	3.8	℃/W

Electrical Characteristics (T_A=25℃unless otherwise noted)

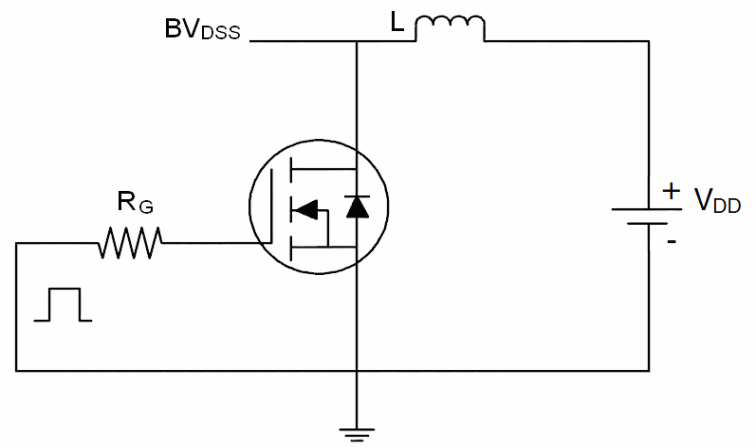
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=20V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±12V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	0.5	0.7	1.2	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=20A	-	10.5	12	mΩ
		V_{GS}=4.5V, I_D=20A	-	11	13	mΩ
		V_{GS}=2.5V, I_D=20A	-	14	18	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=20A	10	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=10V,V_{GS}=0V, F=1.0MHz		1544		PF
Output Capacitance	C_{oss}			210.1		PF
Reverse Transfer Capacitance	C_{rss}			201.4		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{GS}=10V,V_{DS}=10V R_L=0. 5Ω,R_{GEN}=3Ω	-	4.5	-	nS
Turn-on Rise Time	t_r		-	9.2	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	18.7	-	nS
Turn-Off Fall Time	t_f		-	3.3	-	nS
Total Gate Charge	Q_g	V_{GS}=4.5V,V_{DS}=10V,I_D=20A		23.5		nC
Gate-Source Charge	Q_{gs}			2.8		nC
Gate-Drain Charge	Q_{gd}			5.75		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=20A	-	-	1.2	V
Diode Forward Current (Note 2)	I_S	-	-	-	30	A
Reverse Recovery Time	t_{rr}	T_J = 25°C, I_F = 20A di/dt = 100A/μs (Note3)	-	18	-	nS
Reverse Recovery Charge	Qrr		-	9.5	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

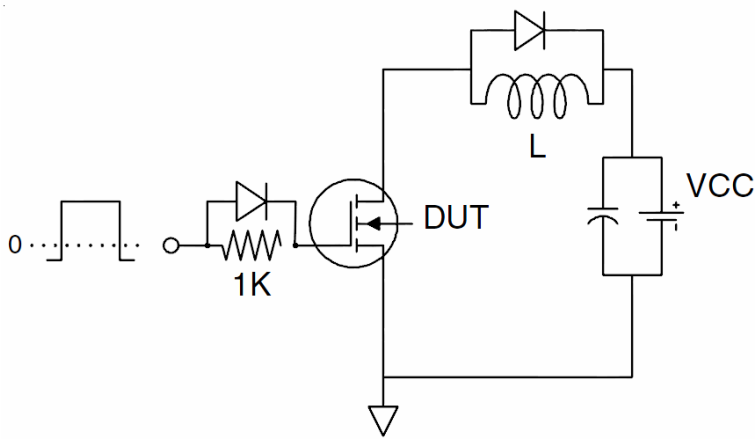
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition：Tj=25℃,V_{DD}=10V,V_G=10V,L=0.5mH,Rg=25Ω

Test circuit

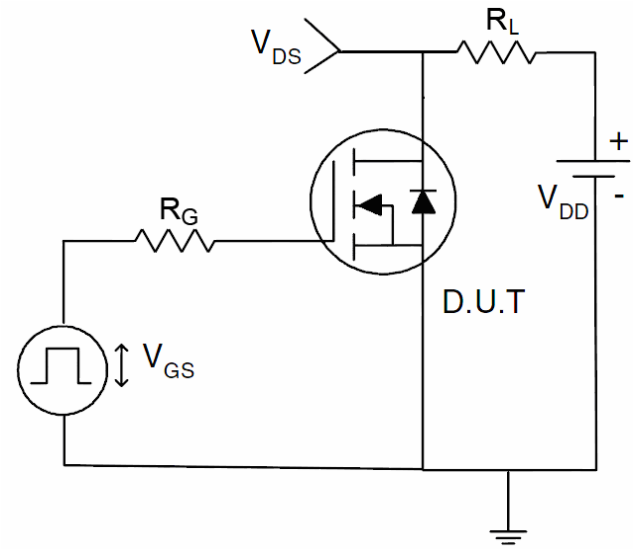
1）E_{AS} test Circuits



2）Gate charge test Circuit:



3）Switch Time Test Circuit：



Typical Electrical and Thermal Characteristics (Curves)

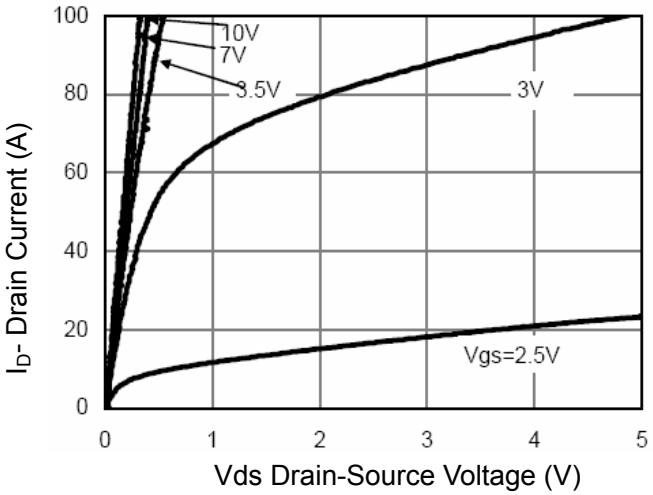


Figure 1 Output Characteristics

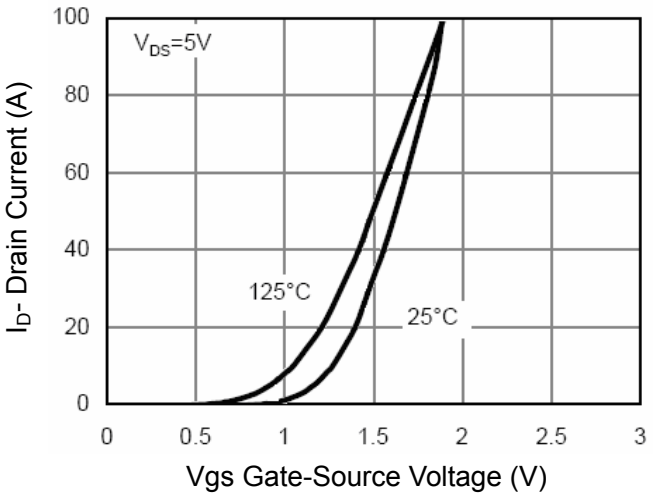


Figure 2 Transfer Characteristics

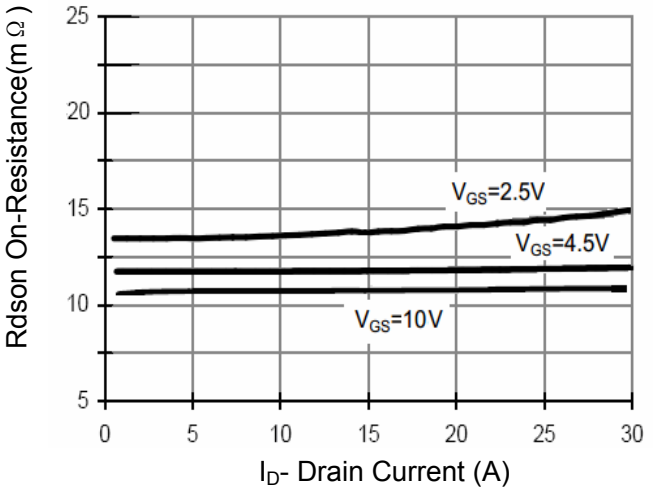


Figure 3 Rdson- Drain Current

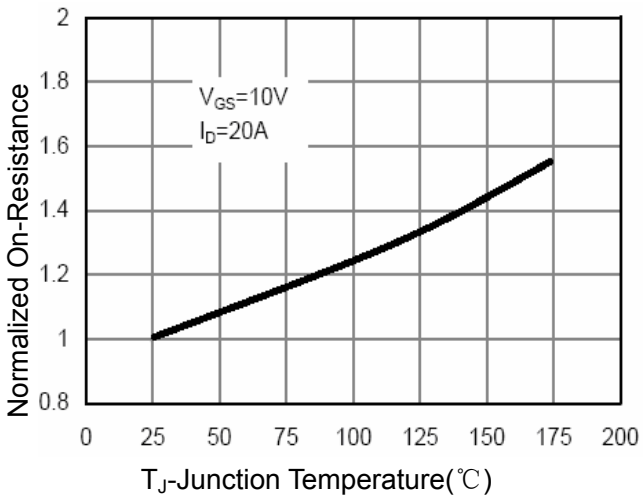


Figure 4 Rdson-Junction Temperature

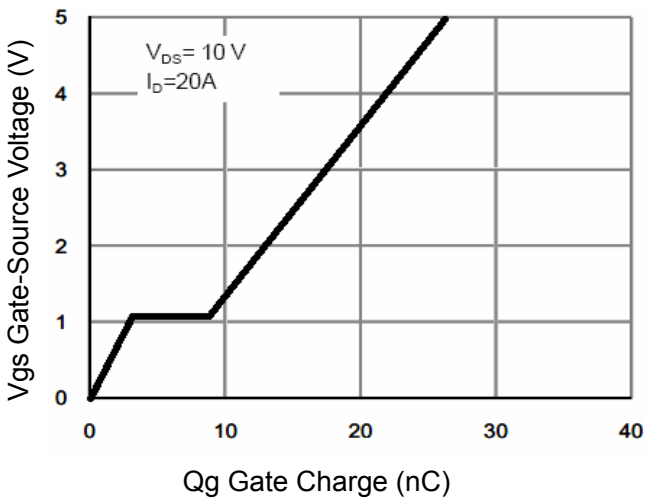


Figure 5 Gate Charge

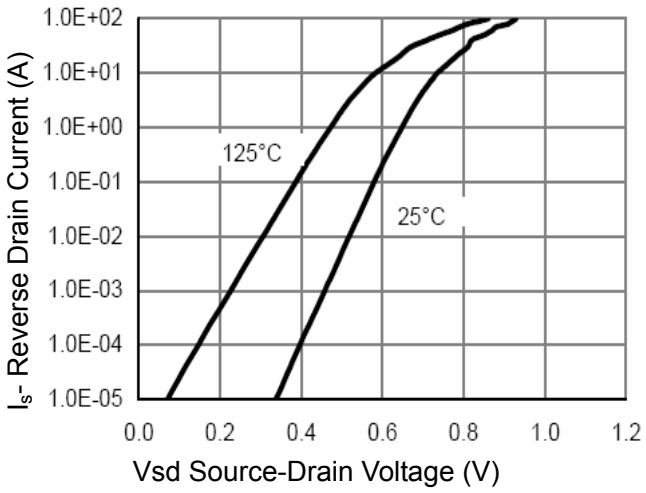


Figure 6 Source- Drain Diode Forward

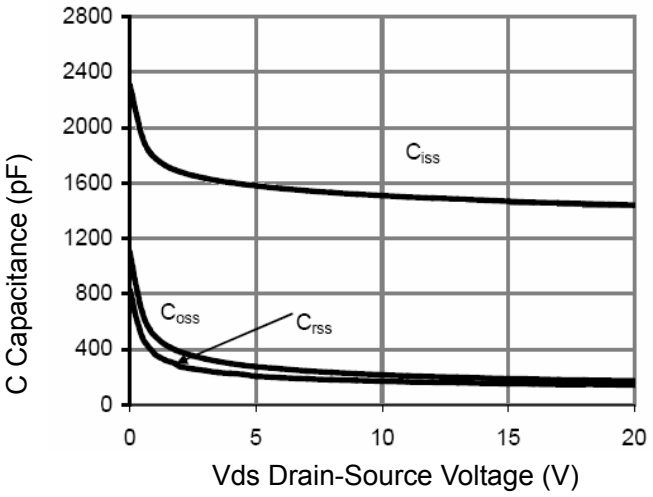


Figure 7 Capacitance vs Vds

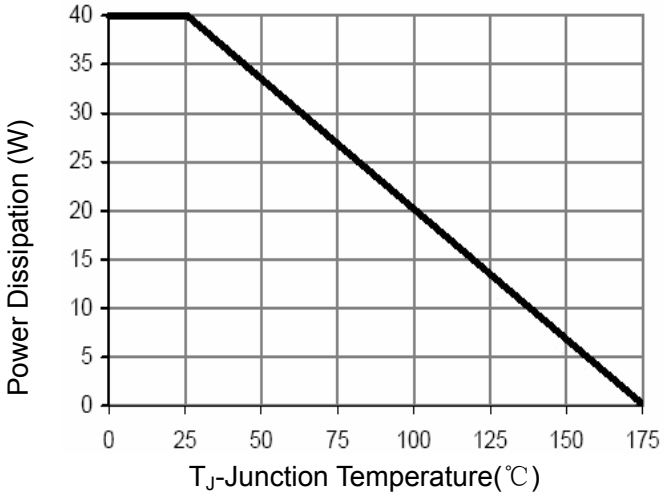


Figure 9 Power De-rating

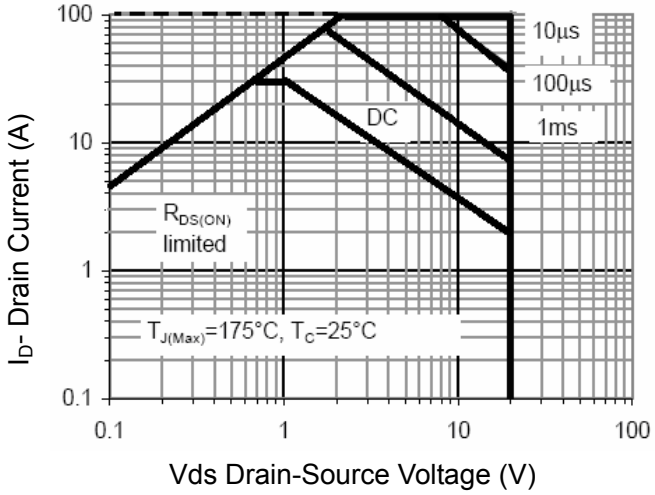


Figure 8 Safe Operation Area

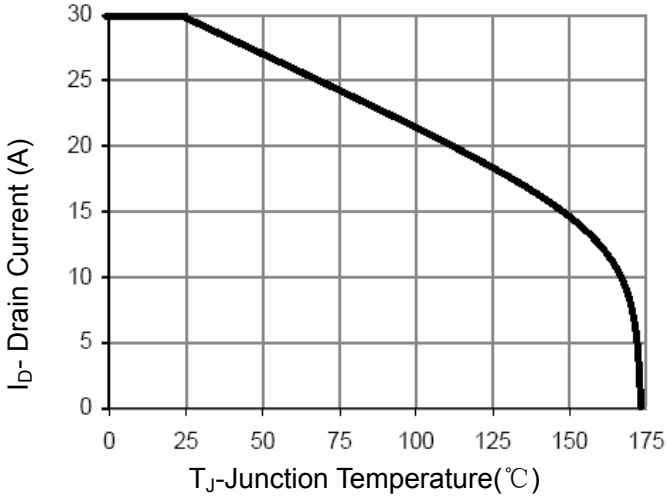


Figure 10 Current De-rating

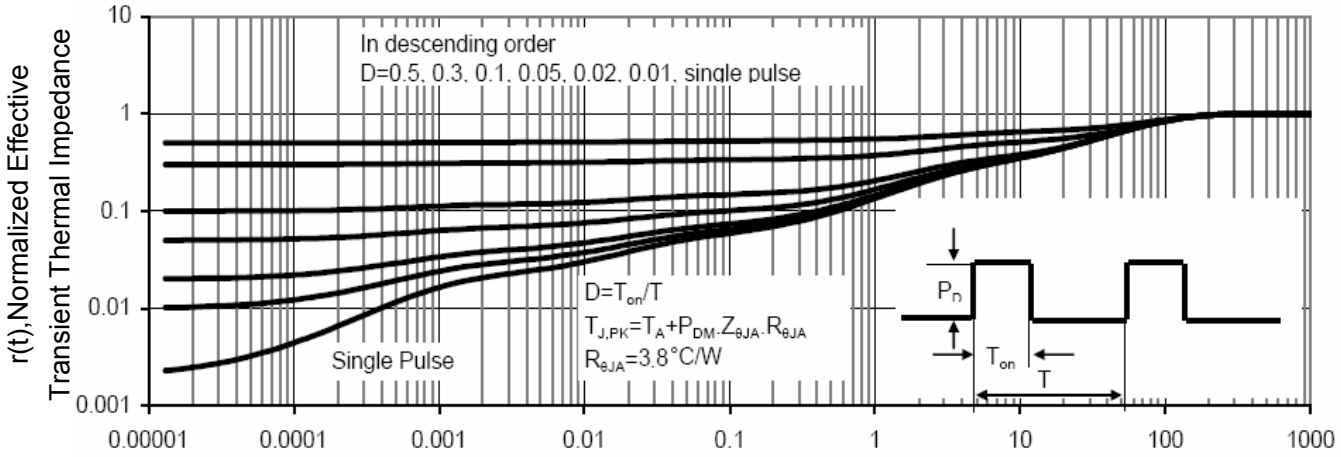
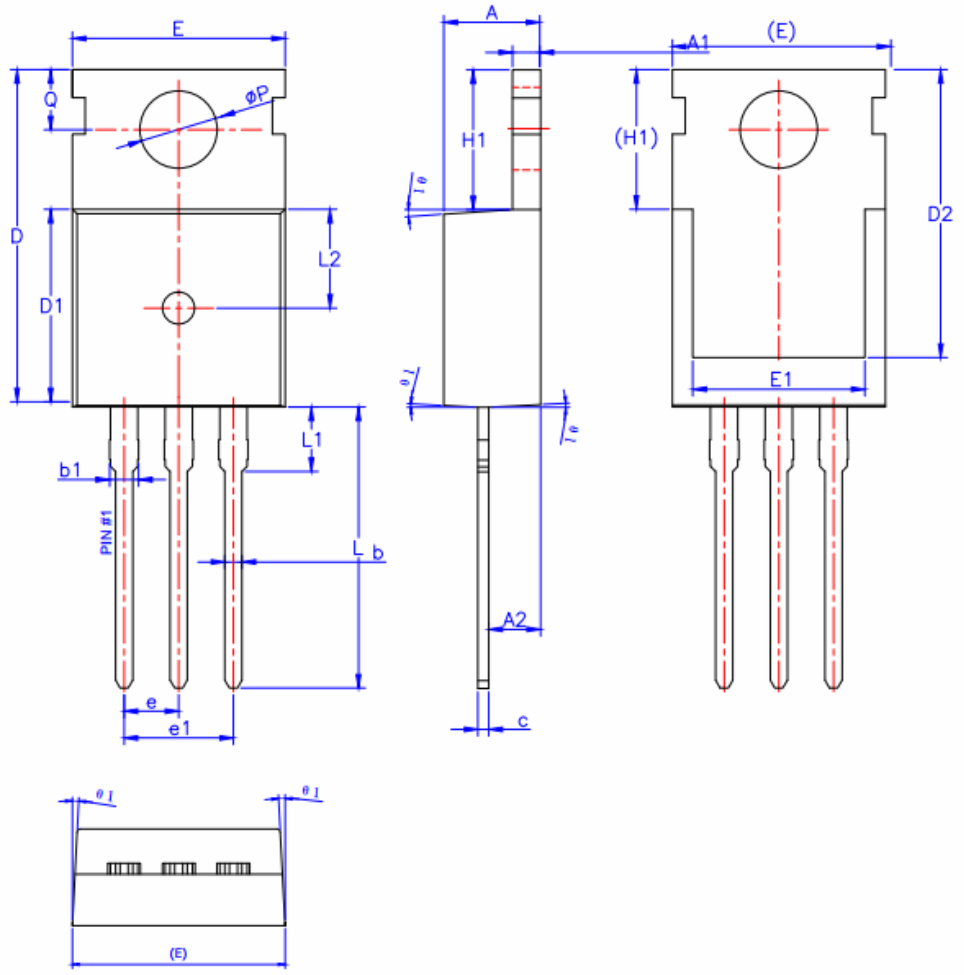


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220-3L Package Information



SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	–	0.90
b1	–	–	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	–	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	–	–	3.50
L2	4.60REF		
ØP	3.55	3.60	3.65
Q	2.73	–	2.87
θ1	1°	3°	5°

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