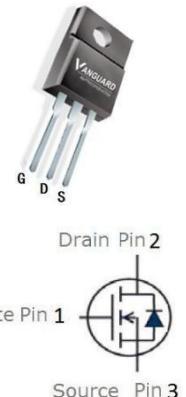


Features

- Extremely low gate charge
- 100% avalanche tested
- Super Junction Technology
- Pb-free lead plating; RoHS compliant; Halogen free

V_{DS}	700	V
$R_{DS(on),TYP}$ @ $V_{GS}=10V$	520	$m\Omega$
I_D	8	A

TO-220F

RoHS

Halogen-Free

Part ID	Package Type	Marking	Tube Information
VSF600N70HS	TO-220F	600N70H	50pcs/Tube

Maximum ratings, at $T_A=25^\circ C$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	700	V
V_{GS}	Gate-Source voltage	± 30	V
I_S	Diode continuous forward current	$T_c=25^\circ C$	A
I_D	Continuous drain current @ $V_{GS}=10V$	$T_c=25^\circ C$	A
		$T_c=100^\circ C$	A
I_{DM}	Pulse drain current tested ①	$T_c=25^\circ C$	A
EAS	Avalanche energy, single pulsed ②	175	mJ
P_D	Maximum power dissipation	$T_c=25^\circ C$	W
T_{STG}, T_J	Storage and Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\alpha C}$	Thermal Resistance, Junction-to-Case	5	°C/W
$R_{\alpha A}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	700	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=700\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=560\text{V}, V_{\text{GS}}=0\text{V}$	--	--	50	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 30\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	3.0	--	4.6	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ③	$V_{\text{GS}}=10\text{V}, I_D=4.0\text{A}$	--	520	600	$\text{m}\Omega$
		$T_j=100^\circ\text{C}$	--	830	--	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	500	585	670	pF
C_{oss}	Output Capacitance		235	275	315	pF
C_{rss}	Reverse Transfer Capacitance		--	12	20	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=350\text{V}, I_D=7\text{A}, V_{\text{GS}}=10\text{V}$	--	13	--	nC
Q_{gs}	Gate-Source Charge		--	4	--	nC
Q_{gd}	Gate-Drain Charge		--	4.4	--	nC

Switching Characteristics

$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=350\text{V}, I_D=3.5\text{A}, R_G=10\Omega, V_{\text{GS}}=10\text{V}$	--	39	--	ns
t_r	Turn-on Rise Time		--	22	--	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	109	--	ns
t_f	Turn-Off Fall Time		--	29	--	ns

Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

V_{SD}	Forward on voltage	$I_{\text{SD}}=8\text{A}, V_{\text{GS}}=0\text{V}$	--	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{sd}}=3.5\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	--	190	--	ns
Q_{rr}	Reverse Recovery Charge		--	1.4	--	μC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

② Limited by $T_{j\text{max}}$, starting $T_j = 25^\circ\text{C}$, $L = 14\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 5\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value.

③ Pulse width $\leq 380\mu\text{s}$; duty cycles $\leq 2\%$.



Vanguard
Semiconductor

VSF600N70HS

700V/8A N-Channel Advanced Power MOSFET

Typical Characteristics

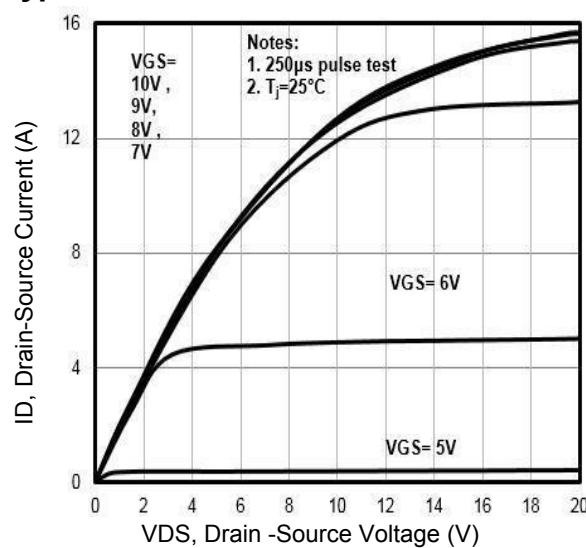


Fig1. Typical Output Characteristics

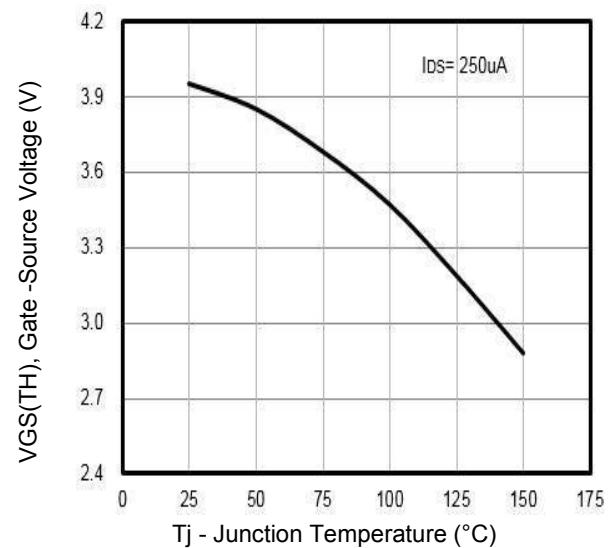


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

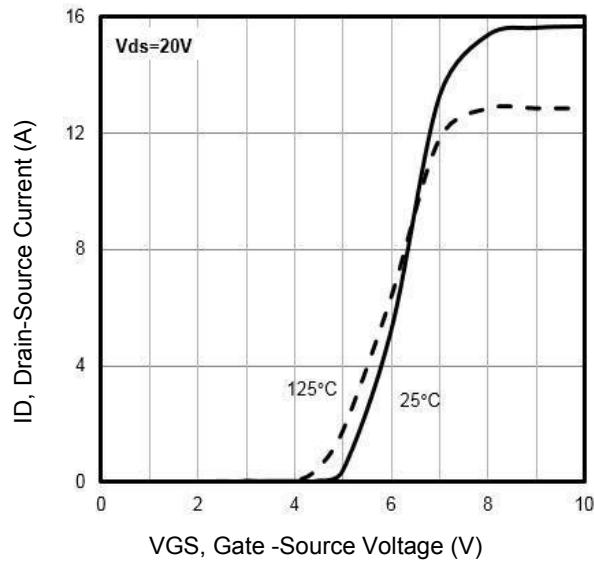


Fig3. Typical Transfer Characteristics

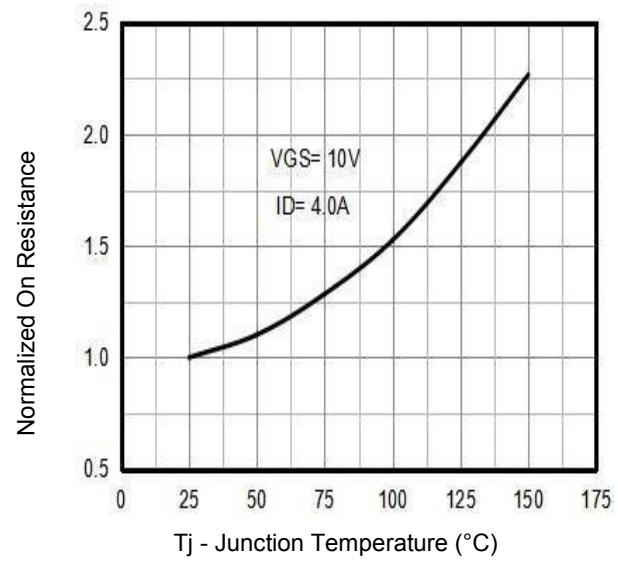


Fig4. Normalized On-Resistance Vs. Temperature

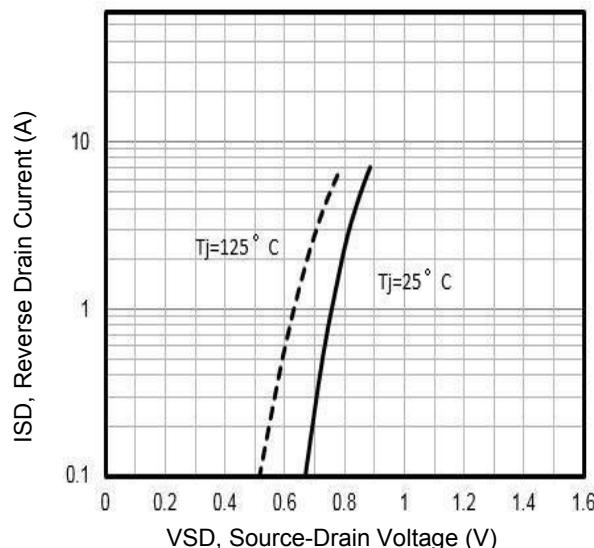


Fig5. Typical Source-Drain Diode Forward Voltage

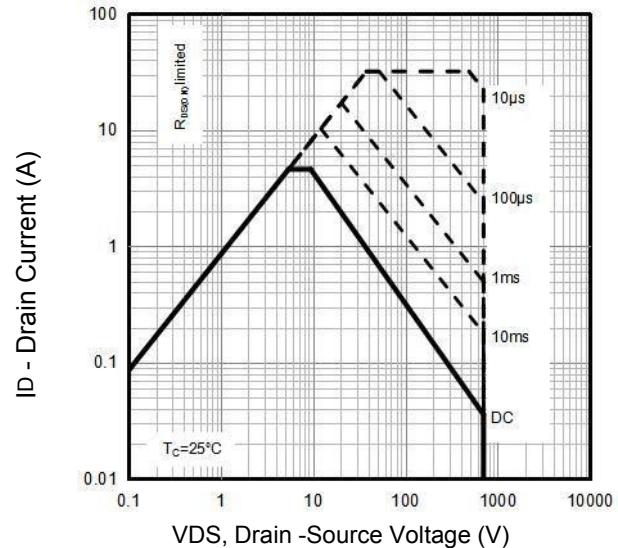


Fig6. Maximum Safe Operating Area



Typical Characteristics

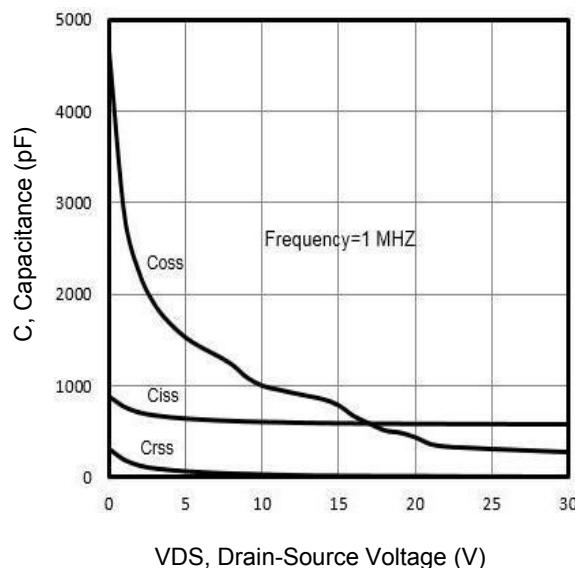


Fig7. Typical Capacitance Vs. Drain-Source Voltage

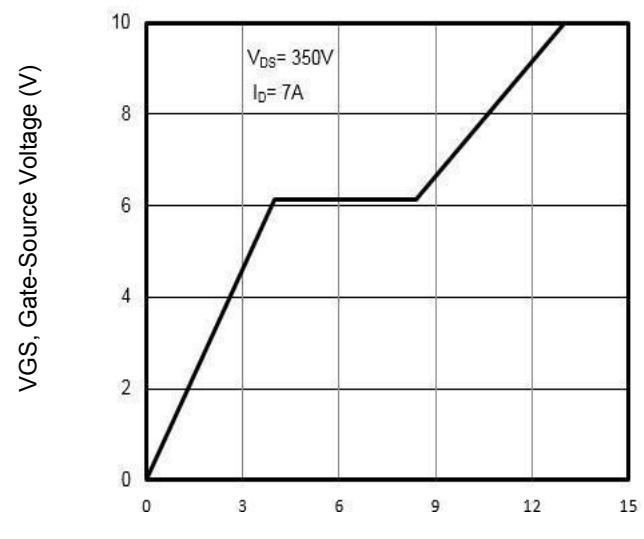


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

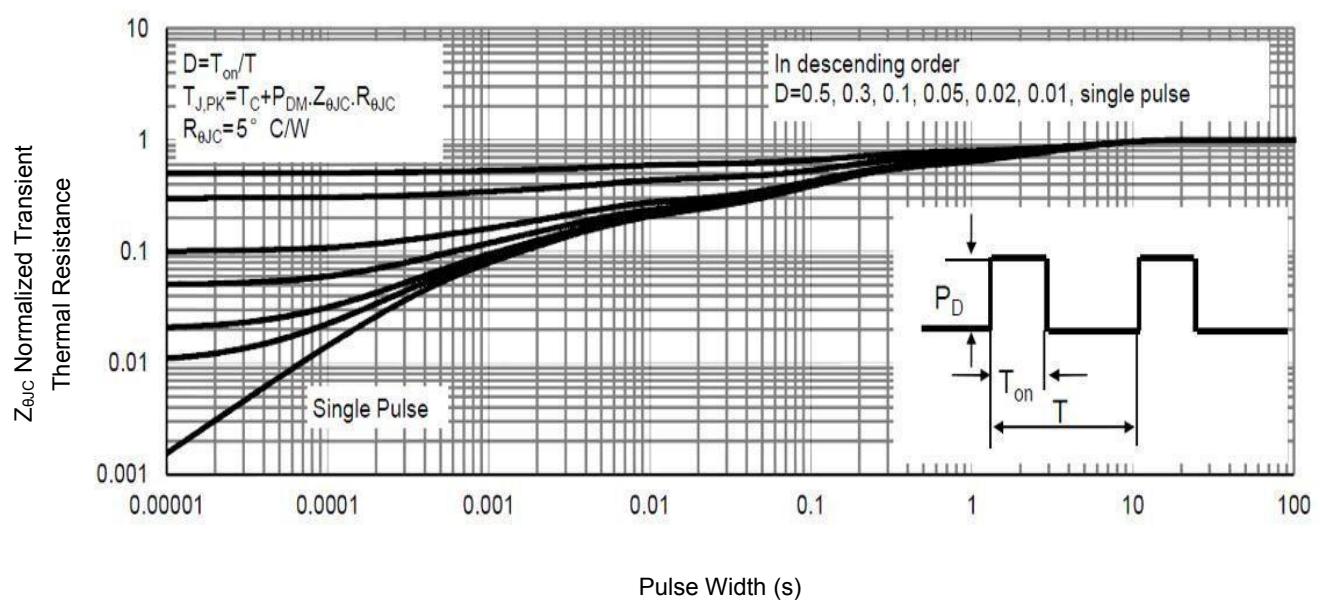


Fig9. Normalized Maximum Transient Thermal Impedance

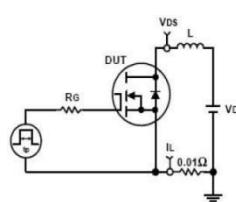


Fig10. Unclamped Inductive Test Circuit and waveforms

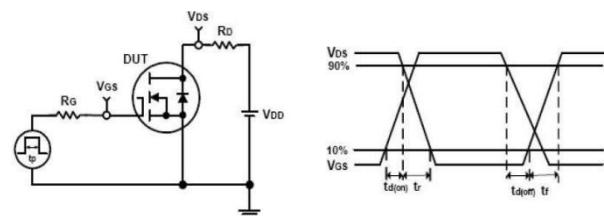
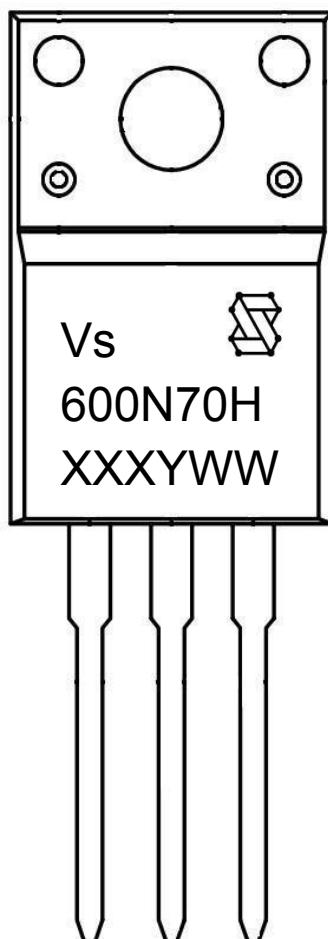


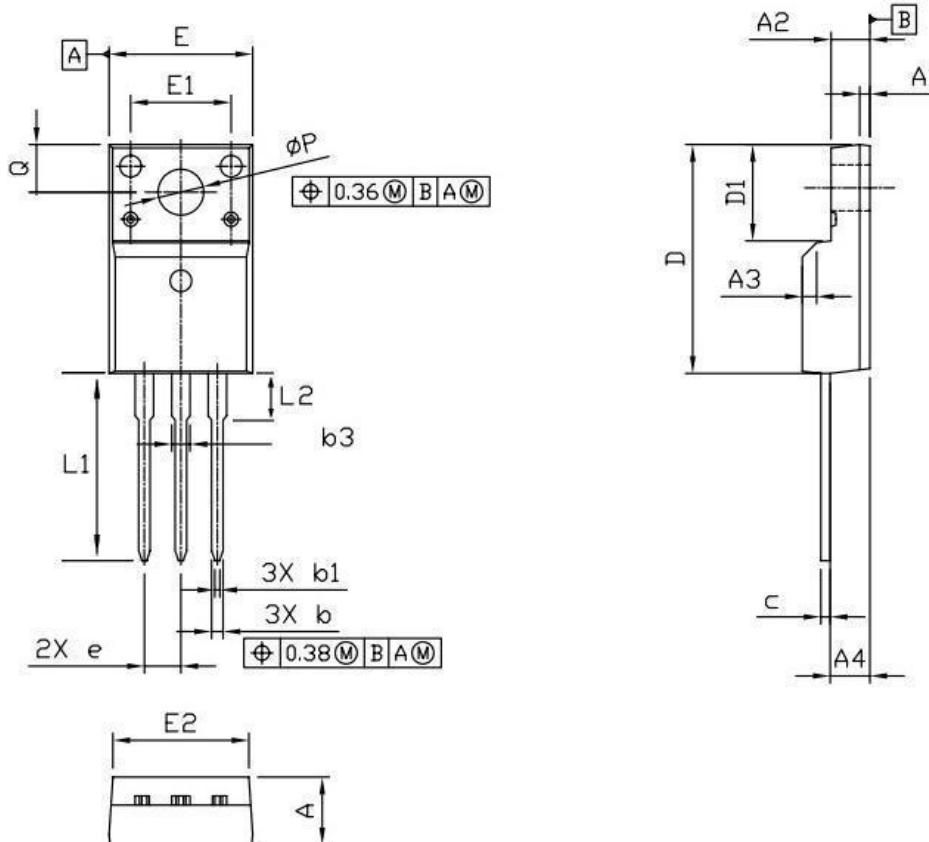
Fig11. Switching Time Test Circuit and waveforms

Marking Information



- 1st line: Vanguard Code (Vs), Vanguard Logo
2nd line: Part Number (600N70H)
3rd line: Date code (XXXYWW)
XXX: Wafer Lot Number Code , code changed with Lot Number
Y: Year Code, (e.g. E=2017, F=2018, G=2019, H=2020, etc)
WW: Week Code (01 to 53)

TO-220F Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	4.40	4.70	5.00
A1	0.45	0.70	0.95
A2	2.30	2.55	2.80
A3	1.0 x 45°		
A4	2.45	2.76	3.05
b	0.60	0.80	1.00
b1	0.25	0.35	0.45
b3	1.18	--	1.47
c	0.30	0.50	0.70
D	15.40	15.90	16.40
D1	6.40	6.70	7.00
e	--	2.54	--
E	9.86	10.16	10.46
E1	6.80	7.00	7.20
E2	9.40	9.70	10.00
L1	12.30	12.80	13.30
L2	2.95	3.25	3.55
Q	3.05	3.30	3.55
φP	2.92	3.12	3.32

Customer Service

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