

# **68V N-Channel Trench MOSFET**

## **FEATURES**

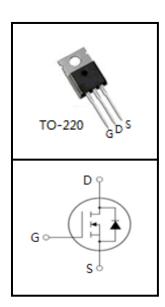
- Trench Power MOSFET Technology
- Low Rds(on)
- Low Gate Charge
- Optimized For Fast-switching Applications

## **APPLICATIONS**

- DC/DC Converters
- Synchronous Rectification

Device Marking and Package Information				
Device	Package	Marking		
CTP06N6P8	TO-220	CTP06N6P8		





<b>Absolute Maximum Ratings</b> at $T_j = 25^{\circ}$ C unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	68	V
Continuous Drain Current T <sub>C</sub> = 25°C	(note2)		115	А
Continuous Drain Current T <sub>C</sub> = 100°C	(note2)	I <sub>D</sub>	85	А
Pulsed Drain Current	(note1)	I <sub>DM</sub>	460	А
Gate Source Voltage		V <sub>GSS</sub>	±20	V
Single Pulse Avalanche Energy	(note1)	E <sub>AS</sub>	487	mJ
Avalanche Current		I <sub>AS</sub>	57	А
Power Dissipation T <sub>C</sub> = 25°C	(note3)		158	W
Power Dissipation T <sub>C</sub> = 100°C	(note3)	$P_{D}$	79	W
Operating Junction and Storage Temperature Range		$T_J,T_stg$	-55~+175	°C

Thermal Characteristics				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.95	•C/W	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	64	] *C/VV	



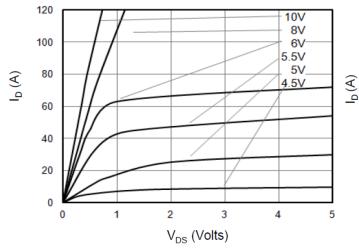
Electrical Characteristics T <sub>j</sub> = 25°C unless otherwise specified						
Doromotor	0	rmbol Test Conditions	Value			
Parameter	Symbol		Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	68	-		V
Zero Gate Voltage Drain Current		$V_{DS} = 68V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	uA
Zero Gate Voltage Brain Garrent	I <sub>DSS</sub>	$V_{DS} = 68V, V_{GS} = 0V, T_{J} = 100^{\circ}C$			25	uA
Gate-Source Leakage	$I_{\rm GSS}$	$V_{GS} = \pm 20V$		-	±100	nA
Gate-Source Threshold Voltage	$V_{\text{GS(th)}}$	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2	3	4	V
Drain-Source On-Resistance (note2)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_{D} = 30A$		5.4	6.8	mΩ
Forward Transconductance	gfs	$V_{DS} = 5V, I_{D} = 20A$		30		S
Dynamic						
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0V$ ,		5094		pF
Output Capacitance	$C_{oss}$	$V_{DS} = 30V$ ,		332		
Reverse Transfer Capacitance	$C_{rss}$	f = 1.0MHz		282		
Total Gate Charge	$Q_g$			87		nC
Gate-Source Charge	$Q_gs$	$V_{DS} = 30V, I_{D} = 30A,$ $V_{GS} = 10V$		23		
Gate-Drain Charge	$Q_gd$	55		22		
Turn-on Delay Time	$t_{d(on)}$			23		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 30V, I_{D} = 30A,$ $R_{G} = 2.5\Omega, V_{GS} = 10V$		18		ns
Turn-off Delay Time	$t_{\text{d(off)}}$	$N_{\rm G} = 2.322, V_{\rm GS} = 10V$		67		
Turn-off Fall Time	t <sub>f</sub>			30		
Body Diode Characteristics						
Source-Drain Current(Body Diode)	I <sub>SD</sub>	-			115	•
Pulsed Source-Drain Current(Body Diode)	I <sub>SDM</sub>	T <sub>C</sub> = 25°C			460	А
Body Diode Voltage	$V_{SD}$	$T_J = 25^{\circ}C$ , $I_{SD} = 30A$ , $V_{GS} = 0V$			1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30A		33		ns
Reverse Recovery Charge	$Q_{rr}$	di <sub>F</sub> /dt = 100A/µs		122		nC

#### **Notes**

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 30A,  $V_{DD}$  = 50V,L=0.3mH,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 3. The power dissipation PD is based on TJ(MAX)=175  $^{\circ}$  C, using junction-to-case thermal resistance.



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted



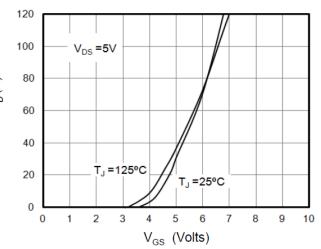
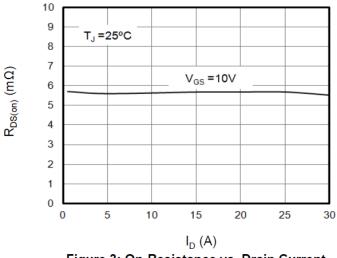


Figure 1: On-Region Characteristics

Figure 2: Transfer Characteristics



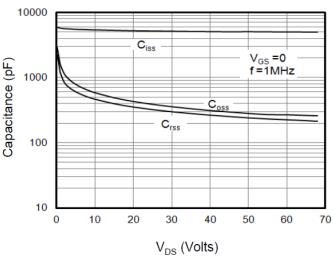
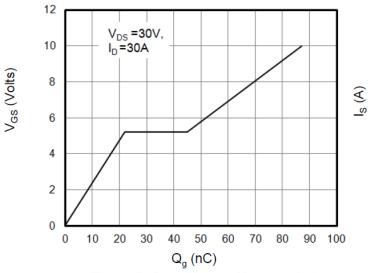


Figure 3: On-Resistance vs. Drain Current

Figure 4: Capacitance Characteristics





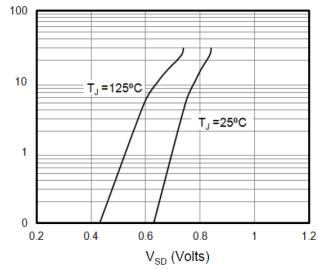
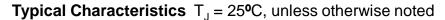
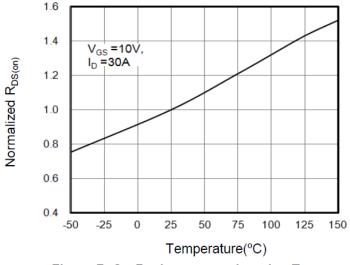


Figure 6: Body Diode Forward Voltage







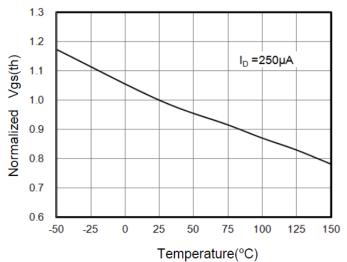
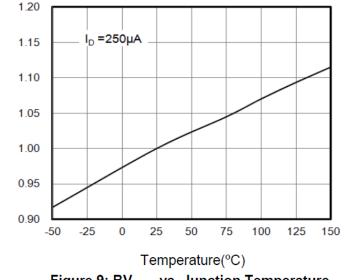


Figure 7: On-Resistance vs. Junction Temperature

Figure 8: Vgs(th) vs. Junction Temperature



Normalized BV<sub>DSS</sub>

 $Z_{\theta JC}$  Normalized Transient Thermal Resistance

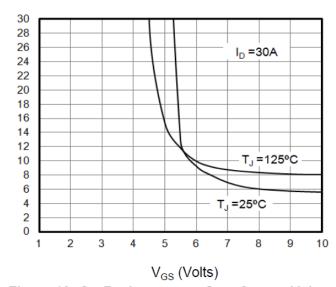
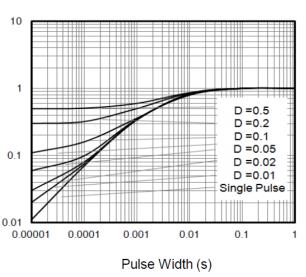


Figure 9: BV<sub>DSS</sub> vs. Junction Temperature

Figure 10: On-Resistance vs. Gate-Source Voltage



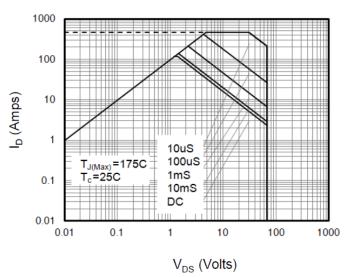


Figure 11: Normalized Transient Thermal Resistance

Figure 12: Safe Operating Area

 $R_{DS(on)}$  (m $\Omega$ )



Figure A: Gate Charge Test Circuit and Waveform

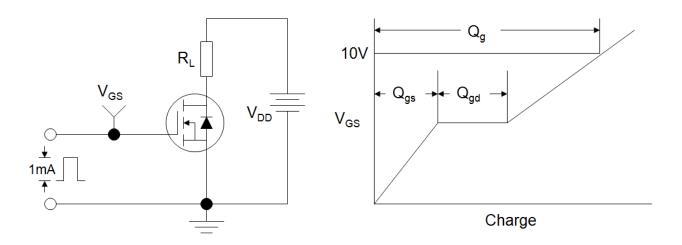


Figure B: Resistive Switching Test Circuit and Waveform

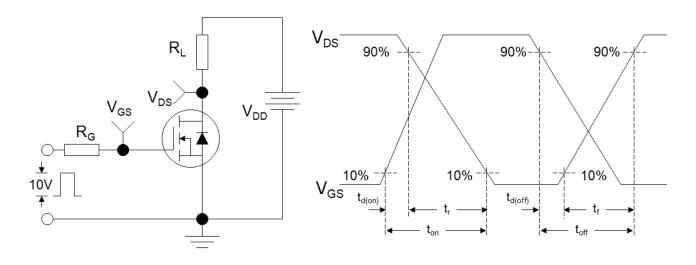
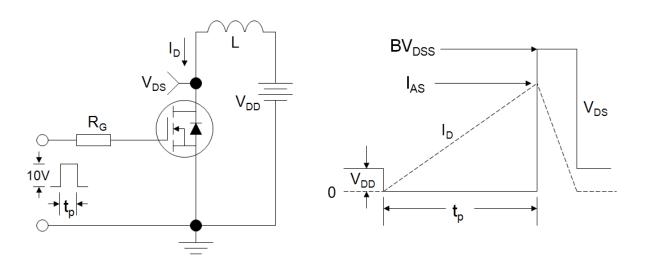
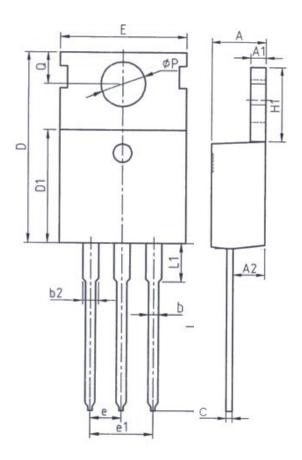


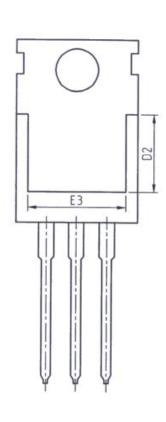
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





**TO-220** 





Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
ь	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5, 50	_		

Unit: mm				
Symbol	Min.	Max.		
E	9. 70	10. 30		
E3	7. 00	-		
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25	6. 85		
L	12. 75	13.80		
L1	-	3. 40		
Р	3. 40	3. 80		
Q	2. 60	3. 00		



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