

# **60V N-Channel Trench MOSFET**

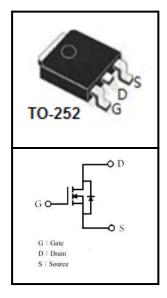
### **FEATURES**

- Super Low Gate Charge
- 100% EAS Guaranteed
- RoHS compliant
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Hard switched and high frequency circuits





Device Marking and Package Information			
Device	Package	Marking	
CTD06N030	TO-252	CTD06N030	

<b>Absolute Maximum Ratings</b> at $T_j$ = 25°C unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	60	V
Drain Current-Continuous(Tc=25°C)	(note1)	,	35	А
Drain Current-Continuous(Tc=100°C)	(note1)	I <sub>D</sub>	22	А
Pulsed Drain Current	(note2)	I <sub>DM</sub>	46	А
Gate Source Voltage		V <sub>GSS</sub>	±20	V
Power Dissipation $T_C = 25^{\circ}C$	(note4)	P <sub>D</sub>	41	W
Single Pulse Avalanche Energy	(note3)	E <sub>AS</sub>	26	mJ
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C

Thermal Characteristics					
Parameter		Symbol	Value	Unit	
Thermal Resistance Junction-Ambient	(note1)	R <sub>eJA</sub>	62	°C/W	
Thermal Resistance Junction-Case	(note1)	$R_{\theta JC}$	3.6	°C/W	



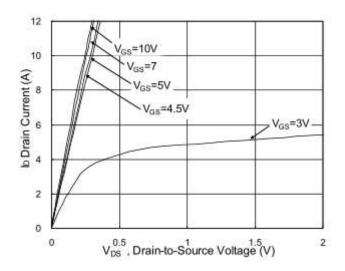
_			Value				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	60			>	
Zero Gate Voltage Drain Current		$V_{DS} = 48V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	uA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 48V, V_{GS} = 0V, T_{J} = 55^{\circ}C$			5		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20V$			±100	nA	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2		2.5	V	
Drain Course On Decistones (note2)		V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		25	30	mΩ	
Drain-Source On-Resistance (note2)	$R_{DS(on)}$	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		30	38	mΩ	
Dynamic							
Input Capacitance	$C_{iss}$	$V_{GS} = 0V$ ,		1378		pF	
Output Capacitance	$C_{oss}$	$V_{DS} = 15V,$		86			
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		64			
Total Gate Charge (4.5V)	$Q_g$			12.6			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 48V, I_{D} = 15A,$ $V_{GS} = 10V$		3.2		nC	
Gate-Drain Charge	$Q_{gd}$	65		6.3			
Turn-on Delay Time	t <sub>d(on)</sub>			8			
Turn-on Rise Time	t <sub>r</sub>	$V_{DS} = 30V, I_{D} = 15A$		14.2			
Turn-off Delay Time	$t_{d(off)}$	$V_{GS} = 10V, R_{G} = 3.3\Omega$		24.4		ns	
Turn-off Fall Time	t <sub>f</sub>			4.6			
Body Diode Characteristics							
Continuous Body Diode Current	Is	T 05.00			35	^	
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			46	A	
Body Diode Voltage	V <sub>SD</sub>	$I_{SD} = 5A$ , $V_{GS} = 0V$			1.2	V	

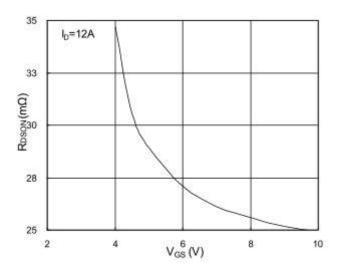
#### Notes

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq\!300\text{us}$  , duty cycle  $\!\leq\!2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.5mH
- 4. The power dissipation is limited by 175°C junction temperature
- 5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted





**Fig.1 Typical Output Characteristics** 

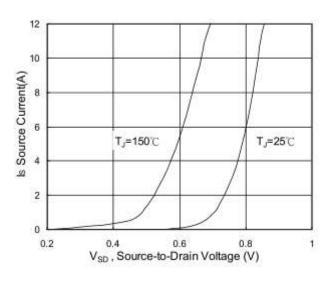


Fig.2 On-Resistance vs. G-S Voltage

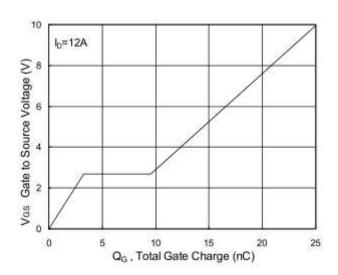


Fig.3 Forward Characteristics of Reverse Diode

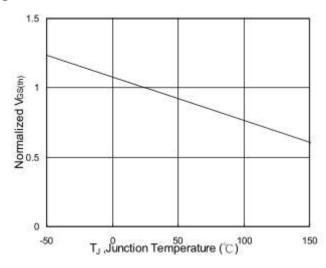


Fig.5 Normalized VGS(th) vs. TJ

Fig.4 Gate-Charge Characteristics

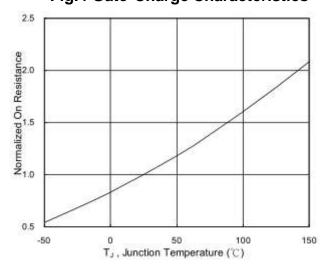
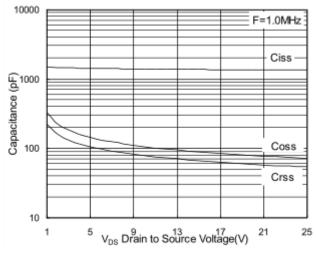


Fig.6 Normalized RDSON vs. TJ



## **Typical Characteristics** $T_J = 25$ °C, unless otherwise noted



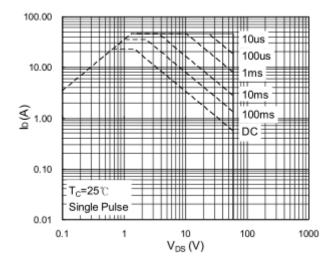


Fig.7 Capacitance

Fig.8 Safe Operating Area

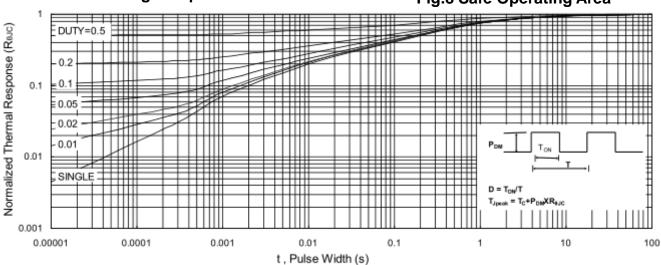


Fig.9 Normalized Maximum Transient Thermal Impedance



Figure A: Gate Charge Test Circuit and Waveform

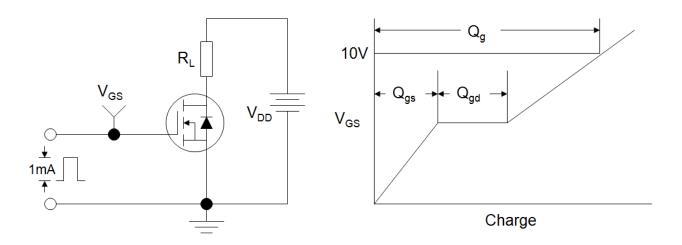


Figure B: Resistive Switching Test Circuit and Waveform

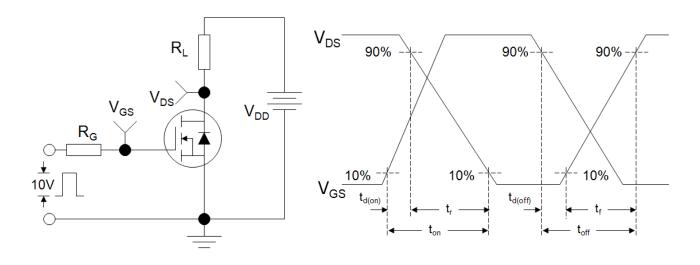
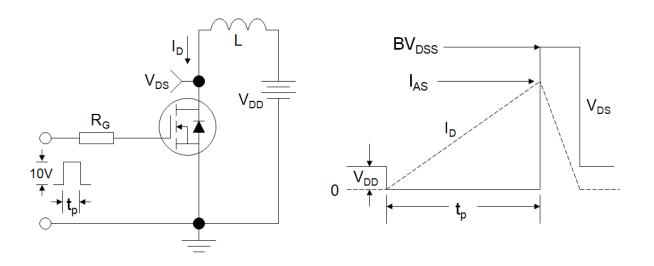
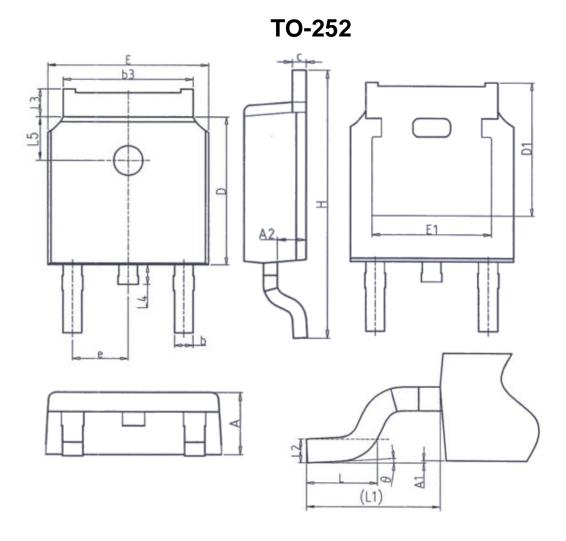


Figure C: Unclamped Inductive Switching Test Circuit and Waveform







Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A1	0.00	0. 20	
A2	0. 97	1. 17	
b	0. 68	0. 90	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	
D1	D1 5. 30REF		
E	6. 40	6. 80	
E1	4. 63	_	

Unit: mm				
Symbol	Min.	Max.		
е	2. 286BSC			
Н	9. 40	10.50		
L	1. 38	1. 75		
L1	2. 90REF			
L2	0. 51BSC			
L3	0.88	1. 28		
L4	_	1.00		
L5	1. 65	1. 95		
θ	0°	8°		



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