

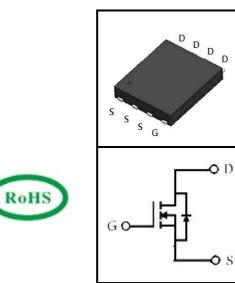
40V N-Channel Split Gate MOSFET

FEATURES

- Super Low Gate Charge
- 100% EAS Guaranteed
- RoHS compliant
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

APPLICATIONS

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



| Device Marking and Package Information | | | | |
|--|---------|-----------|--|--|
| Device | Package | Marking | | |
| CSN04N1P5 | DFN5*6 | CSN04N1P5 | | |

| Absolute Maximum Ratings at T _j = 25°C unless otherwise noted | | | | | |
|--|---------|-----------------------------------|----------|------|--|
| Parameter | | Symbol | Value | Unit | |
| Drain-Source Voltage (V _{GS} = 0V) | | V _{DSS} | 40 | V | |
| Continuous Drain Current T _C = 25°C | (note1) | | 180 | А | |
| Continuous Drain Current T _C = 100°C | (note1) | Ι _D | 120 | А | |
| Pulsed Drain Current | (note2) | I _{DM} | 520 | А | |
| Gate Source Voltage | | V _{GSS} | ±20 | V | |
| Single Pulse Avalanche Energy | (note3) | E _{AS} | 430 | mJ | |
| Power Dissipation T _c = 25°C | (note4) | P _D | 125 | W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | -55~+175 | °C | |

| Thermal Characteristics | | | | |
|---|---------|-----------------------|-------|------|
| Parameter | | Symbol | Value | Unit |
| Thermal Resistance, Junction-Ambient | (note1) | $R_{	extsf{	heta}JA}$ | 50 | °C/W |
| Thermal Resistance, Junction-to-Ambient | (note1) | $R_{	extsf{	heta}JA}$ | 32 | °C/W |



| Electrical Characteristics T _j | = 25°C ur | nless otherwise specified | | | | | |
|---|---|---|-------|------|------|------|--|
| Demonster | Or mark at | Test Oseditions | Value | | | | |
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | $V_{(BR)DSS}$ | V _{GS} = 0V, I _D = 250µA | 40 | 1 | | V | |
| Zero Gate Voltage Drain Current | | V _{DS} = 32V, V _{GS} = 0V, T _J = 25°C | | | 1 | uA | |
| | I _{DSS} | V _{DS} = 32V, V _{GS} = 0V, T _J = 55°C | | | 5 | uA | |
| Gate-Source Leakage | I _{GSS} | V_{GS} = $\pm 20V$ | | | ±100 | nA | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 1.0 | 1.6 | 2.3 | V | |
| | | V _{GS} = 10V, I _D = 30A | | 1.25 | 1.5 | mΩ | |
| Drain-Source On-Resistance (note2) | R _{DS(on)} | V _{GS} = 4.5V, I _D = 20A | | 1.9 | 2.5 | mΩ | |
| Dynamic | | | | | | | |
| Input Capacitance | C _{iss} | $\lambda = 0 \lambda$ | | 3972 | | | |
| Output Capacitance | $R_{DS(on)}$ $V_{GS} = 4.5V, I_D = 20A$ | 1119 | | pF | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 82 | | | |
| Total Gate Charge (4.5V) | Q _g | | | 45 | | | |
| Gate-Source Charge | Q_gs | V _{DD} = 15V, I _D = 20A, V _{CS} = 10V | | 12 | | nC | |
| Gate-Drain Charge | Q_{gd} | $V_{DD} = 15V, I_D = 20A,$ $V_{GS} = 10V$ | | 18.5 | | | |
| Turn-on Delay Time | t _{d(on)} | | | 18.5 | | | |
| Turn-on Rise Time | t _r | V _{DS} = 15V, I _D = 20A | | 9 | | | |
| Turn-off Delay Time | $t_{d(off)}$ | $V_{GS} = 10V, R_{G} = 3.3\Omega$ | | 58.5 | | ns | |
| Turn-off Fall Time | t _f | | | 32 | | | |
| Body Diode Characteristics | | | | | • | | |
| Continuous Body Diode Current | I _s | T - 2500 | | | 180 | А | |
| Pulsed Diode Forward Current | I _{SM} | T _C = 25°C | | | 520 | А | |
| Body Diode Voltage | V_{SD} | T _J = 25°C, I _{SD} = 5A, V _{GS} = 0V | | - | 1.2 | V | |

Notes

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width ${\leq}300 \text{us}$, duty cycle ${\leq}2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.1mH
- 4. The power dissipation is limited by 175°C junction temperature
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

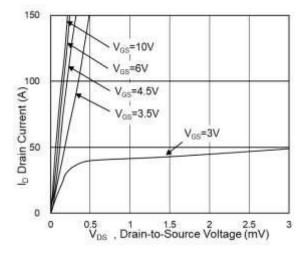


Fig.1 Typical Output Characteristics

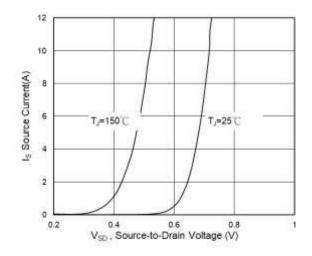
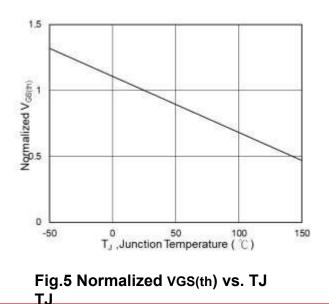


Fig.3 Forward Characteristics of Reverse diode



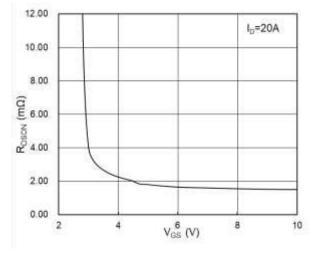


Fig.2 On-Resistance vs. G-S Voltage

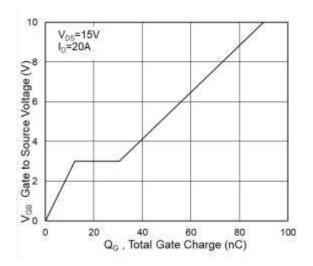


Fig.4 Gate-Charge Characteristics

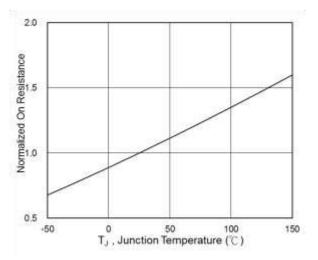


Fig.6 Normalized RDSON vs.



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

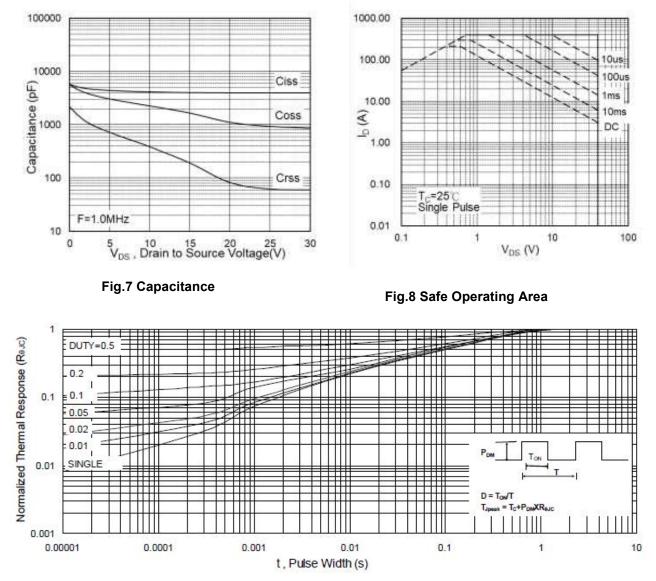


Fig.9 Normalized Maximum Transient Thermal Impedance





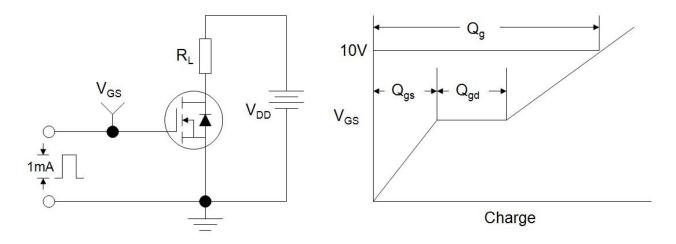


Figure B: Resistive Switching Test Circuit and Waveform

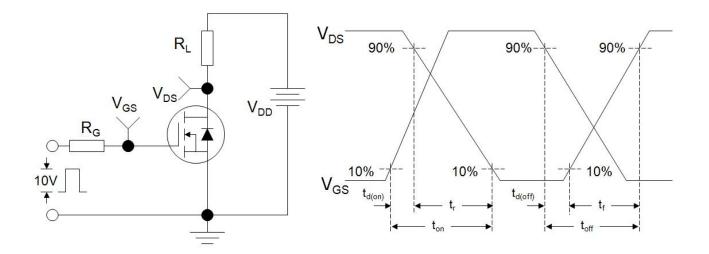
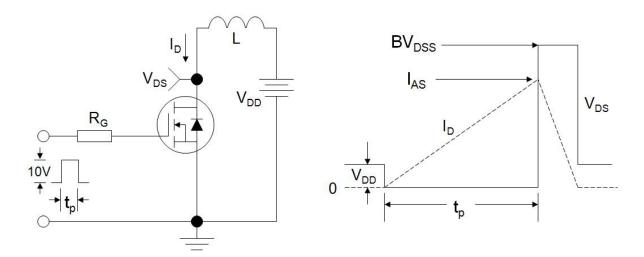
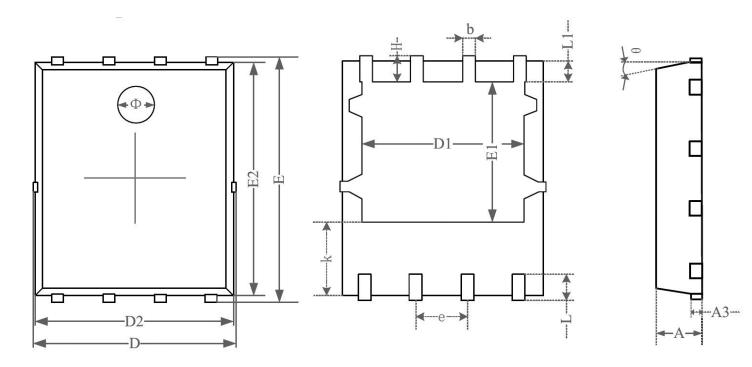


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





DFN5*6



| SYMBOLS | DIMENSIONS IN MILLIMETERS | | | DIMENSIONS IN INCHES | | |
|---------|---------------------------|-------|-------|----------------------|-----------|-------|
| | MIN | NOM | MAX | MIN | NOM | MAX |
| А | 0.870 | 0.900 | 0.930 | 0.034 | 0.035 | 0.036 |
| A3 | 0.152REF. | | | 2 | 0.006REF. | |
| D | 4.944 | 5.020 | 5.096 | 0.195 | 0.198 | 0.201 |
| Е | 5.974 | 6.050 | 6.126 | 0.235 | 0.238 | 0.241 |
| Dl | 3.910 | 4.010 | 4.110 | 0.154 | 0.158 | 0.162 |
| El | 3.375 | 3.475 | 3.575 | 0.133 | 0.137 | 0.141 |
| D2 | 4.870 | 4.900 | 4.930 | 0.192 | 0.193 | 0.194 |
| E2 | 5.720 | 5.750 | 5.780 | 0.226 | 0.227 | 0.228 |
| k | 1.190 | 1.290 | 1.390 | 0.047 | 0.051 | 0.055 |
| b | 0.350 | 0.380 | 0.410 | 0.014 | 0.015 | 0.016 |
| e | 1.270TYP. | | | | 0.050TYP. | 5. |
| L | 0.559 | 0.635 | 0.711 | 0.022 | 0.025 | 0.028 |
| L1 | 0.424 | 0.500 | 0.576 | 0.017 | 0.020 | 0.023 |
| Н | 0.574 | 0.650 | 0.726 | 0.023 | 0.026 | 0.029 |
| θ | 10° | 11° | 12 ° | 10° | 11° | 12° |
| Φ | 1.150 | 1.200 | 1.250 | 0.045 | 0.047 | 0.049 |



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