



800V Super-junction Power MOSFET

Description

800V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

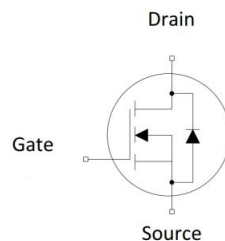
Features

- Ultra-fast body diode
- Very low FOM $R_{DS(on)} \times Q_g$
- Easy to use/drive
- 100% avalanche tested
- RoHS compliant

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LLC Half-bridge
- Low Power Chargers and Adapters

TO-247



Device Marking and Package Information

Device	Package	Marking
TPW80R300MFD	TO-247	80R300MFD

Key Performance Parameters

Parameter	Value	Unit
$V_{DS} @ T_{j,max}$	850	V
$R_{DS(on),max}$	0.3	Ω
$Q_{g,typ}$	42.95	nC
I_D	17	A
$I_{D,pulse}$	51	A
$E_{OSS} @ 400V$	3.96	μJ
Body Diode di_f/dt	900	A/ μs
t_{rr}	133.6	ns
Q_{rr}	0.82	μC
I_{rrm}	11.8	A



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted				
Parameter		Symbol	Values	Unit
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	17	A
	$T_C = 100^\circ\text{C}$		10.2	
Pulsed Drain Current	(note1)	$I_{D,pulse}$	51	A
Gate-Source Voltage		V_{GSS}	$\pm 30\text{V}$	V
Single Pulse Avalanche Energy	(note2)	E_{AS}	245	mJ
Repetitive Avalanche Energy	(note2)	E_{AR}	0.7	mJ
Avalanche Current		I_{AR}	3.5	A
MOSFET dv/dt Ruggedness, $V_{DS} = 0 \dots 480\text{V}$		dv/dt	50	V/ns
Power Dissipation For TO-247		P_D	151	W
Continuous Diode Forward Current		I_S	17	A
Diode Pulsed Current	(note1)	$I_{S,pulse}$	51	
Reverse Diode dv/dt	(note3)	dv/dt	50	V/ns
Maximum Diode Commutation Speed	(note3)	di/dt	900	A/ μs
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55~+150	$^\circ\text{C}$

Thermal Resistance For TO-247			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	0.83	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62	



Electrical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	800	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 800V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 30V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	3	--	5	V
Drain-Source On-State-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 8.5A$	--	0.27	0.3	Ω
Gate Resistance	R_G	$f = 1.0\text{MHz}$ open drain	--	4.9	--	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 100V$ $f = 1.0\text{MHz}$	--	1830.9	--	pF
Output Capacitance	C_{oss}		--	49.59	--	
Reverse Transfer Capacitance	C_{rss}		--	0.88	--	
Total Gate Charge	Q_g	$V_{DD} = 640V,$ $I_D = 17A,$ $V_{GS} = 10V$	--	42.95	--	nC
Gate-Source Charge	Q_{gs}		--	7.05	--	
Gate-Drain Charge	Q_{gd}		--	13.4	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 400V$ $I_D = 17A$ $R_G = 25\Omega$	--	47.8	--	ns
Turn-on Rise Time	t_r		--	28.5	--	
Turn-off Delay Time	$t_{d(off)}$		--	154	--	
Turn-off Fall Time	t_f		--	51.4	--	
Drain-Source Body Diode Characteristics						
Body Diode Forward Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = 8.5A, V_{GS} = 0V$	--	0.9	1.5	V
Reverse Recovery Time	t_{rr}	$V_R = 400V$ $I_F = 17A$ $di_F/dt = 100A/\mu\text{s}$	--	133.6	--	ns
Reverse Recovery Charge	Q_{rr}		--	0.82	--	μC
Peak Reverse Recovery Current	I_{rrm}		--	11.8	--	A

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_D = 17A, V_{DD} = 50V, R_G = 25\Omega, \text{Starting } T_J = 25^\circ\text{C}$
3. Identical low side and high side switch with identical R_G



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

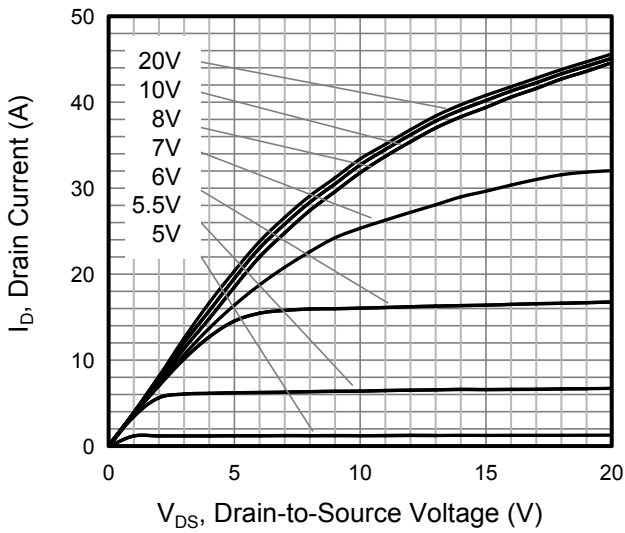


Figure 2. Transfer Characteristics

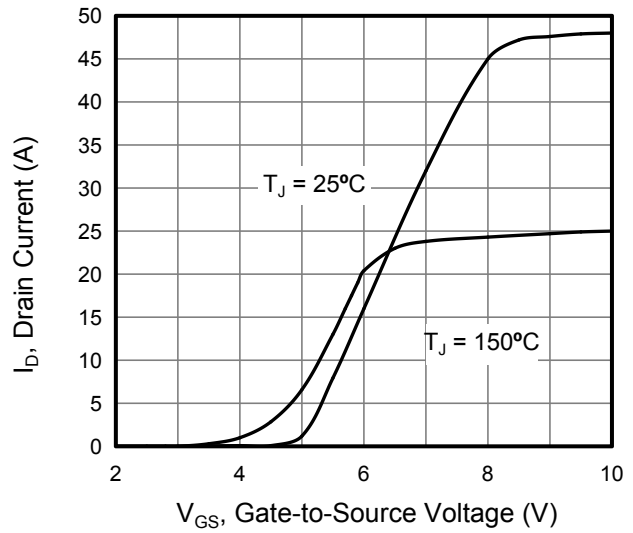


Figure 3. On-Resistance vs. Drain Current

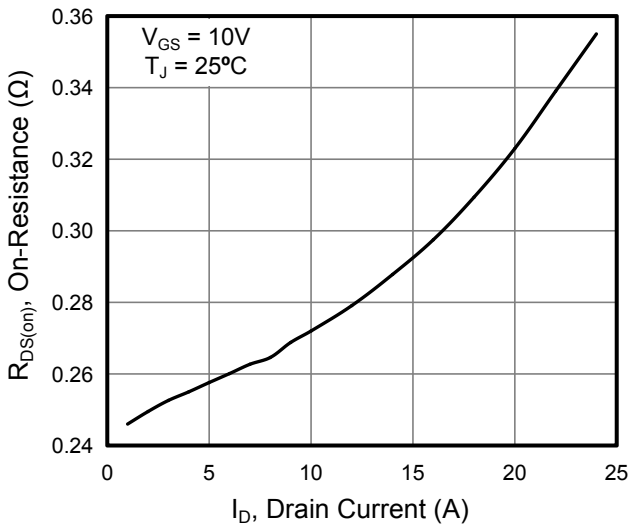


Figure 4. Capacitance

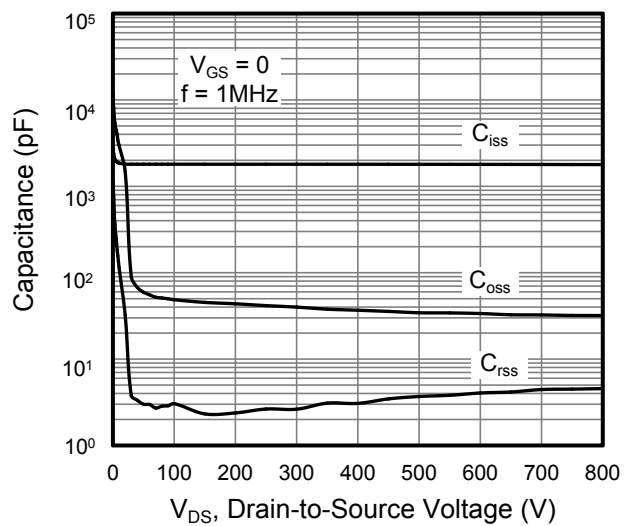


Figure 5. Gate Charge

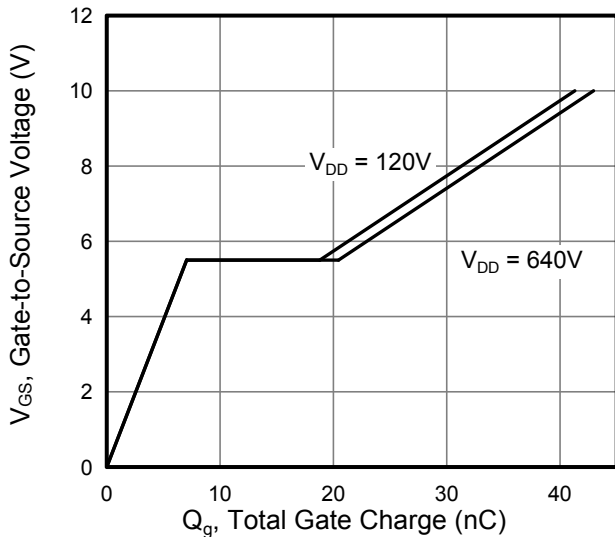
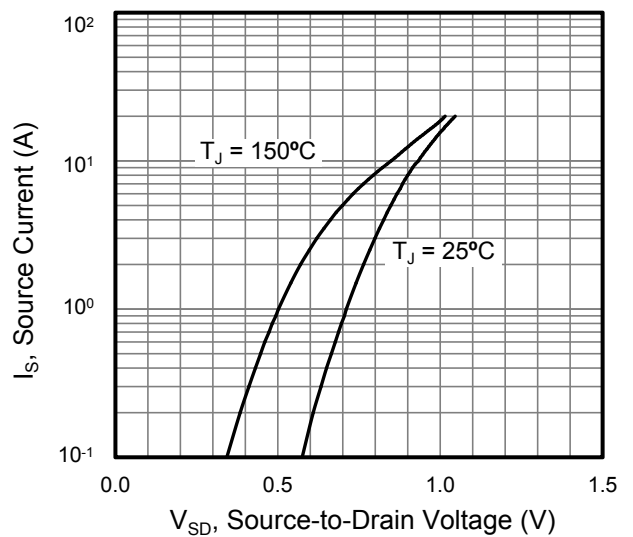


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. Temperature

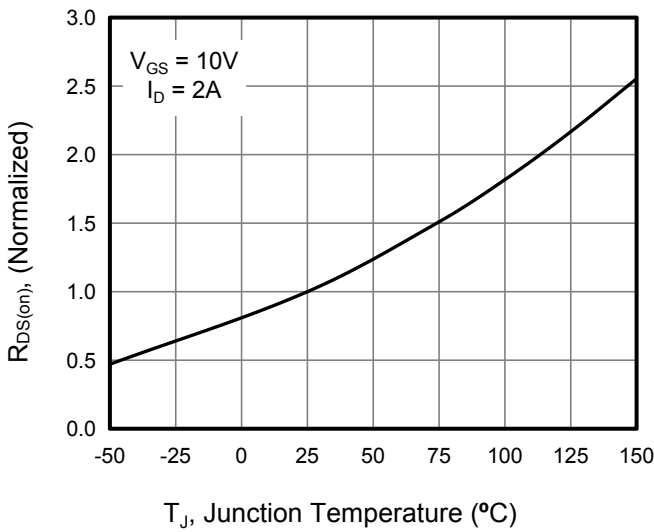


Figure 8. Breakdown Voltage vs. Junction Temperature

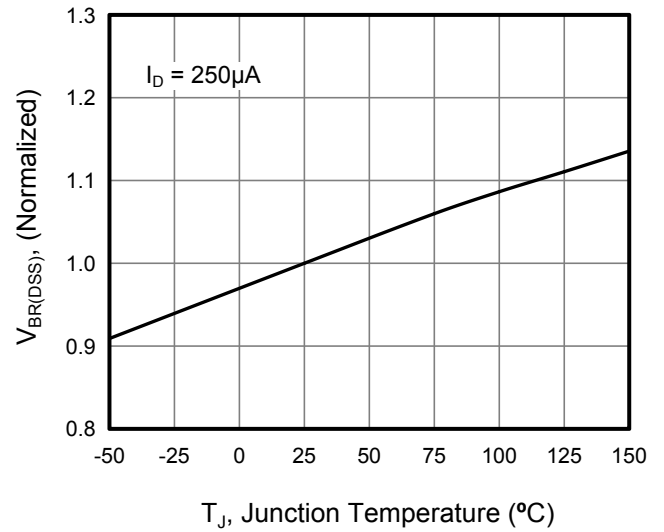


Figure 9. Transient Thermal Impedance For TO-247

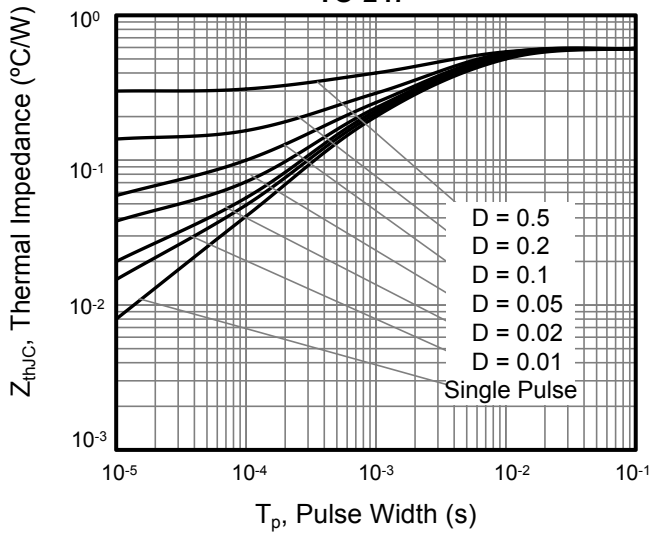


Figure 10. Safe Operation Area For TO-247

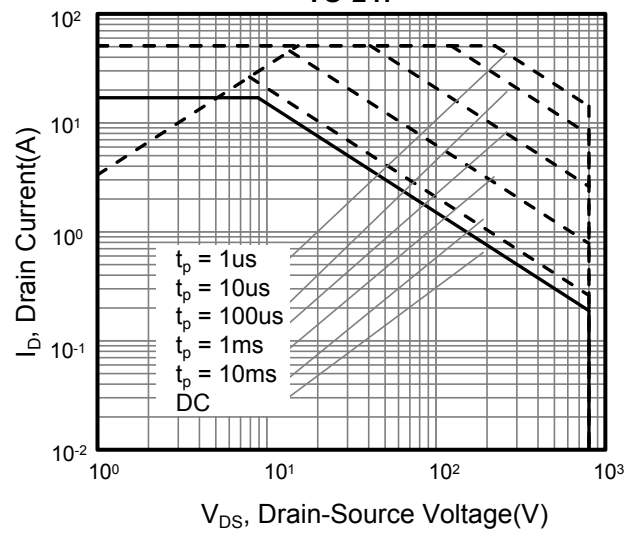


Figure 11. Typ. Coss Stored Energy

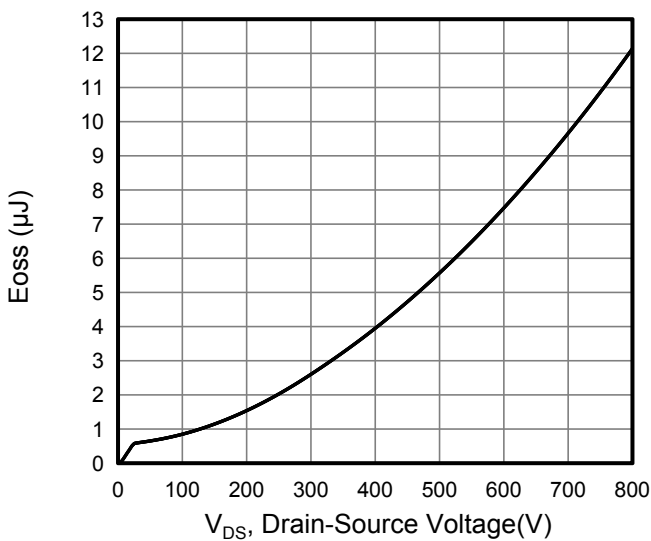




Figure A: Gate Charge Test Circuit and Waveform

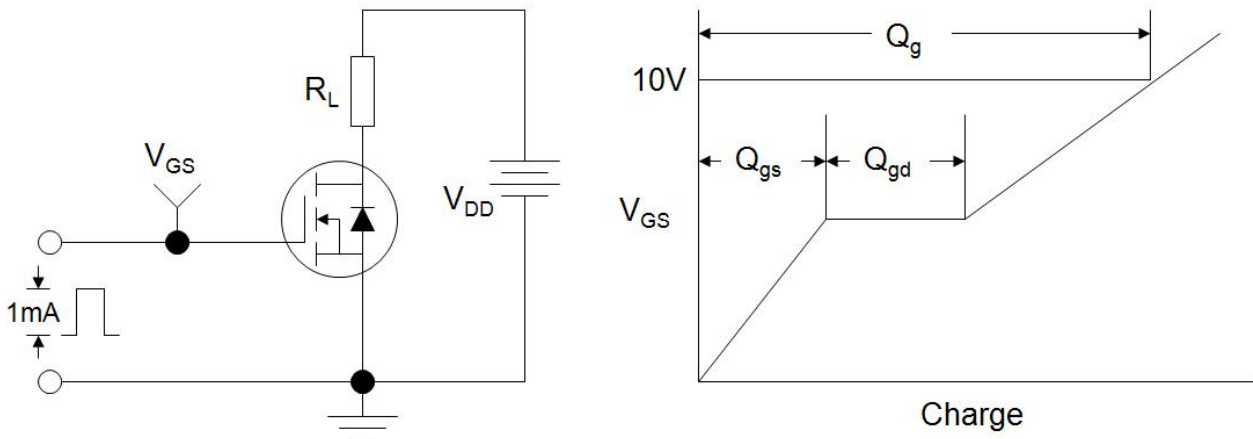


Figure B: Resistive Switching Test Circuit and Waveform

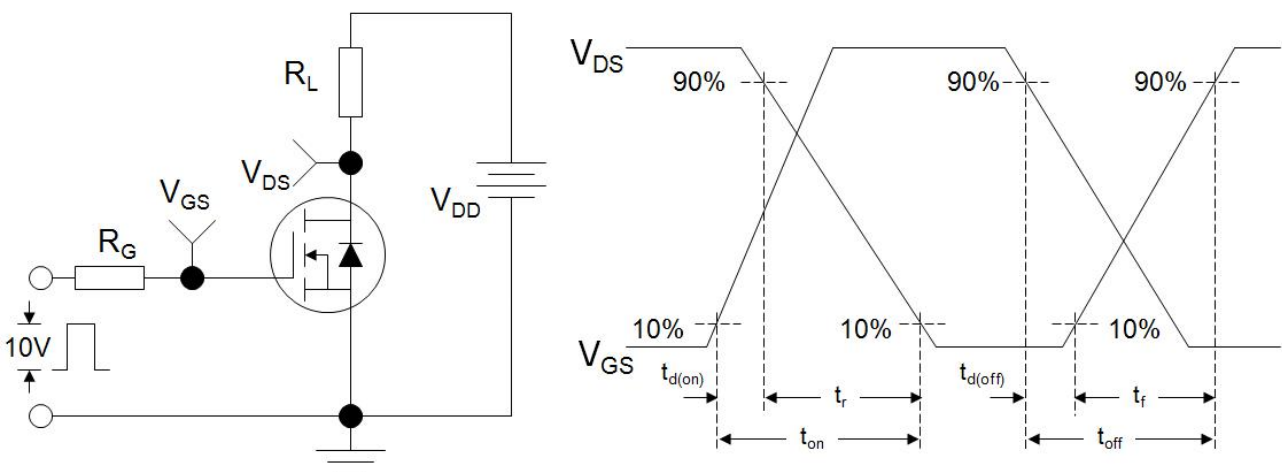
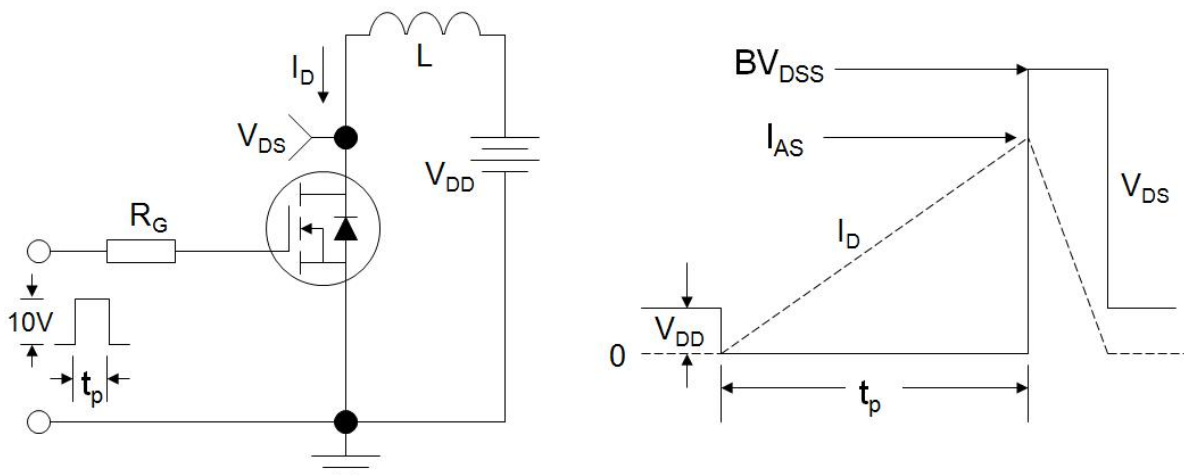
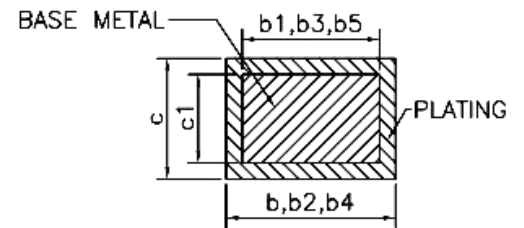
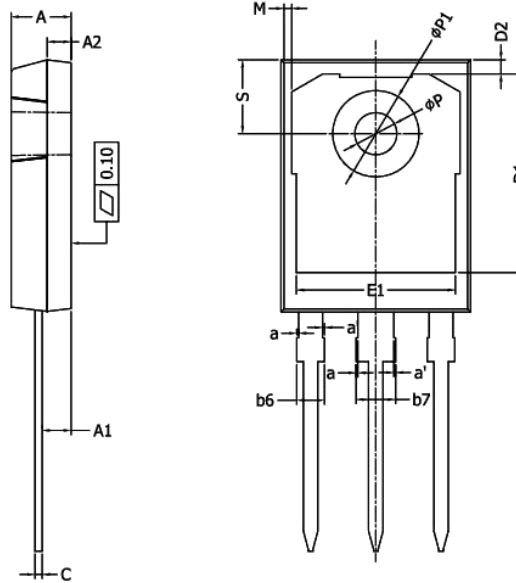
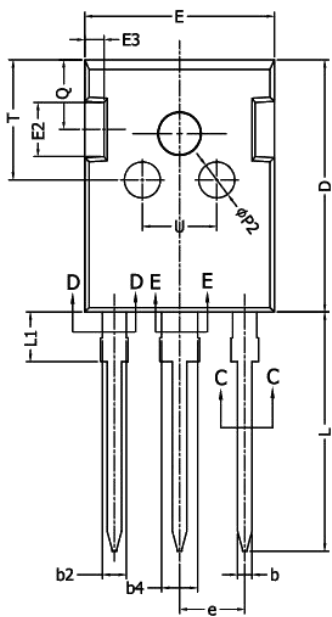


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-247 (封装厂 I)



SYMBOL	MIN	NOM	MAX
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0	---	0.15
a'	0	---	0.15
b	1.16	---	1.26
b1	1.15	1.2	1.22
b2	1.96	---	2.06
b3	1.95	2.00	2.02
b4	2.96	---	3.06
b5	2.96	3.00	3.02
b6	---	---	2.25
b7	---	---	3.25
c	0.59	---	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
e	5.436 BSC		
L	19.80	19.92	20.10
L1	---	---	4.30
M	0.35	---	0.95
P	3.40	3.50	3.60
P1	7.00	---	7.40
P2	2.40	2.50	2.60
Q	5.60	---	6.00
S	6.05	6.15	6.25
T	9.80	---	10.20
U	6.00	---	6.40



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