

## N and P Channel Enhancement Mode Power MOSFET

**Description**

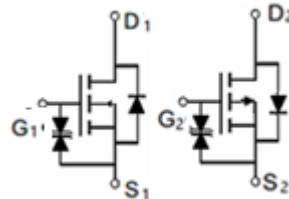
This Product uses advanced trench technology MOSFETs to provide excellent  $R_{DS(ON)}$  and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

**General Features**

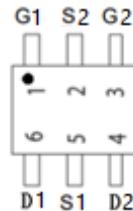
- **NMOS**
- $V_{DS}$  20V
- $I_D$  (at  $V_{GS} = 10V$ ) 1.36A
- $R_{DS(ON)}$  (at  $V_{GS} = 4.5V$ ) < 375mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = 2.5V$ ) < 450mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = 1.8V$ ) < 800mΩ
  
- **PMOS**
- $V_{DS}$  -20V
- $I_D$  (at  $V_{GS} = -10V$ ) -1.15A
- $R_{DS(ON)}$  (at  $V_{GS} = -4.5V$ ) < 520mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = -2.5V$ ) < 700mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = -1.8V$ ) < 1000mΩ
- 100% Avalanche Tested
- RoHS Compliant
- ESD (HBM)>2.0KV

**Application**

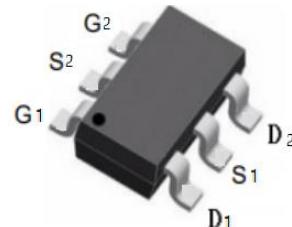
- Power switch
- DC/DC converters



Schematic diagram



Marking and pin assignment



SOT-23-6L

Device	Package	Marking	Packaging
G1NP02ELL	SOT-23-6	G1NP02	3000pcs/Reel

**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	NMOS	PMOS	Unit
Drain-Source Voltage	$V_{DS}$	20	-20	V
Continuous Drain Current	$I_D$	1.36	-1.15	A
Pulsed Drain Current (note1)	$I_{DM}$	5.4	-3.4	A
Gate-Source Voltage	$V_{GS}$	$\pm 10$	$\pm 10$	V
Power Dissipation	$P_D$	1.12	1.12	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	-55 To 150	°C

**Thermal Resistance**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	111	°C/W

**NMOS Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

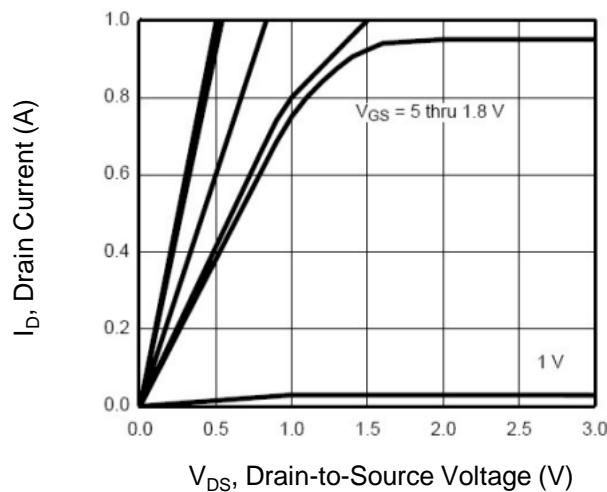
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}$	--	--	$\pm 10$	$\text{uA}$
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.35	0.55	1	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 0.65\text{A}$	--	159	370	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 0.55\text{A}$	--	209	450	
		$V_{\text{GS}} = 1.8\text{V}, I_D = 0.45\text{A}$	--	290	800	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=10\text{V}, I_D=0.5\text{A}$	--	1.6	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 10\text{V}, f = 1.0\text{MHz}$	--	60	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	15	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	5	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 10\text{V}, I_D = 250\text{mA}, V_{\text{GS}} = 4.5\text{V}$	--	750	--	$\text{pC}$
Gate-Source Charge	$Q_{\text{gs}}$		--	75	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	225	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}, I_D = 0.5\text{A}, R_G = 10\Omega$	--	6.7	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	4.8	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	17.3	--	
Turn-off Fall Time	$t_f$		--	7.4	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	1.36	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 0.5\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V

**Notes**

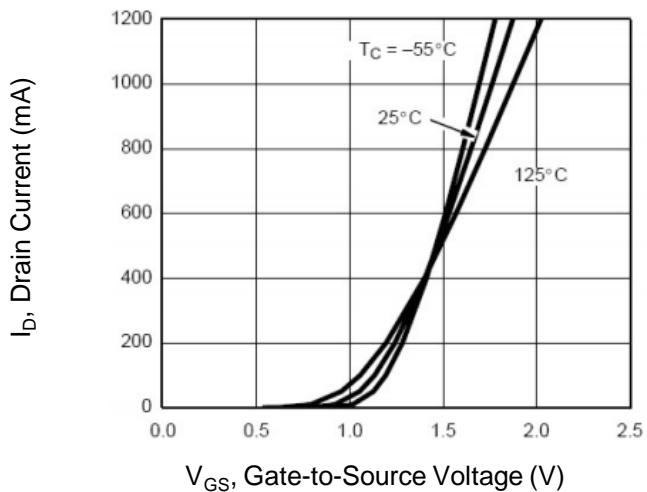
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical  $R_G$

NMOS Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

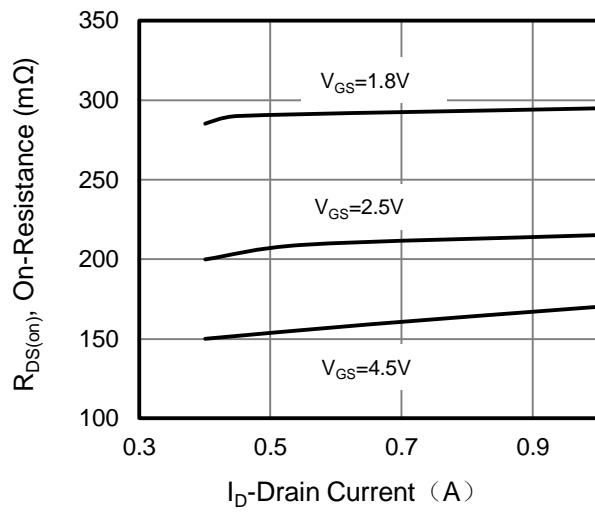
**Figure 1. Output Characteristics**



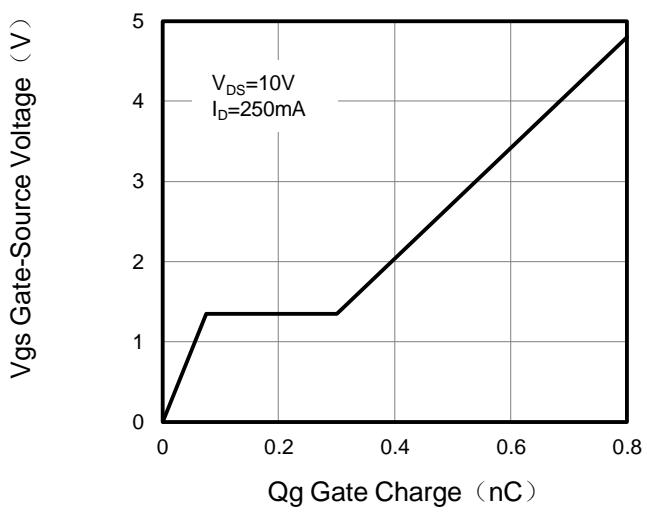
**Figure 2. Transfer Characteristics**



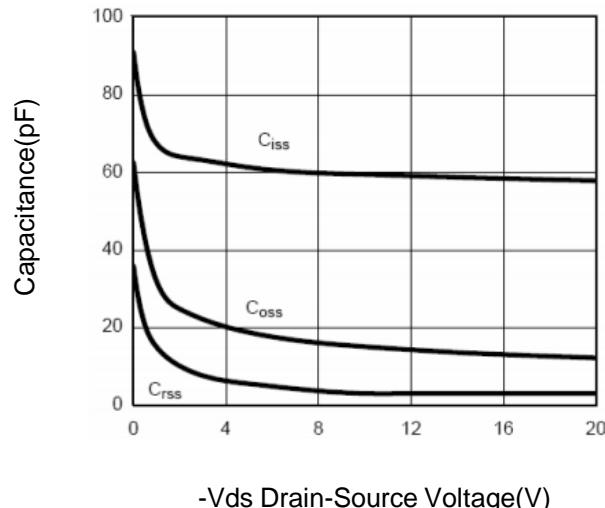
**Figure 3. Drain-Source On-Resistance**



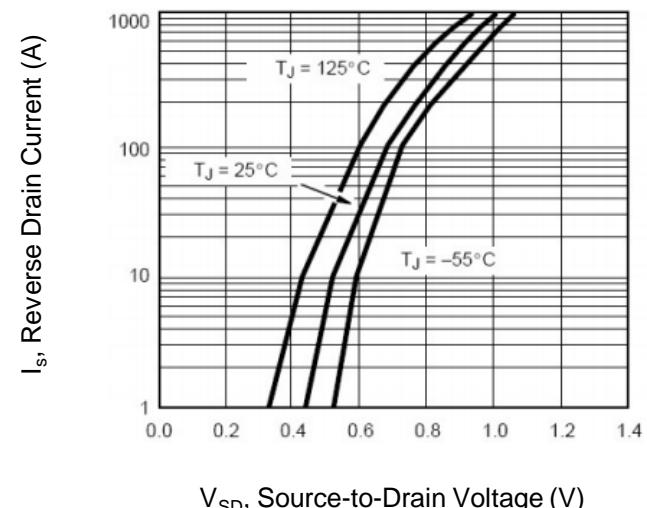
**Figure 4. Gate Charge**



**Figure 5. Capacitance**



**Figure 6. Source-Drain Diode Forward**



NMOS Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 7. Drain-Source On-Resistance

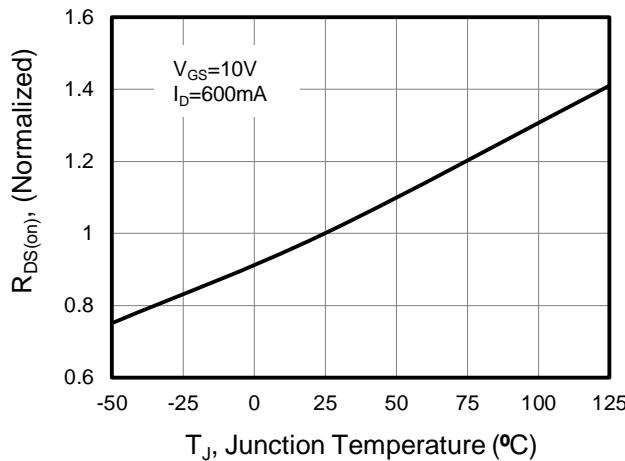


Figure 8. Safe Operation Area

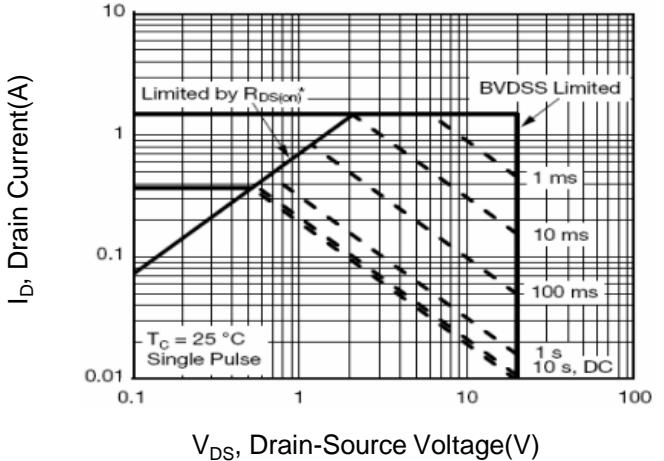
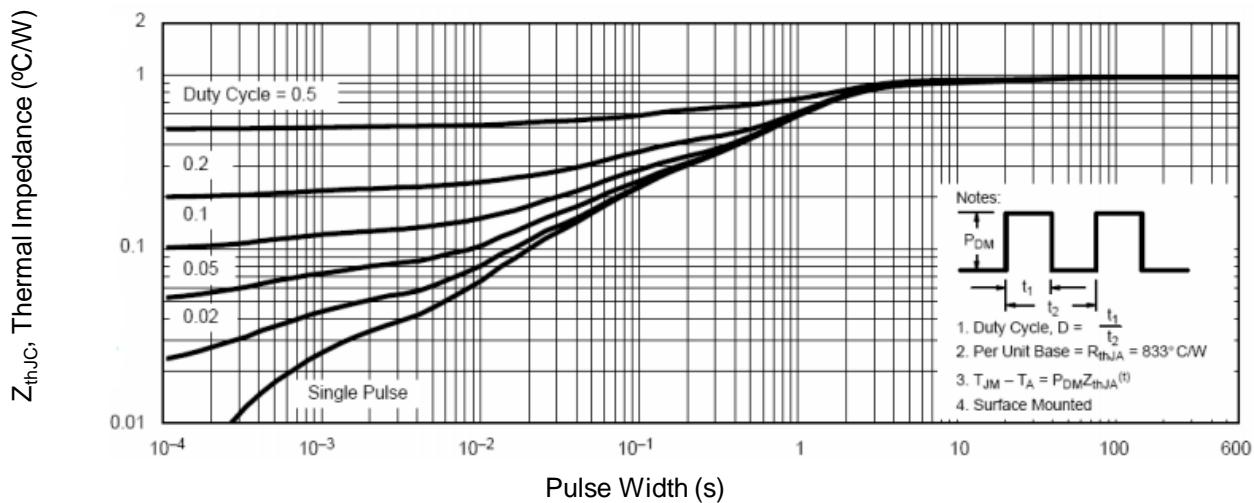


Figure 9. Normalized Maximum Transient Thermal Impedance



**PMOS Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

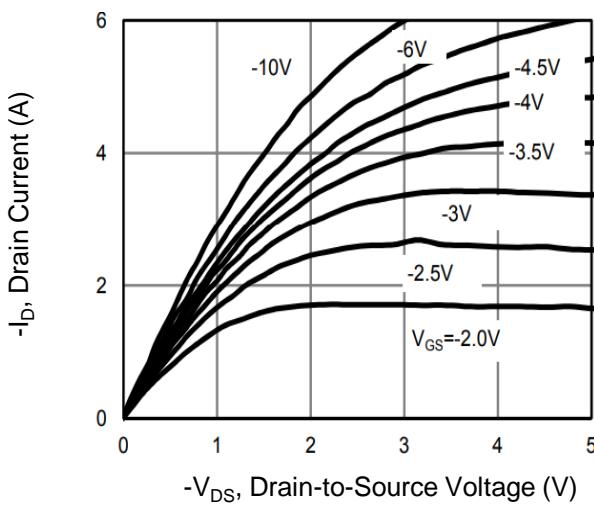
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-20	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	-1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}$	--	--	$\pm 10$	$\mu\text{A}$
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.35	-0.55	-0.8	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -4.5\text{V}, I_D = -0.5\text{A}$	--	375	520	$\text{m}\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_D = -0.5\text{A}$	--	478	700	
		$V_{\text{GS}} = -1.8\text{V}, I_D = -0.5\text{A}$	--	621	1000	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = -5\text{V}, I_D = -0.6\text{A}$	--	1.7	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -10\text{V}, f = 1.0\text{MHz}$	--	68.4	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	17	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	5.7	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = -10\text{V}, I_D = -250\text{mA}, V_{\text{GS}} = -4.5\text{V}$	--	855	--	$\text{pC}$
Gate-Source Charge	$Q_{\text{gs}}$		--	85.5	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	256.5	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -10\text{V}, I_D = -0.6\text{A}, R_G = 3\Omega$	--	6.5	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	6.5	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	18.2	--	
Turn-off Fall Time	$t_f$		--	5.5	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	-1.15	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = -0.5\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	-1	V

**Notes**

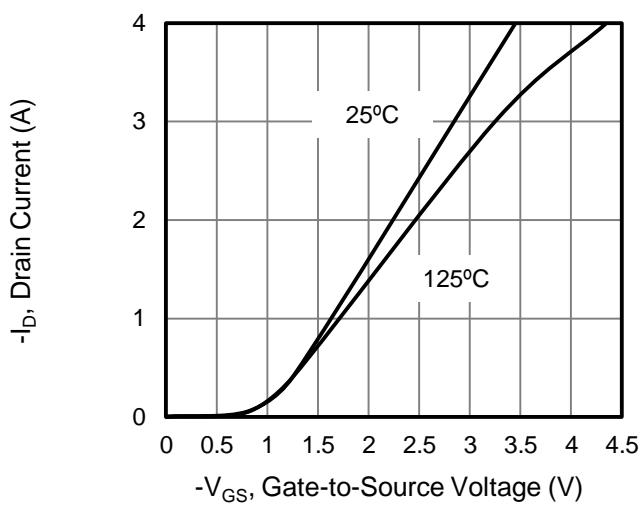
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical  $R_G$

**PMOS Typical Characteristics**  $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

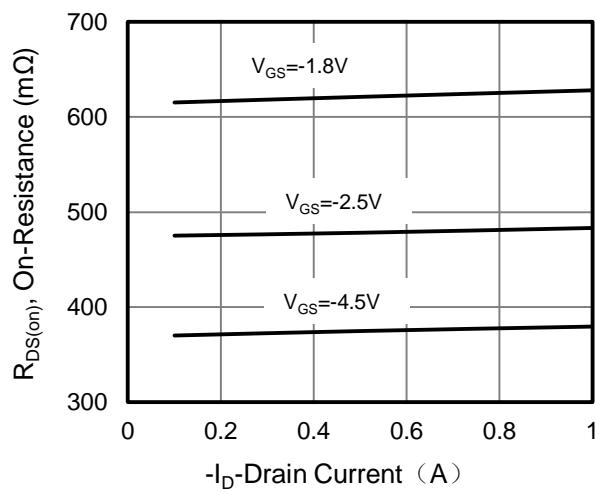
**Figure 1. Output Characteristics**



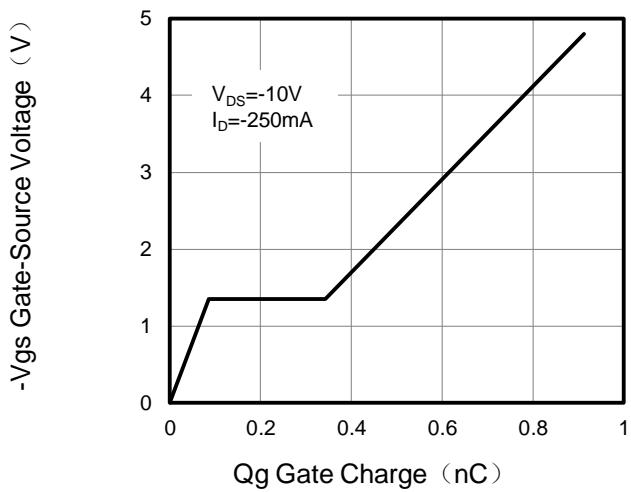
**Figure 2. Transfer Characteristics**



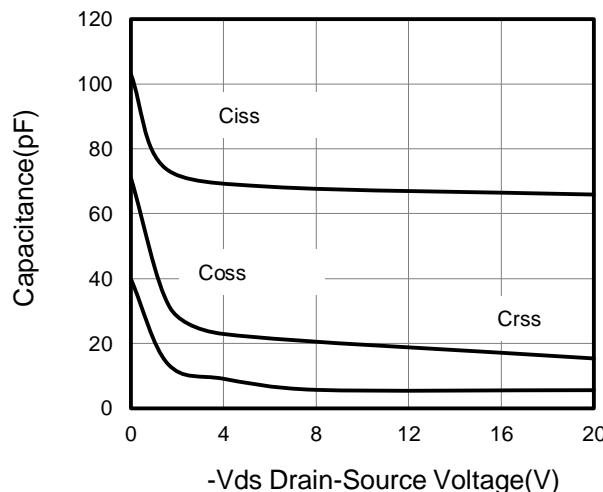
**Figure 3. Rdson-Drain Current**



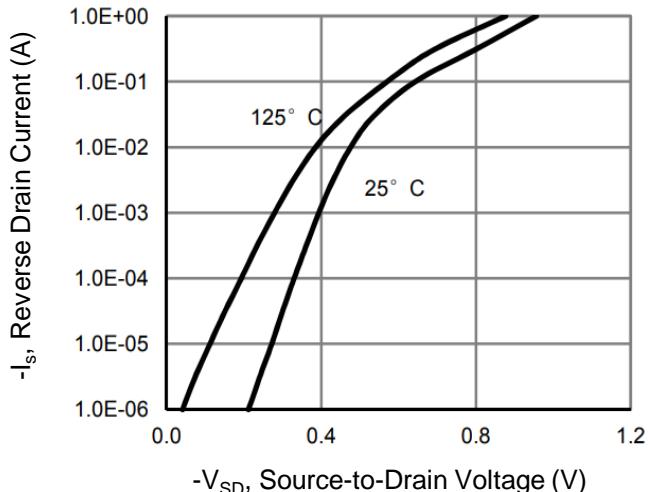
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

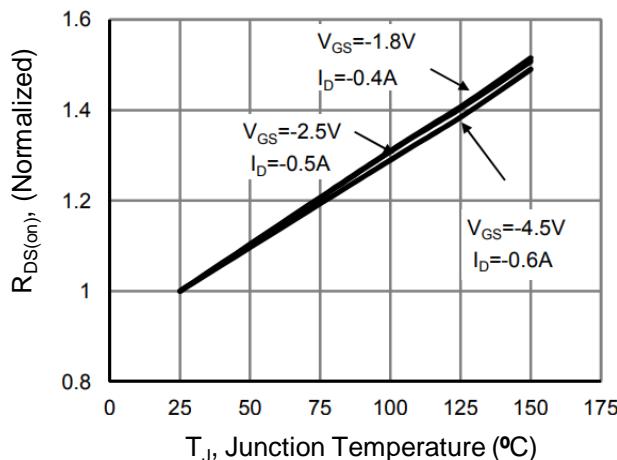


**Figure 6. Source-Drain Diode Forward**

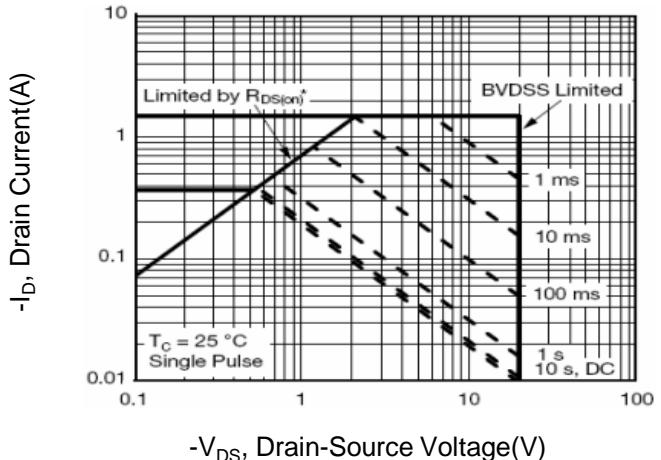


**PMOS Typical Characteristics**  $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

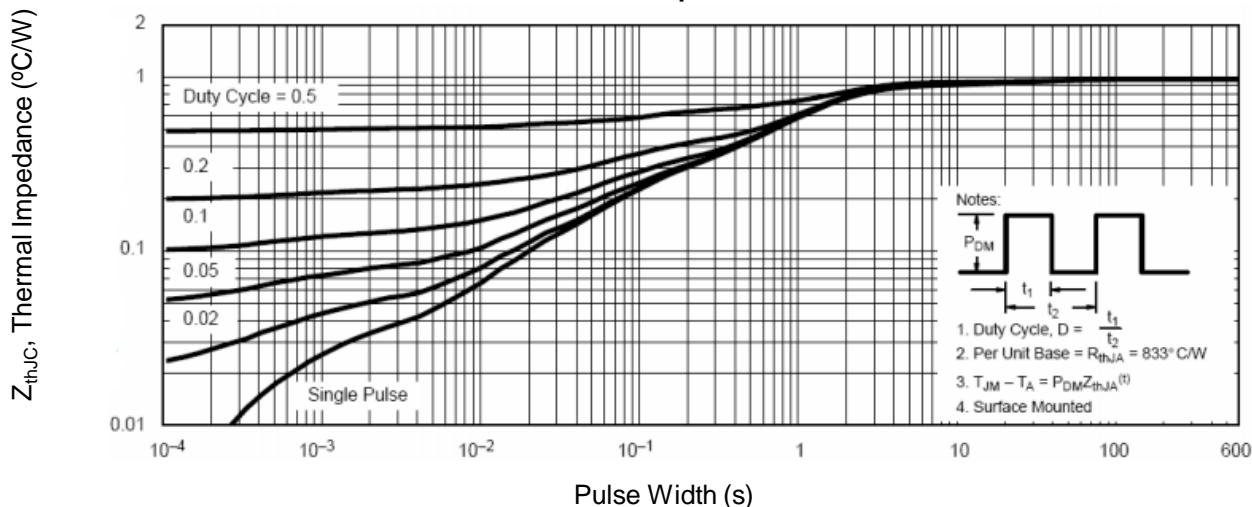
**Figure 7. Drain-Source On-Resistance**



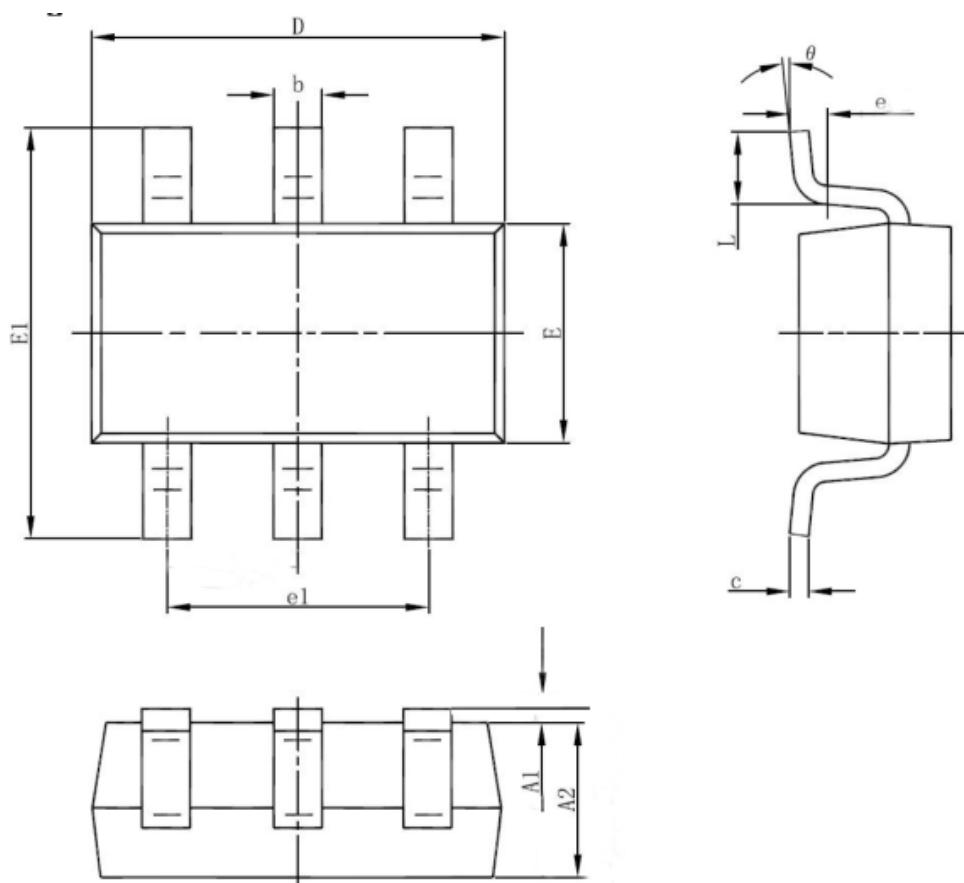
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## SOT-23-6L Package Information



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A1	0.00	-	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.10	0.15	0.20
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
e	0.2GAUGE PLANE		
e1	-	1.90	-
L	0.30	0.45	0.60
Θ	0°	-	8°
All Dimensions in mm			