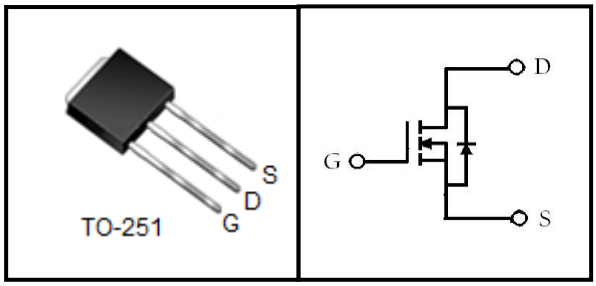
200V N-Channel Trench MOSFET

FEATURES

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology



APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Hard switched and high frequency circuits

RoHS

Device Marking and Package Information

Device	Package	Marking
CTU20N170	TO-251	CTU20N170

Absolute Maximum Ratings at T_j= 25ºC unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V_{GS} = 0V)	V_{DSS}	200	V
Continuous Drain Current T_C = 25ºC (note1)	I_D	18	A
Continuous Drain Current T_C = 100ºC (note1)		12	A
Drain Current-Continuous@ Current-Pulsed (note2)	I_{DM}	40	A
Gate-Source Voltage (VDS=0V)	V_{GSS}	±20	V
Single Pulse Avalanche Energy (note3)	E_{AS}	15	mJ
Maximum Power Dissipation(Tc=25℃) (note4)	P_D	83	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+175	ºC

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (note1)	$R_{\theta JC}$	3.6	ºC/W
Thermal Resistance, Junction-to-Ambient (note1)	$R_{\theta JA}$	62	

Electrical Characteristics T_j= 25ºC unless otherwise specified						
Parameter	**Symbol**	**Test Conditions**	**Value**			**Unit**
			Min.	**Typ.**	**Max.**	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	200	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 200V, V_{GS} = 0V, T_J = 25ºC	--	--	1	uA
		V_{DS} = 200V, V_{GS} = 0V, T_J = 55ºC	--	--	5	
Gate-Source Leakage	I_{GSS}	V_{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	--	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 8A	--	140	170	mΩ
		V_{GS} = 4.5V, I_D = 8A	--	150	180	mΩ
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1.0MHz	--	2047	--	pF
Output Capacitance	C_{oss}		--	109	--	
Reverse Transfer Capacitance	C_{rss}		--	70	--	
Total Gate Charge	Q_g	V_{DS} = 80V, I_D = 9A, V_{GS} = 10V	--	45	--	nC
Gate-Source Charge	Q_{gs}		--	9	--	
Gate-Drain Charge	Q_{gd}		--	10.5	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DS} = 100V, I_D = 9A, R_G = 3Ω, V_{GS}=10V,	--	13	--	ns
Turn-on Rise Time	t_r		--	8.2	--	
Turn-off Delay Time	$t_{d(off)}$		--	25	--	
Turn-off Fall Time	t_f		--	11	--	
Body Diode Characteristics						
Continuous Body Diode Current	I_S		--	--	18	A
Pulsed Diode Forward Current	I_{SM}		--	--	40	
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 9A, V_{GS} = 0V	--	--	1.2	V
Reverse Recovery Time	t_{rr}	I_F = 9A di_F/dt = 100A/μs TJ=25℃	--	37	--	ns
Reverse Recovery Charge	Q_{rr}		--	103	--	nC

Notes

1.The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width≦300us , duty cycle≦2%
3.The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.1mH
4.The power dissipation is limited by 175℃ junction temperature
5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics T_J = 25ºC, unless otherwise noted

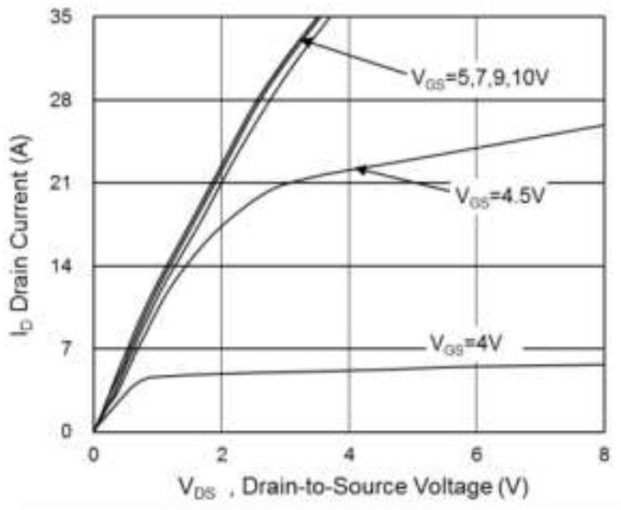


Fig.1 Typical Output Characteristics

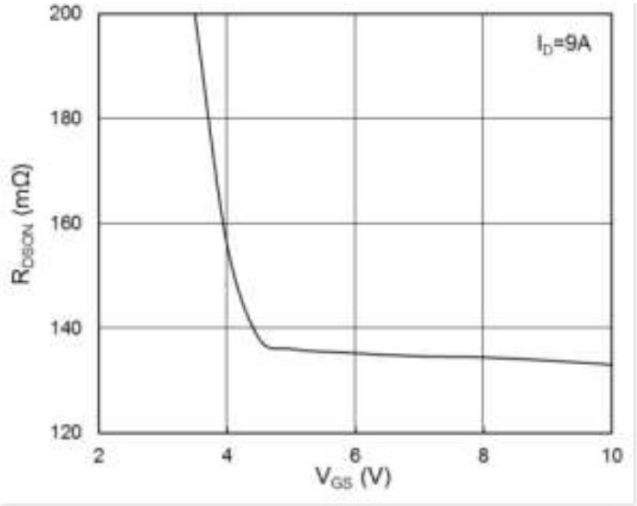


Fig.2 On-Resistance vs. G-S Voltage

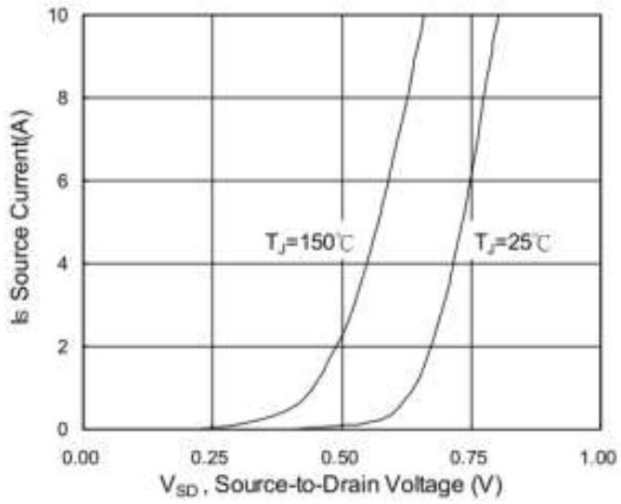


Fig.3 Forward Characteristics of Reverse Diode

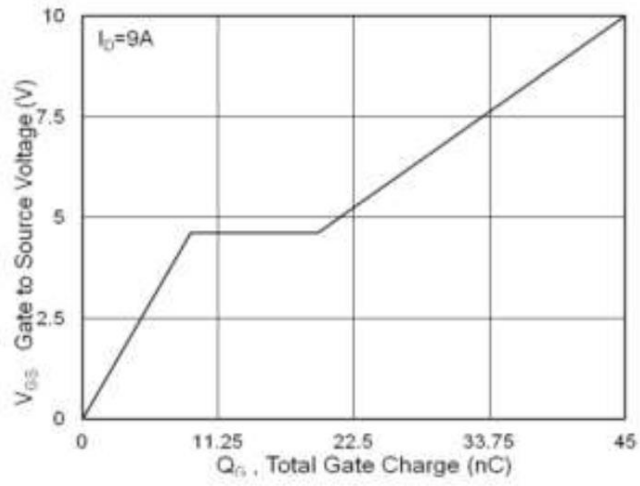


Fig.4 Gate-Charge Characteristics

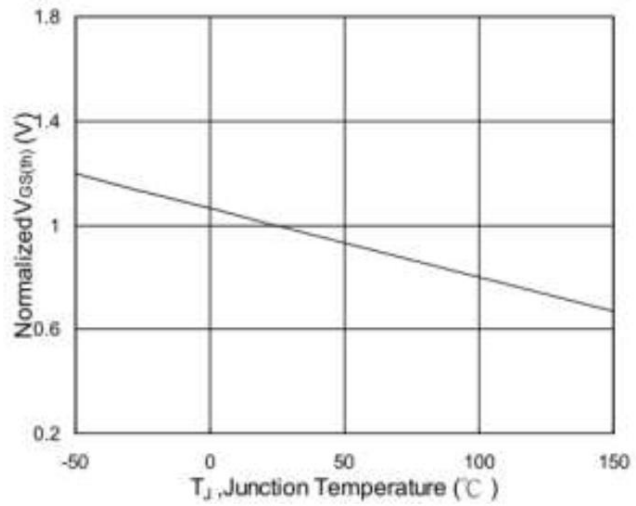


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

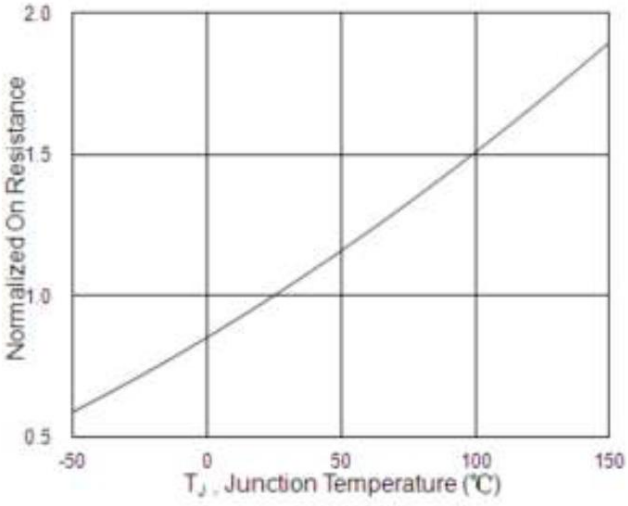


Fig.6 Normalized R_{DSON} vs. T_J

Typical Characteristics T_J = 25ºC, unless otherwise noted

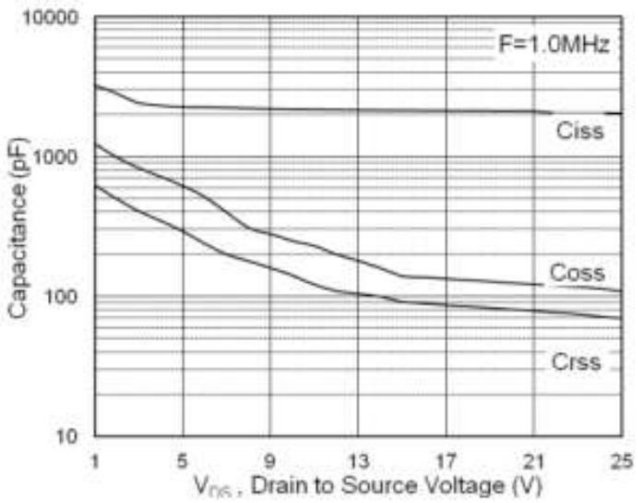


Fig.7 Capacitance

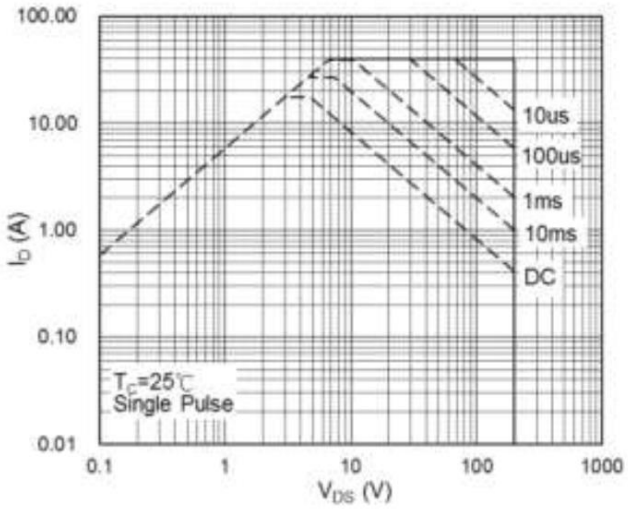


Fig.8 Safe Operating Area

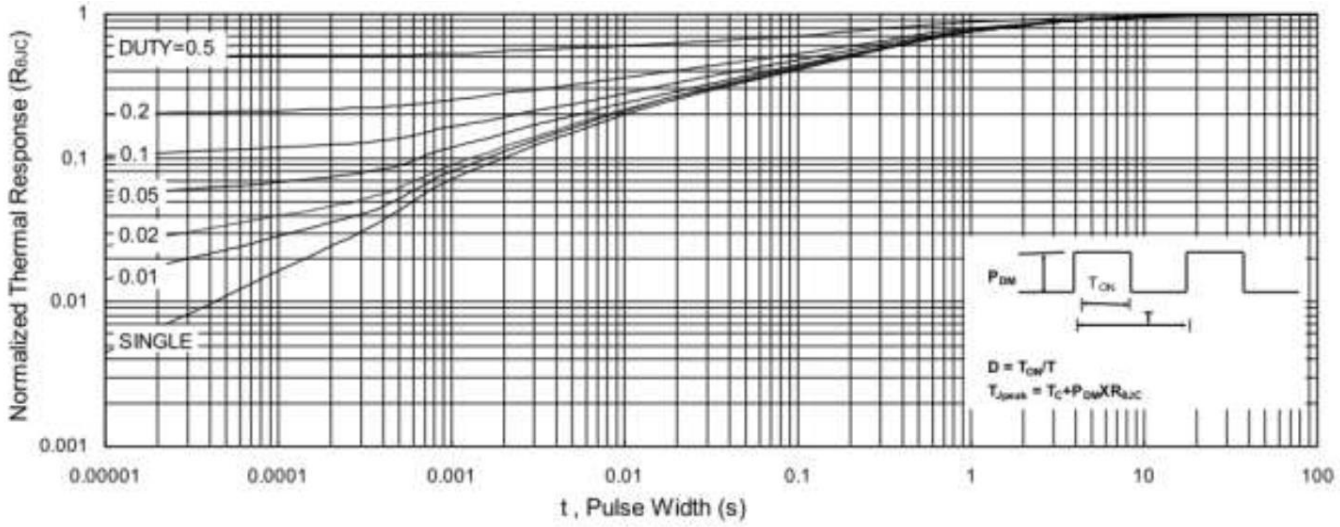


Fig.9 Normalized Maximum Transient Thermal Impedance

Figure A：Gate Charge Test Circuit and Waveform

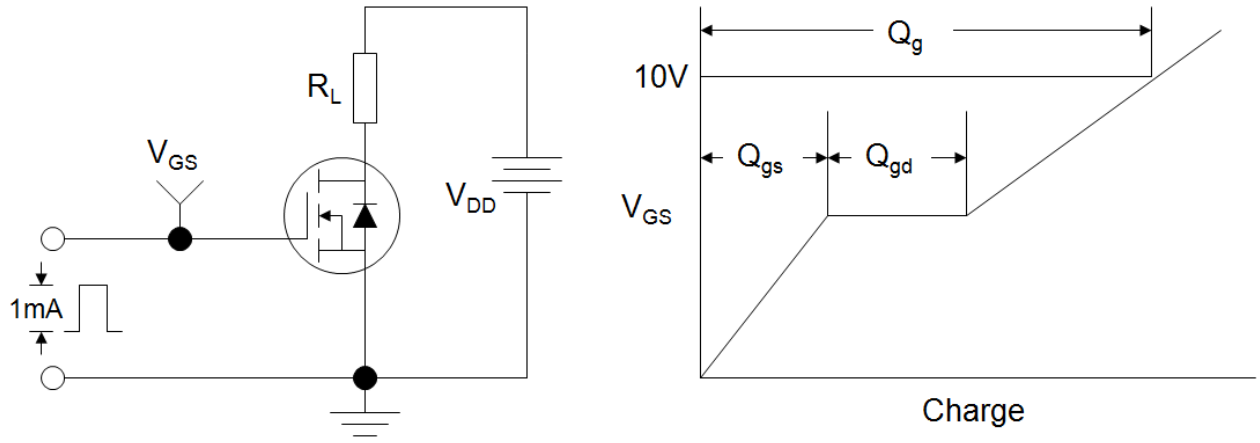


Figure B：Resistive Switching Test Circuit and Waveform

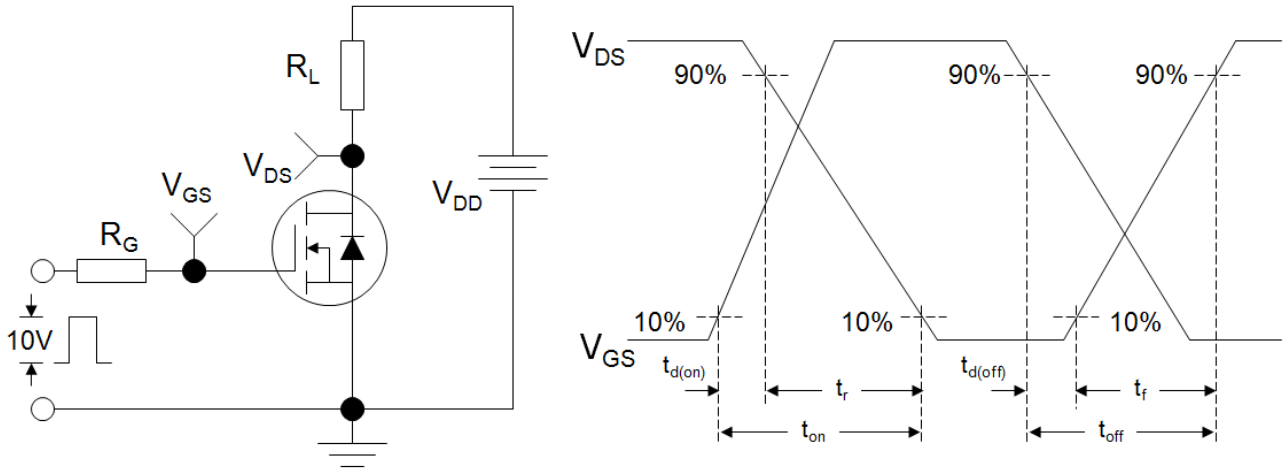
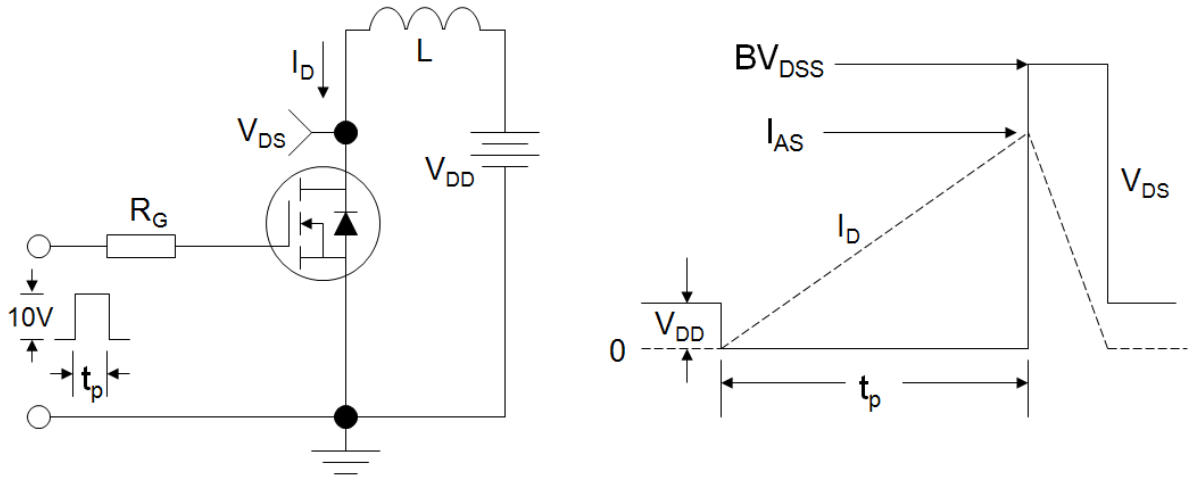


Figure C：Unclamped Inductive Switching Test Circuit and Waveform



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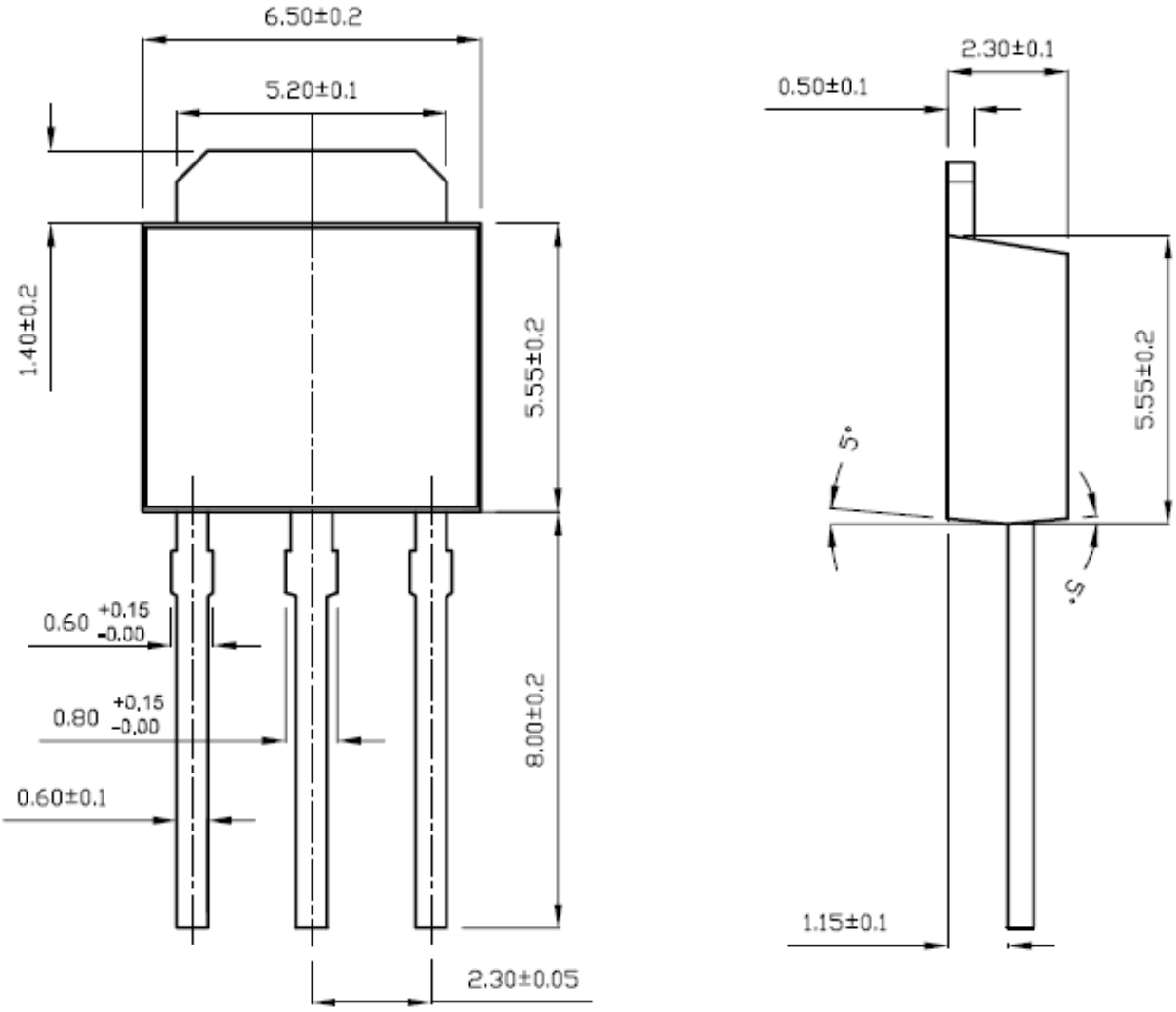
6.50±0.2
5.20±0.1
1.40±0.2
5.55±0.2
0.60 +0.15 -0.00
0.80 +0.15 -0.00
0.60±0.1
8.00±0.2
2.30±0.05
2.30±0.1
0.50±0.1
5°
5°
5.55±0.2
1.15±0.1

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