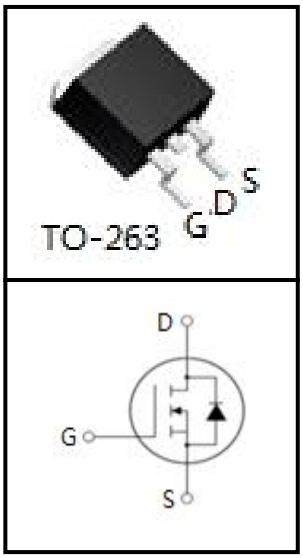
80V N-Channel Split Gate MOSFET

FEATURES

- Trench Power MOSFET Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Optimized For Fast-switching Applications



APPLICATIONS

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

RoHS

Device Marking and Package Information

Device	Package	Marking
CSB08N6P5	TO-263	CSB08N6P5

Absolute Maximum Ratings at T_j= 25ºC unless otherwise noted

Parameter		Symbol	Value	Unit
Drain-Source Voltage (V_{GS} = 0V)		V_{DSS}	80	V
Continuous Drain Current T_C = 25ºC	(note1)	I_D	130	A
Continuous Drain Current T_C = 100ºC	(note1)		100	A
Pulsed Drain Current	(note2)	I_{DM}	280	A
Gate Source Voltage		V_{GSS}	±20	V
Single Pulse Avalanche Energy	(note3)	E_{AS}	100	mJ
Power Dissipation T_C = 25ºC	(note4)	P_D	56	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55~+150	ºC

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.4	ºC/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	

Electrical Characteristics T_j= 25ºC unless otherwise specified						
Parameter	**Symbol**	**Test Conditions**	**Value**			**Unit**
			Min.	**Typ.**	**Max.**	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	80	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 64V, V_{GS} = 0V, T_J = 25ºC	--	--	1	uA
		V_{DS} = 64V, V_{GS} = 0V, T_J = 100ºC	--	--	5	uA
Gate-Source Leakage	I_{GSS}	V_{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	--	2.5	V
Drain-Source On-Resistance (note2)	$R_{DS(on)}$	V_{GS} = 10V, I_D = 30A	--	4.5	6.5	mΩ
		V_{GS} = 4.5V, I_D = 20A	--	6.5	8.5	mΩ
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 40V, f = 1.0MHz	--	2900	--	pF
Output Capacitance	C_{oss}		--	420	--	
Reverse Transfer Capacitance	C_{rss}		--	40	--	
Total Gate Charge （4.5V）	Q_g	V_{DS} = 40V, I_D = 15A, V_{GS} = 10V	--	40	--	nC
Gate-Source Charge	Q_{gs}		--	7.2	--	
Gate-Drain Charge	Q_{gd}		--	6.5	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DS} = 40V, I_D = 15A ,R_G = 3Ω	--	8.3	--	ns
Turn-on Rise Time	t_r		--	4.2	--	
Turn-off Delay Time	$t_{d(off)}$		--	36	--	
Turn-off Fall Time	t_f		--	6.9	--	
Body Diode Characteristics						
Source-Drain Current(Body Diode)	I_S		--	--	130	A
Pulsed Source-Drain Current(Body Diode)	I_{SDM}		--	--	280	
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 1A, V_{GS} = 0V	--	--	1.2	V

Notes

1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width≦300us , duty cycle≦2%
3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.1mH
4. The power dissipation is limited by 175℃ junction temperature
5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics T_J = 25ºC, unless otherwise noted

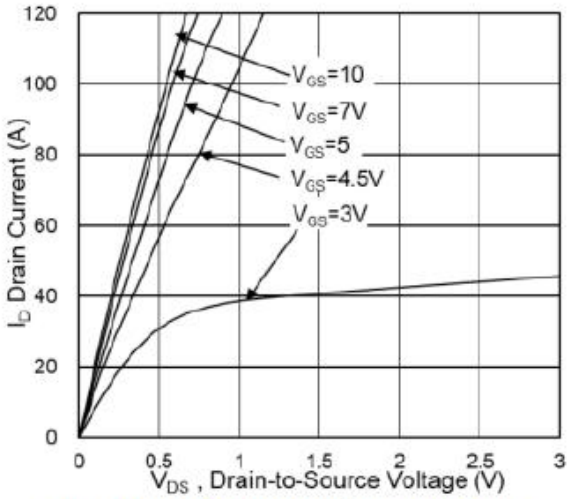


Fig.1 Typical Output Characteristics

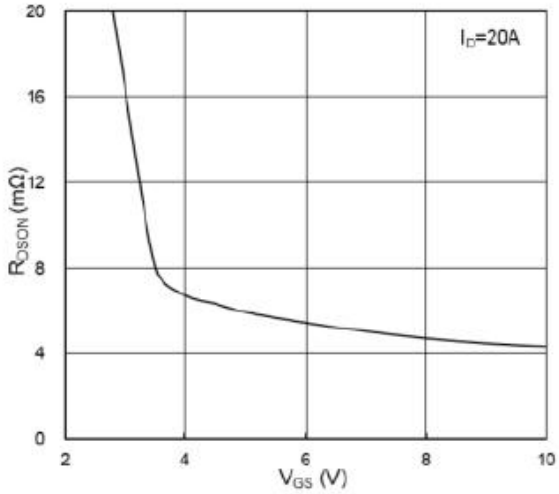


Fig.2 On-Resistance vs. G-S Voltage

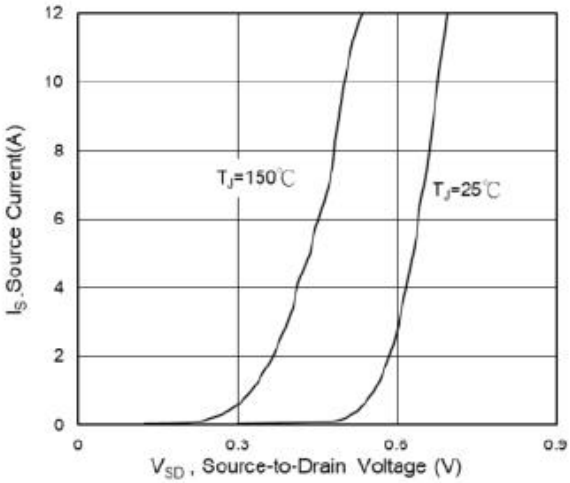


Fig.3 Source Drain Forward Characteristics

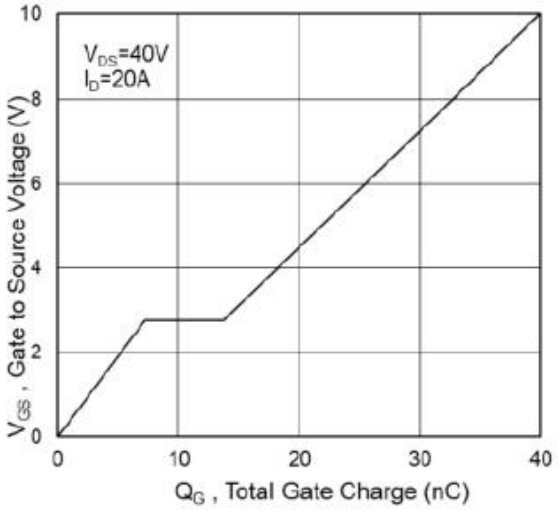


Fig.4 Gate-Charge Characteristics

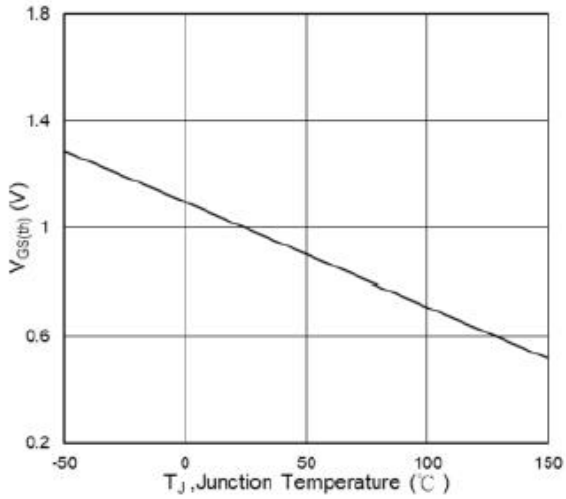


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

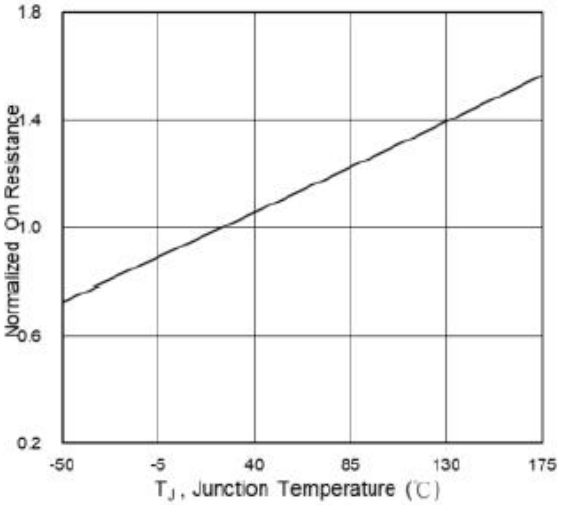


Fig.6 Normalized R_{DSON} vs. T_J

Typical Characteristics T_J = 25ºC, unless otherwise noted

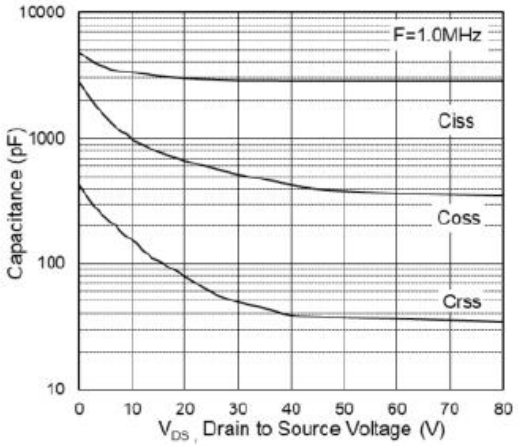


Fig.7 Capacitance

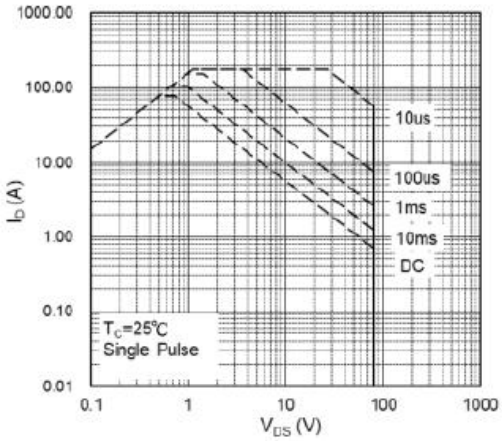


Fig.8 Safe Operating Area

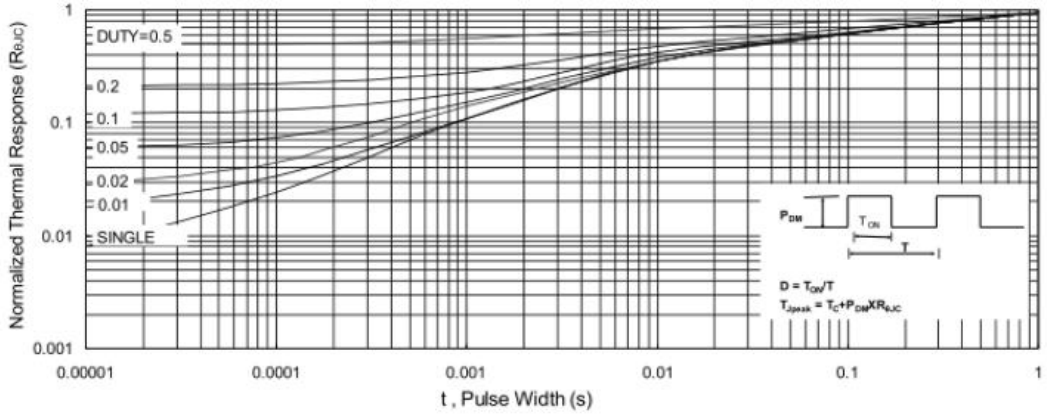


Fig.9 Normalized Maximum Transient Thermal Impedance

Figure A：Gate Charge Test Circuit and Waveform

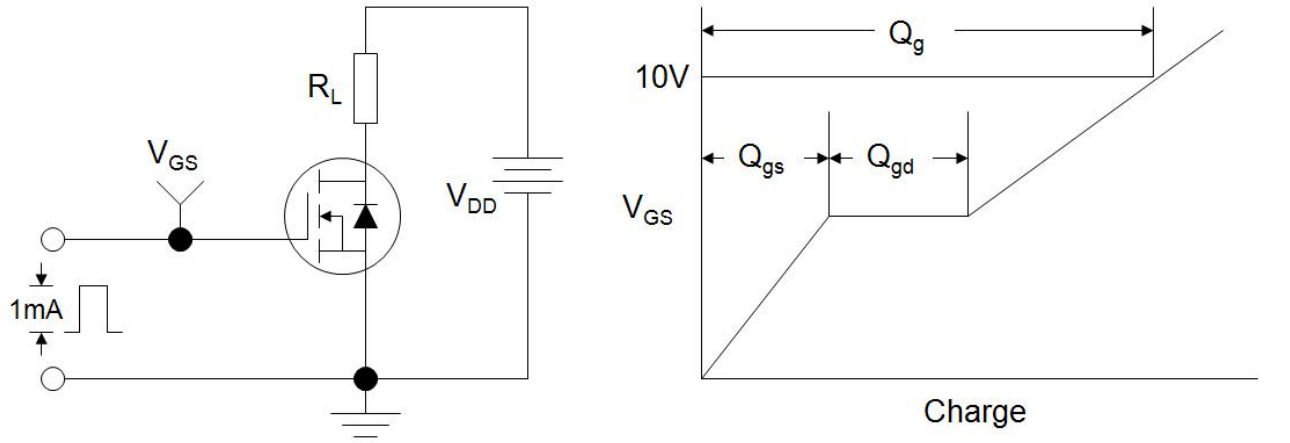


Figure B：Resistive Switching Test Circuit and Waveform

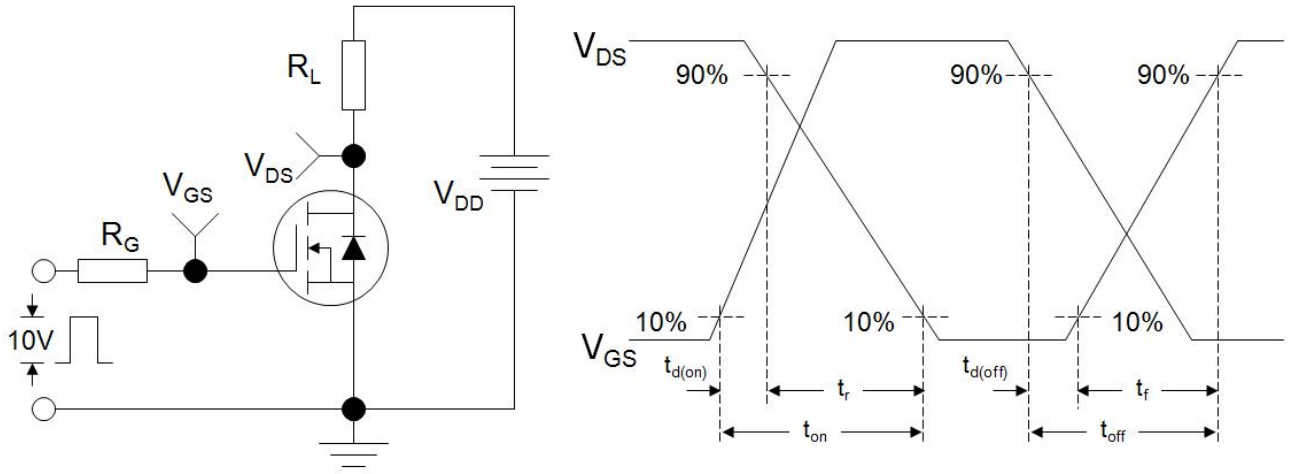
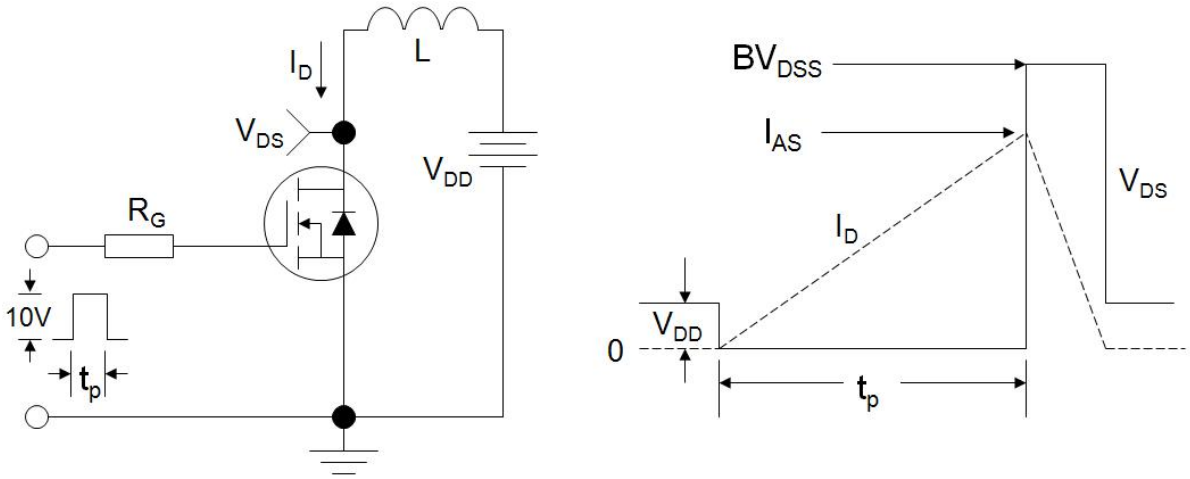


Figure C：Unclamped Inductive Switching Test Circuit and Waveform



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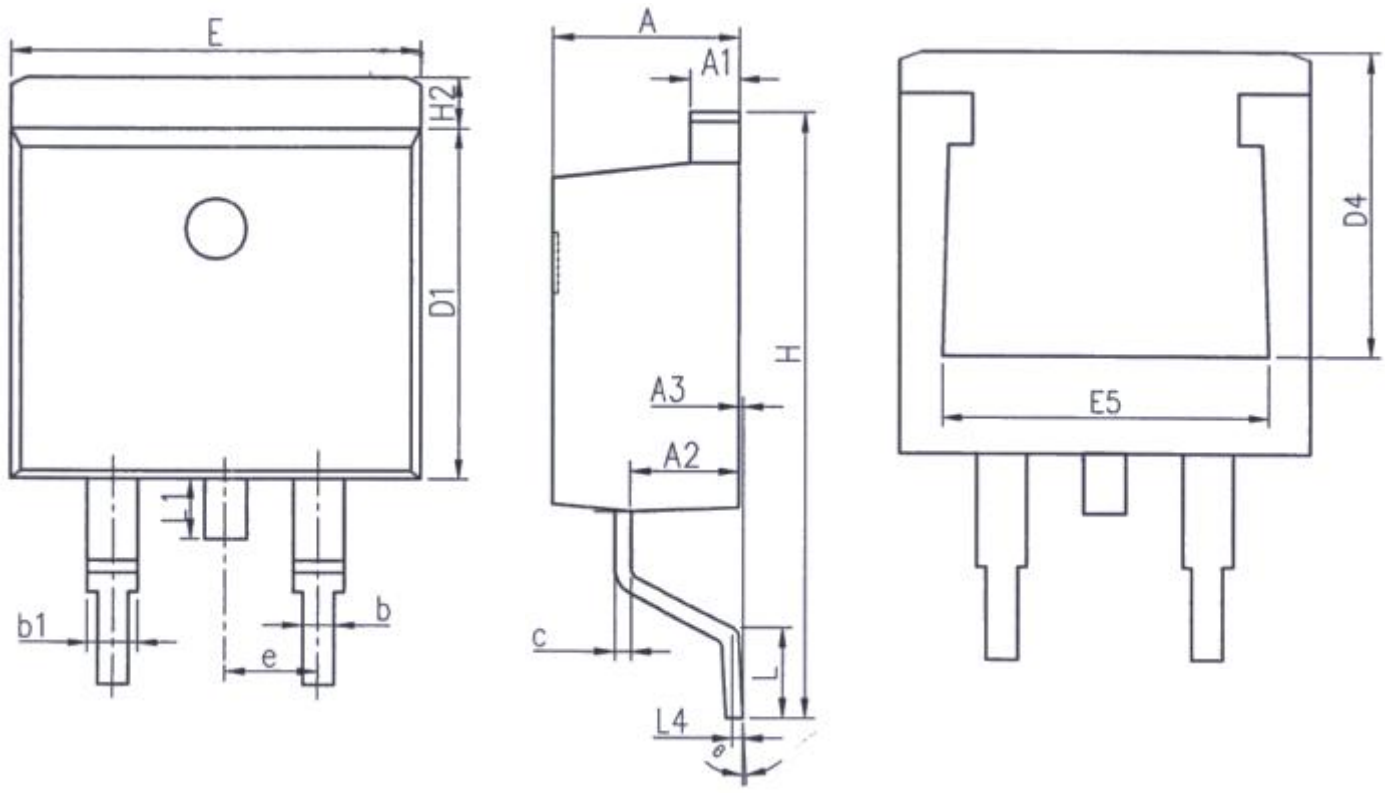

Unit: mm		
Symbol	Min.	Max.
E	9.86	10.36
E5	7.06	-
e	2.54BSC	
H	14.70	15.50
H2	1.07	1.47
L	2.00	2.60
L1	1.40	1.70
L4	0.25BSC	
θ	0°	9°

Unit: mm		
Symbol	Min.	Max.
A	4.37	4.77
A1	1.22	1.42
A2	2.49	2.89
A3	0.00	0.25
b	0.70	0.96
b1	1.17	1.47
c	0.30	0.53
D1	8.50	8.90
D4	6.60	-

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