

# 60V N-Channel Split Gate MOSFET

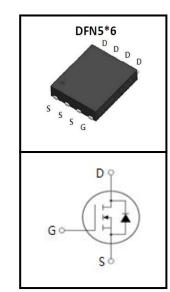
## FEATURES

- Trench Power MOSFET Technology
- Low RDS(ON)
- Low Gate Charge
- Optimized For Fast-switching Applications

## **APPLICATIONS**

- DC/DC Converter
- Ideal for high-frequency switching and

synchronous rectification



RoHS

Device Marking and Package Information				
Device	Package	Marking		
CSN06N3P6	DFN5*6	CSN06N3P6		

Absolute Maximum Ratings at T <sub>j</sub> = 25°C unless otherwise noted					
Parameter	Symbol	Value	Unit		
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	60	V		
Continuous Drain Current T <sub>C</sub> = 25°C		100	А		
Continuous Drain Current T <sub>C</sub> = 100°C	Ι <sub>D</sub>	75	А		
Pulsed Drain Current (note1)	I <sub>DM</sub>	240	А		
Gate Source Voltage	V <sub>GSS</sub>	±20	V		
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	101	mJ		
Power Dissipation $T_c = 25^{\circ}C$	P <sub>D</sub>	83	W		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C		

Thermal Characteristics			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	1.5	
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	45	°C/W
Thermal Resistance, Junction-ambient	$R_{ ext{ hetaJA}}$	55	



## CSN06N3P6

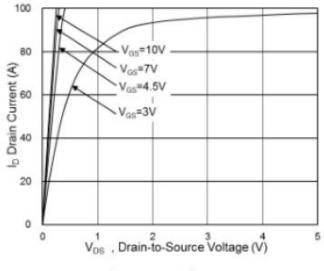
	1	nless otherwise specified	1				
Parameter	Symbol	Test Conditions	Value			Unit	
r arameter	Gymbol		Min. Typ. Max		Max.		
Static			-	-			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_{D} = 250 \mu A$	60			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C		1	uA		
Zelo Gale Voltage Drain Gallent	USS	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C			5	uA	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS}$ = $\pm 20V$			±100	nA	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.2		2.3	V	
Drain-Source On-Resistance		V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	3.0 3.6		3.6		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 15A		4.4	5.4	mΩ	
		Dynamic					
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V,		3450		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V,		1522			
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		22			
Total Gate Charge (4.5V)	Q <sub>g</sub>			58		nC	
Gate-Source Charge	$Q_gs$	$V_{DS} = 30V, I_{D} = 20A, V_{GS} = 4.5V$		16			
Gate-Drain Charge	$Q_{gd}$			4			
Turn-on Delay Time	t <sub>d(on)</sub>			18		ns	
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 30V, I <sub>D</sub> = 1A,		8			
Turn-off Delay Time	$t_{d(off)}$	$V_{GS}$ =10V, $R_{G}$ = 3.3 $\Omega$		50			
Turn-off Fall Time	t <sub>f</sub>			10.5			
	Bo	dy Diode Characteristics					
Source-Drain Current(Body Diode)	I <sub>s</sub>				100	Δ	
Pulsed Source Curren	I <sub>SDM</sub>	$V_{G=}V_{D} = 0V$ , Force Current			240	A	
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25°C, I <sub>SD</sub> = 1A, V <sub>GS</sub> = 0V			1.2	V	

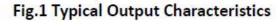
#### Notes

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  ${\leq}300 \text{us}$  , duty cycle  ${\leq}2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.5mH
- 4. The power dissipation is limited by 175°C junction temperature
- 5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted





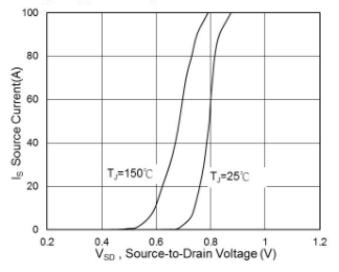


Fig.3 Forward Characteristics of Reverse diode

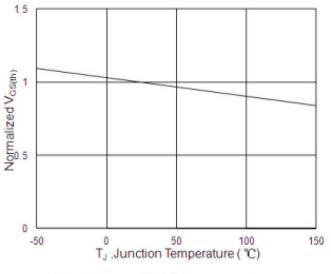


Fig.5 Normalized V<sub>GS(th)</sub> v.s T<sub>J</sub>

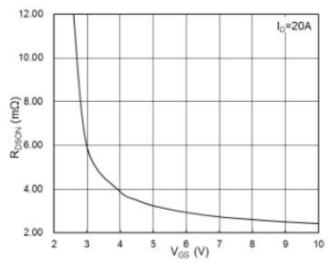


Fig.2 On-Resistance v.s Gate-Source

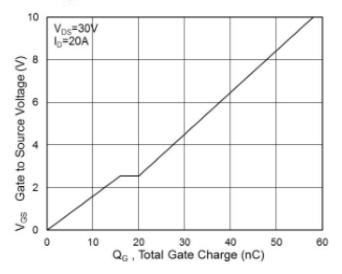


Fig.4 Gate-Charge Characteristics

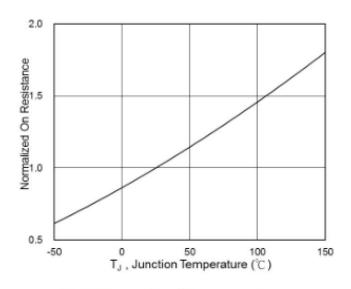


Fig.6 Normalized RDSON v.s TJ



## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

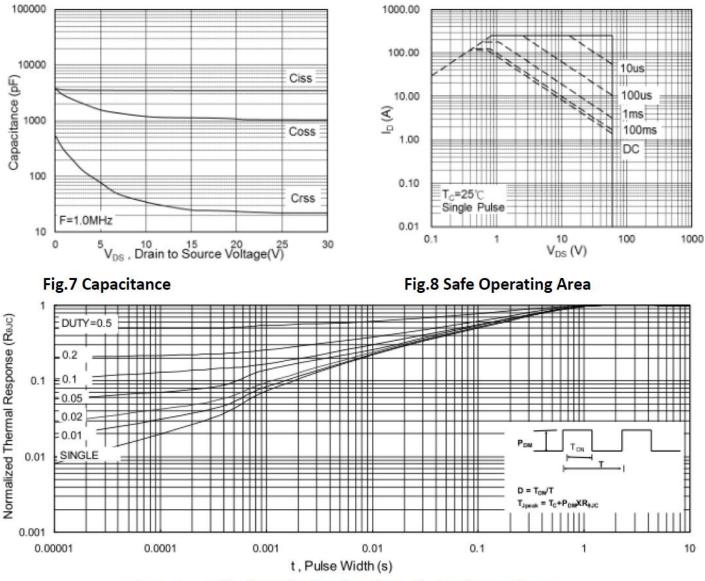


Fig.9 Normalized Maximum Transient Thermal Impedance





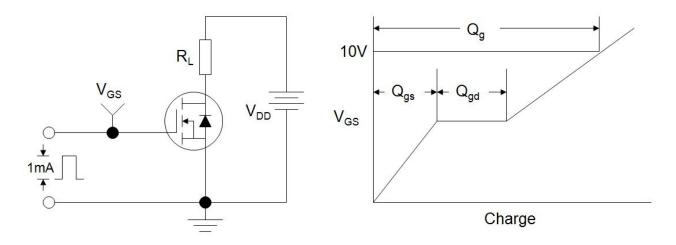


Figure B: Resistive Switching Test Circuit and Waveform

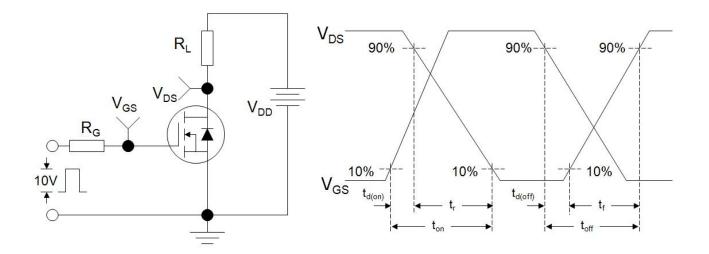
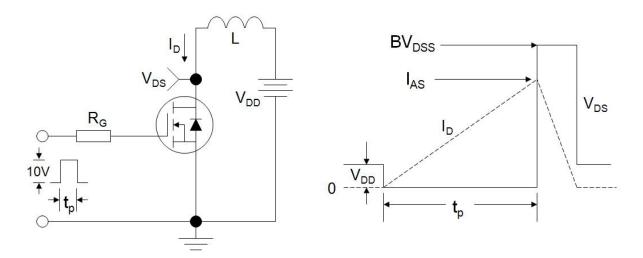


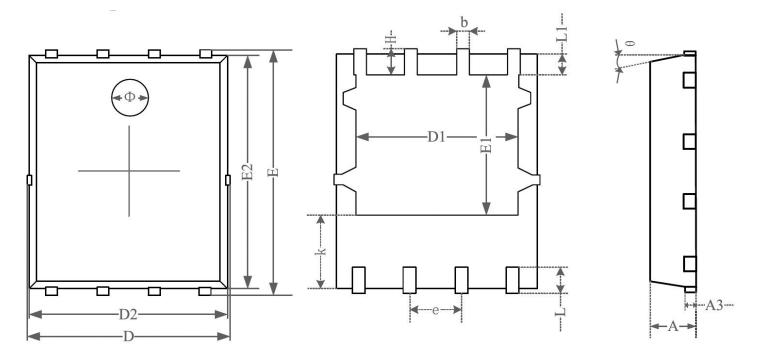
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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# **DFN5\*6**



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
А	0.870	0.900	0.930	0.034	0.035	0.036
A3	0.152REF.		0.006REF.			
D	4.944	5.020	5.096	0.195	0.198	0.201
Е	5.974	6.050	6.126	0.235	0.238	0.241
D1	3.910	4.010	4.110	0.154	0.158	0.162
E1	3.375	3.475	3.575	0.133	0.137	0.141
D2	4.870	4.900	4.930	0.192	0.193	0.194
E2	5.720	5.750	5.780	0.226	0.227	0.228
k	1.190	1.290	1.390	0.047	0.051	0.055
b	0.350	0.380	0.410	0.014	0.015	0.016
e	1.270TYP.				0.050TYP.	25
L	0.559	0.635	0.711	0.022	0.025	0.028
Ll	0.424	0.500	0.576	0.017	0.020	0.023
Н	0.574	0.650	0.726	0.023	0.026	0.029
θ	10°	11°	12 °	10°	11°	12°
Φ	1.150	1.200	1.250	0.045	0.047	0.049

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