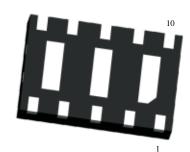


Low Capacitance ESD TVS Array in DFN3020

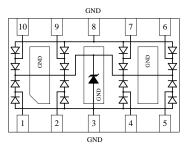
Features

- 1000Watts peak pulse power (tp = 8/20µs)
- DFN3020 package
- Unidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (1.7pF typical I/O to I/O)
- ESD Protection for high-speed data lines to: IEC 61000-4-2 ±30KV contact ±30KV air IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 40A (8/20µs)



Mechanical Data

- Case: DFN3020 (plastic package). Lead free; RoHS compliant
- Molding Compound Flammability Rating: UL 94 V-0
- **Terminals:** High temperature soldering guaranteed: 260 °C/10 sec. at terminals



Applications

- 10/100/1000M Ethernet Ports
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- Cellular Phones
- Switching Systems
- Audio/Video Inputs

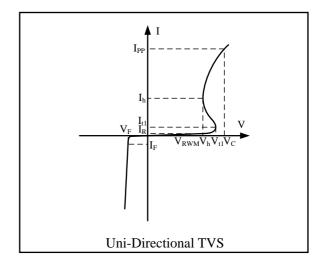
Absolute Maximum Ratings

Ratings at 25 °C, ambient temperature unless otherwise specified

| Parameter | Symbol | Value | Unit |
|---|------------------|-------------|------------|
| Peak Pulse Power (T _P =8/20µS) | P _{PP} | 1000 | W |
| ESD contact/air discharge (IEC-61000-4-2) | V _{ESD} | 30/30 | kV |
| Peak Pulse Current (t₂ = 8/20μS) | I _{PP} | 40 | А |
| Junction Temperature | TJ | -55 to +125 | $^{\circ}$ |
| Storage temperature | T _{STG} | -55 to +150 | $^{\circ}$ |



| Symbol | Parameter | |
|-------------|---|--|
| V_{RWM} | Nominal Reverse Working Voltage | |
| I_R | I _R Reverse Leakage Current @ V _{RWM} | |
| V_{t1} | V _{t1} Trigger Voltage | |
| I_{t1} | Trigger Current @ V _{t1} | |
| V_h | V _h Holding Voltage | |
| I_h | I _h Holding Current @ V _h | |
| $V_{\rm C}$ | V _C Clamping Voltage @ I _{PP} | |
| I_{PP} | I _{PP} Maximum Peak Pulse Current | |
| C_{ESD} | C _{ESD} Parasitic Capacitance | |



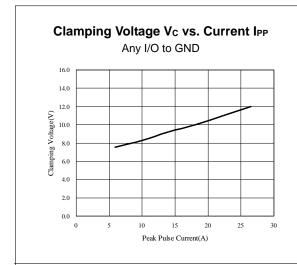
Electrical Characteristics

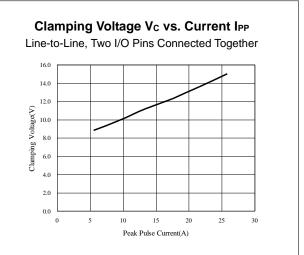
(T_A = 25 °C unless otherwise specified)

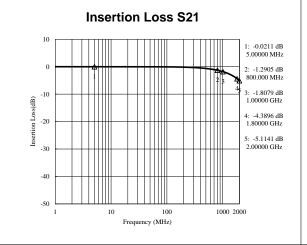
| Symbol | Test Condition | Minimum | Typical | Maximum | Units |
|------------------|---|---------|---------|---------|-------|
| $V_{ m RWM}$ | | | | 3.3 | V |
| I_R | $V_{RWM} = 3.3V, T = 25^{\circ}C$ | | 0.1 | 1.0 | μΑ |
| V_{t1} | $I_{t1}=1\mu A$ | 3.8 | 4.5 | 5.5 | V |
| $V_{\rm h}$ | $I_h = 1 mA$ | 3.5 | | 5.5 | V |
| V _C | Any I/O to Ground $I_{PP} = 1A$, $t_p = 8/20\mu s$ | | | 5.5 | V |
| $V_{\rm C}$ | Any I/O to Ground $I_{PP} = 10A, t_p = 8/20\mu s$ Any I/O to Ground | | | 10.5 | V |
| $V_{\rm C}$ | | | | 18.0 | V |
| $V_{\rm C}$ | Line-to-Line / Line-to-GND, two I/O Pins connected together on each line $I_{PP}=40A,t_p=8/20\mu s$ | | | 25.0 | V |
| C _{ESD} | Between I/O Pins and Ground $V_R = 0V$, $f = 1MHz$ | | 3.8 | 5.0 | pF |
| C_{ESD} | Between I/O Pins $V_R = 0V$, $f = 1MHz$ | | 1.7 | 2.5 | pF |

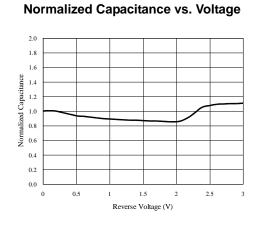


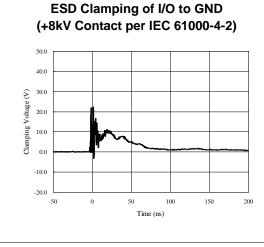
Typical Characteristics ($T_{amb} = 25 \, ^{\circ}\text{C}$ unless otherwise specified)

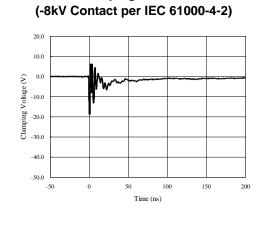






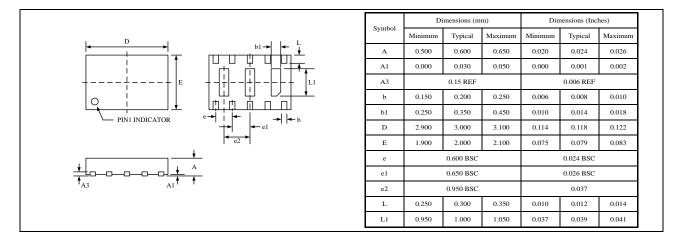




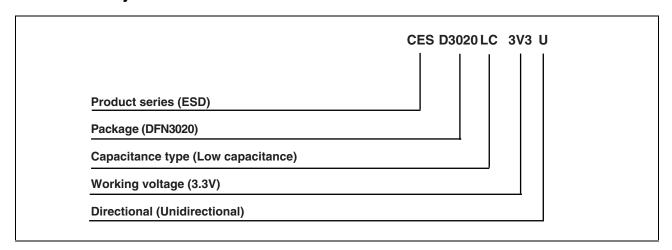


ESD Clamping of I/O to GND

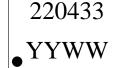
Package Dimensions



Part number system



Marking



Note:

- (1) "220433" is the part number, fixed.
- (2) "YYWW" is date code. "YY" is year (2013 is "13"); while "WW" is the assembly week in a year.

Ordering inormation

| Order code | Package | Packaging option | Base quantity | Packaging specification |
|----------------|---------|------------------|----------------|-------------------------|
| CESD3020LC3V3U | DFN3020 | Tape and reel | 3000pcs / reel | EIA STD RS-481 |

Reision history

| Date | Revision | Changes |
|-------------|----------|-----------------|
| 23-May-2012 | 1.0 | Initial release |



CESD3020LC3V3U

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