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# CESD1006LC5VBL-M

#### **CREATEK Microelectronics**

## Low Capacitance ESD Protection Diode in DFN1006

#### **Features**

- 40Watts peak pulse power (tp =  $8/20\mu s$ ) .
- Tiny DFN1006 package ÷.
- **Bidirectional configurations**
- Solid-state silicon-avalanche technology ÷.
- Low clamping voltage н.
- Low leakage current н.
- Low capacitance (Cj=2.7pF typ.) н.
- Protection one data/power line to:
- IEC 61000-4-2 ±10kV contact ±15kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns) ×.
- IEC 61000-4-5 (Lightning) 3.5A (8/20µs ÷.

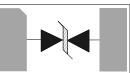
#### **Applications**

- Cell Phone Handsets and Accessories .
- Microprocessor based equipment ÷.
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers ÷.
- Portable Instrumentation

#### **Mechanical Data**

- DFN1006 package ÷
- Molding compound flammability rating: UL 94V-0 .
- Packaging: Tape and Reel
- **RoHS/WEEE Compliant**





**DFN1006** 



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#### **Absolute Maximum Rating**

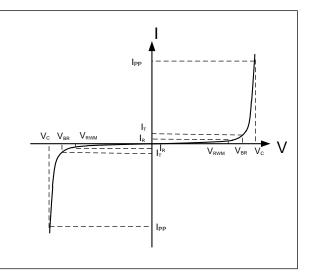
Rating	Symbol	Value	Units
Peak Pulse Power ( t <sub>p</sub> =8/20µs )	P <sub>PP</sub>	40	Watts
Peak Pulse Current ( $t_p = 8/20\mu s$ ) (note1)	I <sub>pp</sub>	3.5	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	15 10	kV
Lead Soldering Temperature	TL	260(10seconds)	°C
Junction Temperature	TJ	-55 to + 125	°C
Storage Temperature	T <sub>stg</sub>	-55 to + 125	°C

#### **Electrical Characteristics**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{RWM}$				5.0	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>T</sub> =1mA	5.5	6.5		V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =5V,T=25℃		0.1	0.5	μA
Peak Pulse Current	I <sub>PP</sub>	tp =8/20µs			3.5	А
Clamping Voltage	Vc	I <sub>PP</sub> =3.5A,t <sub>p</sub> =8/20μs			11	V
Junction Capacitance	Cj	V <sub>R</sub> = 0V, f = 1MHz		2.7	3.5	pF

### Electrical Parameters (TA = 25°C unless otherwise noted)

Symbol	Parameter
IPP	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
VRWM	Working Peak Reverse Voltage
IR	Maximum Reverse Leakage Current @ VRWM
VBR	Breakdown Voltage @ I⊤
Iτ	Test Current



Note:. 8/20µs pulse waveform.

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#### **Typical Characteristics**

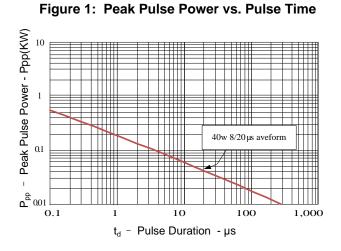
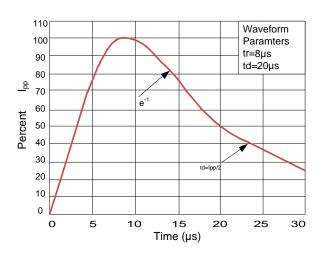


Figure3: Pulse Waveform



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Figure 2: Power Derating Curve

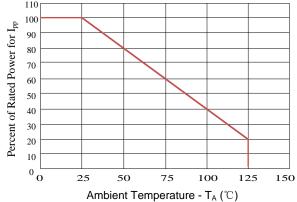
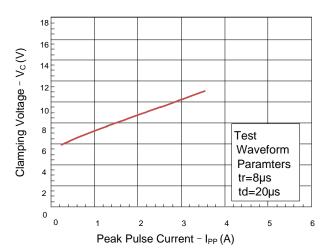


Figure 4: Clamping Voltage vs.lpp



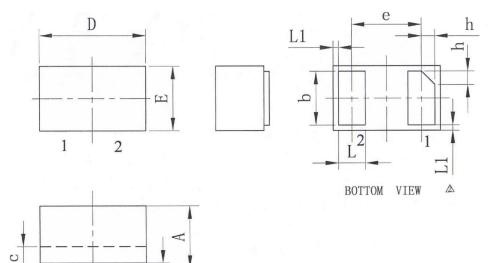
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## CESD1006LC5VBL-M

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## Outline Drawing – DFN1006

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SYMBOL	MILLIMETER			
SYMBOL	MIN	NOM	MAX	
А	0.45	0.50	0.55	
A1	0	0.02	0.05	
b	0.45	0.50	0.55	
с	0.12	0.15	0.18	
D	0.95	1.00	1.05	
e	0.65BSC			
Е	0.55	0.60	0.65	
L	0.20	0.25	0.30	
L1	0.05REF			
h	0.07	0.12	0.17	
载体尺寸 (Mil)	20*20			

## Marking



### **Ordering information**

Order code	Package	Base qty	Delivery mode
CESD1006LC5VBL-M	DFN1006	10k	Tape and reel

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