

SBR1045D1

#### 10A SBR<sup>®</sup> SUPER BARRIER RECTIFIER

### Features

- Low Forward Voltage Drop
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

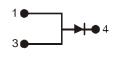
#### **Mechanical Data**

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- Case: TO252 (DPAK)
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 ®
- Weight: 0.33 grams (approximate)



Top View



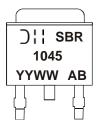
Polarity

# Ordering Information (Note 2)

| Part Number   | Qualification | Case         | Packaging                 |
|---------------|---------------|--------------|---------------------------|
| SBR1045D1-13  | Commercial    | TO252 (DPAK) | 2500/Tape & Reel, 13-inch |
| SBR1045D1Q-13 | Automotive    | TO252 (DPAK) | 2500/Tape & Reel, 13-inch |

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes. 2. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



SBR1045 = Product Type Marking Code AB = Foundry and Assembly Code YYWW = Date Code Marking YY = Last two digits of year (ex: 08 = 2008) WW = Week (01 - 53)



#### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

| Characteristic  | Symbol  | Value | Unit |
|---|---|-------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage              | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>RM</sub> | 45    | V    |
| RMS Reverse Voltage   | V <sub>R(RMS)</sub>                                     | 32    | V    |
| Average Rectified Output Current @ T <sub>C</sub> = 140°C   | lo  | 10    | A    |
| Non-Repetitive Peak Forward Surge Current 8.3ms<br>Single Half Sine-Wave Superimposed on Rated Load | I <sub>FSM</sub>  | 90    | А    |
| Repetitive Peak Avalanche Power (1µs, 25°C)   | P <sub>ARM</sub>  | 5000  | W    |

# **Thermal Characteristics**

| Characteristic  | Symbol                               | Value       | Unit |
|---|--------------------------------------|-------------|------|
| Maximum Thermal Resistance<br>Thermal Resistance Junction to Ambient (Note 3)<br>Thermal Resistance Junction to Case (Note 3) | R <sub>θJA</sub><br>R <sub>θJC</sub> | 29<br>3     | °C/W |
| Operating and Storage Temperature Range   | T <sub>J</sub> , T <sub>STG</sub>    | -65 to +150 | °C   |

# **Electrical Characteristics** $@T_A = 25^{\circ}C$ unless otherwise specified

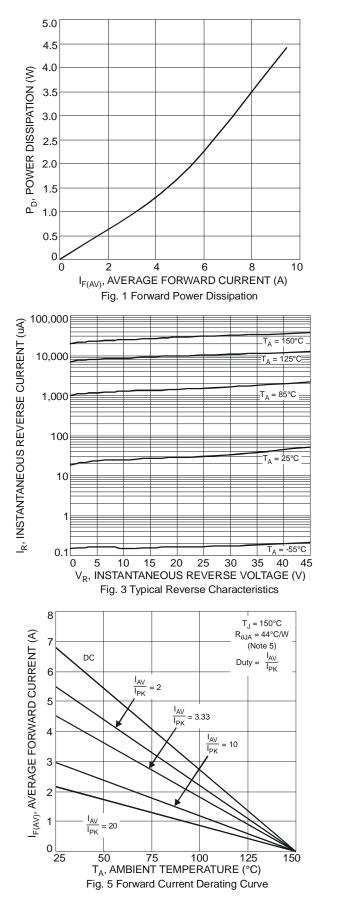
| Characteristic                     | Symbol             | Min         | Тур                       | Max                          | Unit     | Test Condition   |
|------------------------------------|--------------------|-------------|---------------------------|------------------------------|----------|--|
| Reverse Breakdown Voltage (Note 4) | V <sub>(BR)R</sub> | 45          | -                         | -                            | V        | I <sub>R</sub> = 0.45mA  |
| Forward Voltage Drop (per leg)     | VF                 | -<br>-<br>- | 0.42<br>0.37<br>-<br>0.50 | 0.48<br>0.41<br>0.58<br>0.56 | v        | $\begin{split} I_{F} &= 5A, \ T_{J} = 25^{\circ}C \\ I_{F} &= 5A, \ T_{J} = 125^{\circ}C \\ I_{F} &= 10A, \ T_{J} = 25^{\circ}C \\ I_{F} &= 10A, \ T_{J} = 125^{\circ}C \end{split}$ |
| Leakage Current (Note 4)           | I <sub>R</sub>     | -           | 50<br>12                  | 500<br>40                    | μA<br>mA | $V_R = 45V, T_J = 25^{\circ}C$<br>$V_R = 45V, T_J = 125^{\circ}C$  |
| Total Capacitance                  | CT                 | -           | 400                       | -                            | pF       | V <sub>R</sub> = 5V, f = 1MHz<br>T <sub>J</sub> = 25⁰C   |

3. Device mounted on polymide substrate, 240mm<sup>2</sup> Copper pad, double-sided PC Board. Notes:

Short duration pulse test used to minimize self-heating effect.
Device mounted on polymide substrate, 2" \* 2" Copper pad, double-sided PC Board with minimum recommended pad layout.







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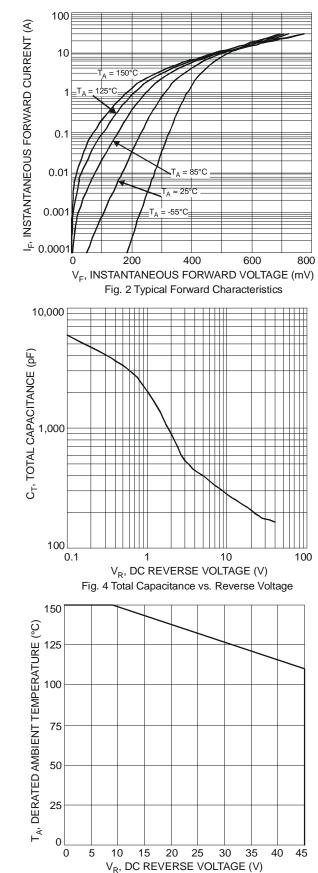


Fig. 6 Operating Temperature Derating





Тур

2.29

0.08

1.07

0.783

0.95

5.33

6.10

\_ 2.286

6.58

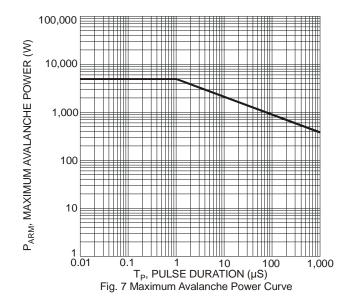
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9.91

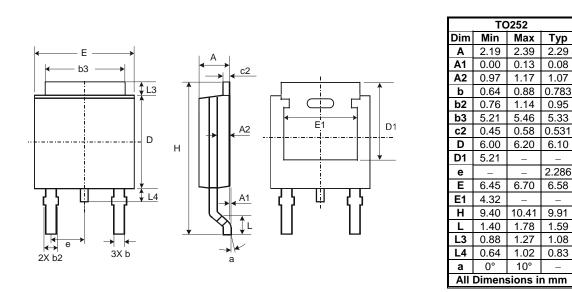
1.59

1.08

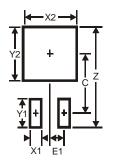
0.83



# **Package Outline Dimensions**



### **Suggested Pad Layout**



| Dimensions | Value (in mm) |
|------------|---------------|
| Z          | 11.6          |
| X1         | 1.5           |
| X2         | 7.0           |
| Y1         | 2.5           |
| Y2         | 7.0           |
| С          | 6.9           |
| E1         | 2.3           |

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