

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期): 2019-7-12

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GF 25V10μF(φ5x11)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLIER								
PREPARED (拟定)	CHECKED (审核)							
郭继伟	刘渭清							

CUSTOMER								
APPROVAL (批准)	SIGNATURE (签名)							

ELECTROLYTIC CAPACITOR SPECIFICATION GF SERIES

	SPECIFICATION				ALTERN	IATION HIS RECORDS	STORY
		GF SERIE		,			
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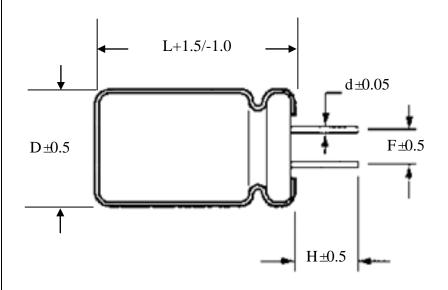
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Unit: mm

Table 1 Product Dimensions and Characteristics



Shape Code	D	5
Shape Code	L	11
	F	2.0
CB Type	Н	3.5
	d	0.5

N o.	SAMXON Part No.	WV (Vdc)	Cap. (μF)	Cap. tolerance	Temp. range($^{\circ}\mathbb{C}$)	tan ō (120Hz, 20℃)	Leakage Current (µA,2min)	Max Ripple Current at 105℃ 100kHz (mA rms)	Impedance at 20°C 100kHz (Ωmax)	Load lifetime (Hrs)		nsion nm) F	фd	Sleeve
1	EGF106M1ED11CBSHP-RR	25	10	-20%~+20%	-40~105	0.14	3	180	0.7	2000	5X11	2.0	0.5	PET

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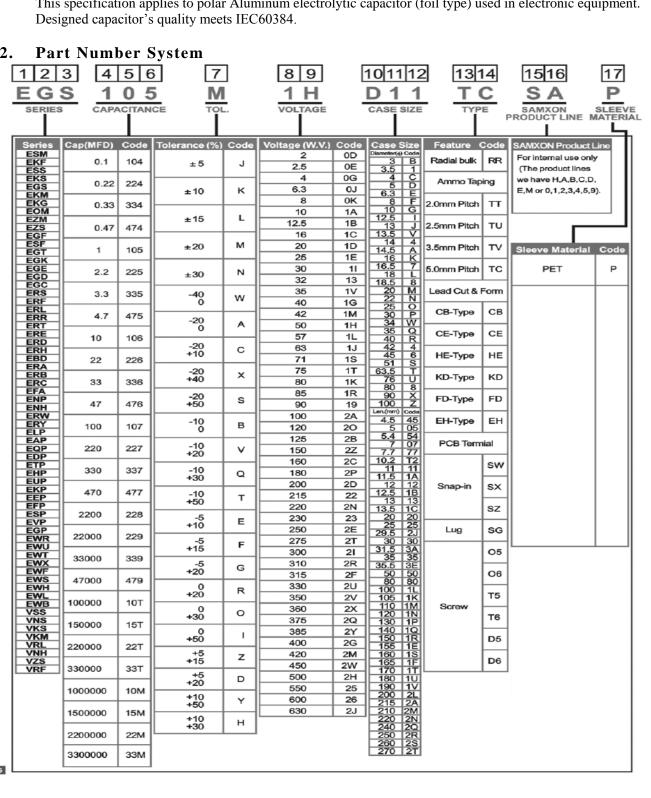
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1. **Application**

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment.



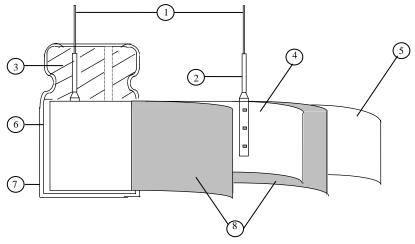
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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature : $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PERFC	RMANC	Έ			
	Rated voltage									
	(WV)	WV (V.DC)	6.3	10	16	25	35	50	63	100
4.1		SV (V.DC)	8	13	20	32	44	63	79	125
	Surge voltage (SV)									
4.2	Nominal capacitance (Tolerance)	<pre><condition> Measuring F Measuring V Measuring T </condition></pre> <pre><criteria></criteria></pre> Shall be withing	oltage emperat	: N ture : 20)±2℃	than 0.5V				
4.3	Leakage current	<condition> Connecting the minutes, and <criteria> Refer to Table</criteria></condition>	then, me				istor (1	kΩ ±10	DΩ) in s	eries for
4.4	tanδ	<condition> See 4.2, Norm Capacitance, for measuring frequency, voltage and temperature. <criteria> Refer to Table 1</criteria></condition>								
4.5	Terminal strength		ength of apacitor ength of pacitor, 2~3 second er of lea	f Termina applied f onds, and d wire	force to als. Force to be then ber	ent the tent it for 90 ille force 1 (kgf) 5 (0.51)	rminal (0° to its	1~4 mm foriginal properties (kg 2.5 (c)	from the position version (force N gf) 0.25)	rubber) f
		Over 0.: <criteria no="" notice<="" td=""><td>a></td><td></td><td>1</td><td>0 (1.0)</td><td>reakage</td><td>·</td><td>ness at the</td><td>e termina</td></criteria>	a>		1	0 (1.0)	reakage	·	ness at the	e termina

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		<condition></condition>								
			Testing	2 Tempe	rature(°C)			Time		
		1		20 ± 2		-	to reach		equilibrii	ım
4.6 chara		2		40(-25)	+3		to reach		· -	
		3		20 ± 2		-	to reach		-	
		4		$\frac{20\pm 2}{105\pm 2}$			to reach		_	
						_	to reach		-	
				20 - 2		Time	to reach	<u> </u>	equinori	4111
			withir	n the lim	it of Item	4 4The 16	eakage cr	irrent me	easured s	hall not
							ouruge et	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		11411 1100
4.5	Temperature			-		it of Iter	n 4.4The	leakage	current	shall not
4.6		_								
4.6	cs	c. At-40°C (-25°	C), im	pedance	(z) ratio s	hall not	exceed th	e value	of the fol	lowing
		table.			,		•		T	T
		Working Voltage	(V)	6.3	10	16	25	35	50	63
		Z-25°C/Z+20°C	C	4	3	2	2	2	2	2
		Z-40°C/Z+20°C	\mathbb{C}	8	6	4	3	3	3	3
		Working Voltage	(V)	100						
					 		than 1000) E for	7 25/7	20°C
		Tor capacitance v			H Add II					
			aruc >	· 1000µ		-		-		
		Capacitance, tanδ		-	Add 1.0	per anot	her 1000	μ F for Z		
				-	Add 1.0	per anot	her 1000	μ F for Z		
		<condition></condition>	, and	impedan	Add 1.0 ace shall b	per anot e measur	ther 1000 ed at 120	μ F for Z Hz.	Z-40°C/Z	Z+20℃.
		<condition> According to IEC</condition>	, and	impedan	Add 1.0 ace shall b	per anote measures, The ca	ther 1000 ed at 120 pacitor is	Pµ F for Z OHz.	Z-40°C/Z	Z+20°C.
		<condition> According to IEC 105 ℃ ±2 with □</condition>	, and 200384 C60384 DC bias	impedan -4No.4.1	Add 1.0 ace shall be a	per anote measures, The carated ripp	ther 1000 ed at 120 pacitor is	PHZ. S stored at for Tab	Z-40°C/Z	Z+20°C. erature of the sum of
		Condition> According to IEC 105 ℃ ±2 with □ DC and ripple poproduct should be	, and C60384 OC biastes were tested	impedan -4No.4.1 s voltage bltage sh	Add 1.0 ace shall be a shall be a shall be a shall not explus the reall not explusive thours recommend to the shall be a shall not explusive the shall not explusive the shall not explusive the shall be a shall	b per anote e measur ds, The ca ated ripp	ther 1000 ed at 120 spacitor is le current	Pµ F for Z DHz. S stored a t for Tab Yorking v	Z-40°C/Z at a tempole 1. (The collage)	Z+20°C. erature of the sum of then the
characteristi cs cs characteristi cs cs characteristi cs cs characteristi cs cs cs cs cs cs cs cs cs cs	b per anote e measur ds, The ca ated ripp	ther 1000 ed at 120 spacitor is le current	Pµ F for Z DHz. S stored a t for Tab Yorking v	Z-40°C/Z at a tempole 1. (The collage)	Z+20°C. erature of the sum of then the					
		Condition> According to IEC 105 ℃ ±2 with □ DC and ripple populate should be result should mee Criteria>	, and C60384 OC bias eak voe tested et the fo	-4No.4.1 s voltage oltage sh l after 16 following	Add 1.0 ace shall be a shall be a shall be a shall not explain the shall not explain the shours recognized table:	b per anote e measures, The ca ated ripp acced the covering t	pacitor is le current rated which at at	Pµ F for Z DHz. S stored a t for Tab Yorking v	Z-40°C/Z at a tempole 1. (The collage)	Z+20°C. erature of the sum of then the
4.7	life	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple popoduct should be result should mee Criteria> The characteristic	, and C60384 OC bias eak voe tested et the focus on the control of	-4No.4.1 s voltage oltage sh l after 16 following	Add 1.0 ace shall be a shall be a shall not explus the real not explain the shours recognized table:	per anote e measur as, The ca ated ripp acced the covering to	pacitor is le current rated when the current the rated when the the current the rated when the the current the rated when the the current	pµ F for Z DHz. s stored a t for Tab yorking v mospher	Z-40°C/Z at a tempole 1. (Tild voltage)	Z+20°C. erature of the sum of then the
4.7	life	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple p product should be result should mee <criteria> The characteristic Leakage company and the characteristic</criteria>	, and C60384 OC bias eak voce tested et the fector control of the	impedand-4No.4.1 s voltage obtage shafter 16 oblowing	Add 1.0 ace shall be a shall not explus the real not explusive table: e following Value in	per anote measures, The carated ripper acced the covering to grequire 4.3 shall	pacitor is le current rated writine at at ments.	Pµ F for Z DHz. s stored a t for Tab corking v mospher	Z-40°C/Z at a tempole 1. (Tild voltage)	Z+20°C. erature of the sum of then the
4.7	life	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple perioduct should be result should mee Criteria> The characteristic Leakage of Capacitance	, and C60384 OC bias eak voce tested et the fector control of the	impedand-4No.4.1 s voltage obtage shafter 16 oblowing	Add 1.0 ace shall be a shall not explus the real not explusive the real shours recognized the following the value in the within ± 1.0 according to the shall be a sha	b per anote measures, The capated ripp acced the covering to grequire 4.3 shall 225% of	pacitor is le current rated when the rated when the ments.	pµ F for 20 DHz. s stored a t for Tab yorking v mospher ied hlue.	Z-40°C/Z at a tempole 1. (The voltage) ic condit	Z+20°C. erature of the sum of then the
4.7	life	<condition> According to IEC 105 °C ±2 with Γ DC and ripple perioduct should be result should mee <criteria> The characteristic Leakage conditions Capacitant</criteria></condition>	, and C60384 OC bias eak voce tested et the fe c shall urrent ce Cha	impedand-4No.4.1 s voltage obtage shafter 16 oblowing	Add 1.0 ace shall be a shall not explus the real not explusive table: e following Value in Within 1.0 Not more	per anote measures, The carted ripp acced the overing to the covering to the c	pacitor is le current rated writine at at ments. be satisficinitial various of the	Pµ F for Z DHz. S stored a t for Tab corking v mospher ied alue.	Z-40°C/Z at a tempole 1. (The coltage) ic condite to ded value.	Z+20°C. erature of the sum of then the
4.7	life	<condition> According to IEC 105 °C ±2 with Γ DC and ripple perioduct should be result should mee <criteria> The characteristic Leakage conditions Capacitant</criteria></condition>	, and C60384 OC bias eak voce tested et the fe c shall urrent ce Cha	impedand-4No.4.1 s voltage obtage shafter 16 oblowing	Add 1.0 ace shall be a shall not explus the real not explusive table: e following Value in Within 1.0 Not more	per anote measures, The carted ripp acced the overing to the covering to the c	pacitor is le current rated writine at at ments. be satisficinitial various of the	Pµ F for Z DHz. S stored a t for Tab corking v mospher ied alue.	Z-40°C/Z at a tempole 1. (The coltage) ic condite to ded value.	Z+20°C. erature of the sum of then the
4.7	life	<condition> According to IEC 105 °C ±2 with Γ DC and ripple perioduct should be result should meet <criteria> The characteristic Leakage condition tanδ Appearance</criteria></condition>	, and C60384 OC bias eak voce tested et the fe c shall urrent ce Cha	impedand-4No.4.1 s voltage obtage shafter 16 oblowing	Add 1.0 ace shall be a shall not explus the real not explusive table: e following Value in Within 1.0 Not more	per anote measures, The carted ripp acced the overing to the covering to the c	pacitor is le current rated writine at at ments. be satisficinitial various of the	Pµ F for Z DHz. S stored a t for Tab corking v mospher ied alue.	Z-40°C/Z at a tempole 1. (The coltage) ic condite to ded value.	Z+20°C. erature of the sum of then the
4.7	life	<condition> According to IEC 105 ℃ ±2 with □ DC and ripple popole product should be result should mee <criteria> The characteristic Leakage condition></criteria></condition>	, and C60384 OC bias eak voce tested et the force shall urrent ce Cha	impedands-4No.4. Is voltage shall after 16 collowing meet the large	Add 1.0 ace shall be 13 method plus the real not explain the real not ex	g require 4.3 shall 25% of than 150 all be no	pacitor is le current rated when the rated when the satisficion initial value of the leakage of the current rate.	pµ F for Z DHz. S stored a t for Tab rorking v mospher ied hlue. E specifie of electro	Z-40°C/Z at a tempole 1. (The condition of the condition	erature of the sum of Then the ions. The
4.7	life	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple perioduct should be result should meet Criteria> The characteristic Leakage of Capacitand tano Appearance Condition> The capacitors are	, and C60384 OC bias eak voce tested et the fet c shall urrent ce Cha	impedanda-4No.4.1 s voltage shal after 16 ollowing meet the large tored with the large shall are the large shall are the large shall are the large shall be	Add 1.0 ace shall be a shall not explus the real not expluse the real not expluse the following table: There shall not expluse the following table: Within \(\frac{1}{2} \) There shall not expluse the following table:	g require 4.3 shall 25% of than 150 all be no	pacitor is le current e rated writer at attements. be satisficinitial value of the leakage of the data at attempts at a telegraph of the leakage of the data at a telegraph of the leakage of the data at a telegraph of the leakage of the data at a telegraph of the leakage of the data at a telegraph of the leakage of the l	Pµ F for D DHz. S stored a t for Tab corking v mospher ied alue. e specifie of electro	z-40°C/z at a tempole 1. (The coltage) ic condite to decord value. and value. and value.	±2°C for
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4.7	life	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple perioduct should be result should meet Criteria> The characteristic Leakage of Capacitand tano Appearance Condition> The capacitors are	, and C60384 OC biaseak voce tested et the force character ce character than so the control of t	impedan -4No.4.1 s voltage shal after 16 following meet the large wing this wing this day to stable to st	Add 1.0 ace shall be a specific plus the result of the shall not expected the specific plus the result of the shall not expected the specific plus the speci	g require 4.3 shall 25% of than 150 all be no	pacitor is le current rated whime at at ments. be satisficinitial various of the leakage of the	pµ F for 20 PHz. S stored at for Table Forking was mospher and the specified of electron and the specified for 4~8	z-40°C/z at a tempole 1. (The voltage) ic conditions and value.	±2°C form the test
4.7	life test	<condition> According to IEC 105 ℃ ±2 with □ DC and ripple poproduct should be result should mee <criteria> The characteristic Leakage condition> Appearance <condition> The capacitors are 1000+48/0 hours chamber and be a second to IEC.</condition></criteria></condition>	, and C60384 OC biaseak voce tested et the force chall urrent ce Chall ce et then so Follo allowered to a	impedand -4No.4.1 s voltage shall after 16 collowing meet the large wing this did to stable series 1	Add 1.0 ace shall be a specific to the shall be a shall not expended to the shall not more than a specific to the shall not not contain the shall not not contain the shall not expended the s	g require 4.3 shall 25% of than 150 all be no	pacitor is le current rated whime at at the ments. be satisficiential various of the leakage of the tors shall apperature to the current rate of	pµ F for 20 PHz. S stored at t for Tab Porking was mospher with a specific properties of electrons at the specific properties of	at a tempole 1. (The voltage) ic conditions of the conditions of t	±2°C form the test Next they d voltage
	life test Shelf	Condition> According to IEC 105 ℃ ±2 with □ DC and ripple periodict should be result should meet Criteria> The characteristic Leakage of Capacitand tano Appearance Condition> The capacitors are 1000+48/0 hours chamber and be a shall be connected	, and C60384 OC biaseak voce tested et the force chall urrent ce Chall ce et then so Follo allowered to a	impedand -4No.4.1 s voltage shall after 16 collowing meet the large wing this did to stable series 1	Add 1.0 ace shall be a specific to the shall be a shall not expended to the shall not more than a specific to the shall not not contain the shall not not contain the shall not expended the s	g require 4.3 shall 25% of than 150 all be no	pacitor is le current rated whime at at the ments. be satisficiential various of the leakage of the tors shall apperature to the current rate of	pµ F for 20 PHz. S stored at t for Tab Porking was mospher with a specific properties of electrons at the specific properties of	at a tempole 1. (The voltage) ic conditions of the conditions of t	±2°C form the test Next they d voltage
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	T	<criteria></criteria>
		The characteristic shall meet the following requirements.
		Leakage current Value in 4.3 shall be satisfied
	Shelf	Capacitance Change Within $\pm 25\%$ of initial value.
4.8	life	tan δ Not more than 150% of the specified value.
	test	
		Appearance There shall be no leakage of electrolyte. Remark: If the capacitors are stored more than 1 year, the leakage current may
		increase. Please apply voltage through about $1 \text{ k}\Omega$ resistor, if necessary.
4.9	Surge test	$ \begin{array}{l} \textbf{} \\ \textbf{Applied a surge voltage to the capacitor connected with a } (100~\pm50)/C_R (k\Omega) \text{ resistor.} \\ \textbf{The capacitor shall be submitted to } 1000 \text{ cycles, each consisting of charge of } 30~\pm5s, \\ \textbf{followed discharge of 5 min 30s.} \\ \textbf{The test temperature shall be } 15\sim35^{\circ}\text{C}. \\ \textbf{C}_R : \textbf{Nominal Capacitance } (\mu~F) \\ \textbf{} \\ \hline \textbf{Leakage current} & \textbf{Not more than the specified value.} \\ \hline \textbf{Capacitance Change} & \textbf{Within } \pm15\% \text{ of initial value.} \\ \hline \textbf{Lano} & \textbf{Not more than the specified value.} \\ \hline \textbf{Appearance} & \textbf{There shall be no leakage of electrolyte.} \\ \hline \textbf{Attention:} \\ \hline \textbf{This test simulates over voltage at abnormal situation only. It is not applicable to such over voltage as often applied.} \\ \hline \end{tabular} $
4.10	Vibration test	Condition> The following conditions shall be applied for 2 hours in each 3 mutually perpendicular directions. Vibration frequency range : 10Hz ~ 55Hz Peak to peak amplitude : 1.5mm Sweep rate : 10Hz ~ 55Hz ~ 10Hz in about 1 minute Mounting method: The capacitor with diameter greater than 12.5mm or longer than 25mm must be fixed in place with a bracket. Within 30° 4mm or less Within 30° After the test, the following items shall be tested: Inner construction No intermittent contacts, open or short circuiting. No damage of tab terminals or electrodes. No mechanical damage in terminal. No leakage of electrolyte or swelling of the case. The markings shall be legible.

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		<condition></condition>					
		The capacitor shall be tes	ted under the following	g conditions:			
		Soldering temperature	: 245±3°C				
		Dipping depth	: 2mm				
4.11	Solderability	Dipping speed	: 25±2.5m	m/s			
7.11	test	Dipping time	: 3±0.5s				
		<c<u>riteria></c<u>					
		Coating quality	A minimu immersed	m of 95% of the surface being			
		<condition></condition>					
			r shall be immersed in	to solder bath at 260±5°C for 10			
		_					
				0mm from the body of capacitor .			
				temperature and normal humidity			
	Resistance to	for 1~2 hours before mea	surement.				
4.12	solder heat	<criteria></criteria>	XX				
	test	Leakage current		the specified value.			
		Capacitance Change	Within ±10%	of initial value.			
		tanδ	Not more than	the specified value.			
		Appearance	There shall be	no leakage of electrolyte.			
		<condition></condition>					
		Temperature Cycle: According to IEC60384-4No.4.7methods, capacitor sha					
		placed in an oven, the condition according as below:					
		To	Time				
		(1)+20°C	≤3 Minutes				
		(2)Rated low temper	30 ± 2 Minutes				
4.13	Change of	(3)Rated high tempe		30±2 Minutes			
4.13	temperature test	(1) to (3)=1 cycle, to		100_2 111111000			
	test	< <u>Criteria></u>	tai 5 cycle				
		The characteristic shall m	neet the following requi	rement			
		Leakage current	Not more than the				
		tan δ	Not more than the				
		Appearance		eakage of electrolyte.			
			There shall be lib.	canage of electrolyte.			
		<condition></condition>					
		Humidity Test:	1No 1 12 mathada	naitor shall be averaged for 500 LS			
		_	_	acitor shall be exposed for 500 ± 8 $2^{\circ}\mathrm{C}$, the characteristic change shal			
		meet the following requir		2 C, the characteristic change shall			
		Criteria>	ement.				
	D 1	Leakage current	Not more than the sp	ecified value			
4.14	Damp heat test	Capacitance Change	Within $\pm 20\%$ of ini				
	test	tan o		of the specified value.			
		Appearance	There shall be no lea	•			
		rippearance	There shall be no lea	auge of electrolyte.			
	1						

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4.15	Vent	22.4 or less Over 22.4 Criteria> The vent shall operate with no pieces of the capacitor and/or c	th its polar ble is applied the is applied the control of the cont	ity reversed ed.	to a DC po	ower source	e. Then a
4.16	Maximum permissible (ripple current)	Condition> The maximum permissible rip at 120Hz and can be applied Table-1 The combined value of D.C rated voltage and shall not reserved. Frequency Multipliers: Coefficient Freq. (Hz) Cap. (μ F) ~180 220~560 680~1800 2200~3900 4700	at maximu	m operating I the peak A	g temperatu	ire	ceed the

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

,	Substances				
	Cadmium and cadmium compounds				
Heavy metals	Lead and lead compounds				
Heavy metais	Mercury and mercury compounds				
	Hexavalent chromium compounds				
	Polychlorinated biphenyls (PCB)				
Chloinated	Polychlorinated naphthalenes (PCN)				
organic	Polychlorinated terphenyls (PCT)				
compounds	Short-chain chlorinated paraffins(SCCP)				
	Other chlorinated organic compounds				
	Polybrominated biphenyls (PBB)				
Brominated .	Polybrominated diphenylethers(PBDE) (including				
organic	decabromodiphenyl ether[DecaBDE])				
compounds	Other brominated organic compounds				
Tributyltin compo	ounds(TBT)				
Triphenyltin com	pounds(TPT)				
Asbestos					
Specific azo com	pounds				
Formaldehyde					
Beryllium oxide					
Beryllium coppe	er				
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)				
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)				
Perfluorooctane s	ulfonates (PFOS)				
Specific Benzotri	azole				

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Attachment: Application Guidelines

1. Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2)Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about 1kΩ.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150 °C for a maximum time of 2 minutes.

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2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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