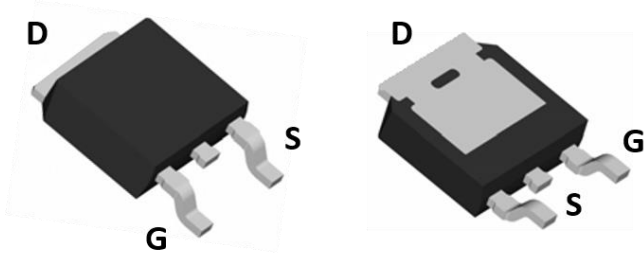
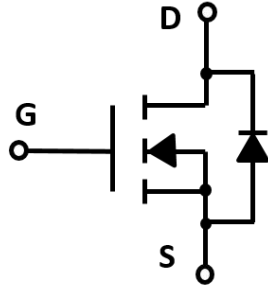


## N-Channel Enhancement Mode Field Effect Transistor



**TO-252**



### Product Summary

- $V_{DS}$  30V
- $I_D$  50A
- $R_{DS(ON)}$  (at  $V_{GS}=10V$ ) <9.0mohm
- $R_{DS(ON)}$  (at  $V_{GS}=4.5V$ ) <11.0mohm
- 100% UIS Tested
- 100%  $\nabla V_{DS}$  Tested

### General Description

- Trench Power LV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

### Applications

- High current load applications
- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

### ■ Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

| Parameter  |                   | Symbol          | Limit    | Unit         |
|--|-------------------|-----------------|----------|--------------|
| Drain-source Voltage                             |                   | $V_{DS}$        | 30       | V            |
| Gate-source Voltage                              |                   | $V_{GS}$        | $\pm 20$ | V            |
| Drain Current                                    | $T_C=25^\circ C$  | $I_D$           | 50       | A            |
|  | $T_C=100^\circ C$ |                 | 35       |              |
| Pulsed Drain Current <sup>A</sup>                |                   | $I_{DM}$        | 150      | A            |
| Total Power Dissipation                          | $T_C=25^\circ C$  | $P_D$           | 34       | W            |
|  | $T_C=100^\circ C$ |                 | 17       | W            |
| Single Pulse Avalanche Energy <sup>B</sup>       |                   | $E_{AS}$        | 80       | mJ           |
| Thermal Resistance Junction-to-Case <sup>C</sup> |                   | $R_{\theta JC}$ | 4.4      | $^\circ C/W$ |
| Junction and Storage Temperature Range           |                   | $T_J, T_{STG}$  | -55~+175 | $^\circ C$   |

### ■ Ordering Information (Example)

| PREFERRED P/N | PACKING CODE | Marking   | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---------------|--------------|-----------|----------------------|-------------------------|----------------------------|---------------|
| YJD50N03A     | F2           | YJD50N03A | 2500                 | 2500                    | 25000                      | 13" reel      |



# YJD50N03A

## ■ Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

| Parameter                             | Symbol              | Conditions   | Min                  | Typ  | Max  | Units |
|---------------------------------------|---------------------|--|----------------------|------|------|-------|
| <b>Static Parameter</b>               |                     |  |                      |      |      |       |
| Drain-Source Breakdown Voltage        | BV <sub>DSS</sub>   | V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA  | 30                   |      |      | V     |
| Zero Gate Voltage Drain Current       | I <sub>DSS</sub>    | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V  | T <sub>J</sub> =25°C |      | 1    | μA    |
|                                       |                     |  | T <sub>J</sub> =55°C |      | 5    |       |
| Gate-Body Leakage Current             | I <sub>GSS</sub>    | V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V  |                      |      | ±100 | nA    |
| Gate Threshold Voltage                | V <sub>GS(th)</sub> | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA  | 1.0                  | 1.5  | 2.5  | V     |
| Static Drain-Source On-Resistance     | R <sub>DS(ON)</sub> | V <sub>GS</sub> = 10V, I <sub>D</sub> =15A   |                      | 6.5  | 9.0  | mΩ    |
|                                       |                     | V <sub>GS</sub> = 4.5V, I <sub>D</sub> =15A  |                      | 8.6  | 11.0 |       |
| Diode Forward Voltage                 | V <sub>SD</sub>     | I <sub>S</sub> =15A, V <sub>GS</sub> =0V   |                      | 0.85 | 1.2  | V     |
| Maximum Body-Diode Continuous Current | I <sub>S</sub>      |  |                      |      | 50   | A     |
| <b>Dynamic Parameters</b>             |                     |  |                      |      |      |       |
| Input Capacitance                     | C <sub>iss</sub>    | V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHZ  |                      | 920  |      | pF    |
| Output Capacitance                    | C <sub>oss</sub>    |  |                      | 198  |      |       |
| Reverse Transfer Capacitance          | C <sub>rss</sub>    |  |                      | 114  |      |       |
| <b>Switching Parameters</b>           |                     |  |                      |      |      |       |
| Total Gate Charge                     | Q <sub>g</sub>      | V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, I <sub>D</sub> =50A  |                      | 28   |      | nC    |
| Gate-Source Charge                    | Q <sub>gs</sub>     |  |                      | 7    |      |       |
| Gate-Drain Charge                     | Q <sub>gd</sub>     |  |                      | 5    |      |       |
| Reverse Recovery Charge               | Q <sub>rr</sub>     | I <sub>r</sub> =20A, di/dt=100A/us   |                      | 25   |      | ns    |
| Reverse Recovery Time                 | t <sub>rr</sub>     |  |                      | 26   |      |       |
| Turn-on Delay Time                    | t <sub>D(on)</sub>  | V <sub>GS</sub> =10V, V <sub>DD</sub> =20V, I <sub>D</sub> =2A, R <sub>L</sub> =1Ω<br>R <sub>GEN</sub> =3Ω |                      | 8    |      | ns    |
| Turn-on Rise Time                     | t <sub>r</sub>      |  |                      | 15   |      |       |
| Turn-off Delay Time                   | t <sub>D(off)</sub> |  |                      | 27   |      |       |
| Turn-off fall Time                    | t <sub>f</sub>      |  |                      | 7    |      |       |

A. Pulse Test: Pulse Width ≤ 300us, Duty cycle ≤ 2%.

B. T<sub>J</sub>=25°C, V<sub>DD</sub>=20V, V<sub>G</sub>=10V, L=0.5mH, R<sub>g</sub>=25 Ω

C. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>θJC</sub> is guaranteed by design, while R<sub>θJA</sub> is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.



■ Typical Performance Characteristics

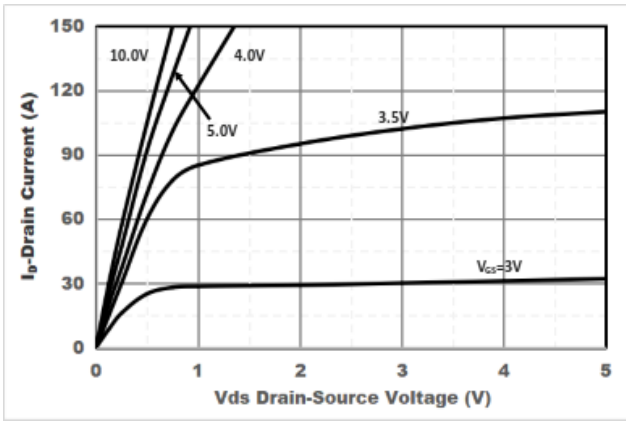


Figure1. Output Characteristics

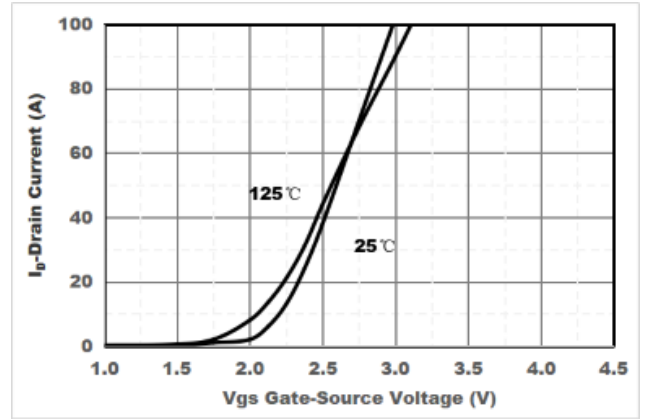


Figure2. Transfer Characteristics

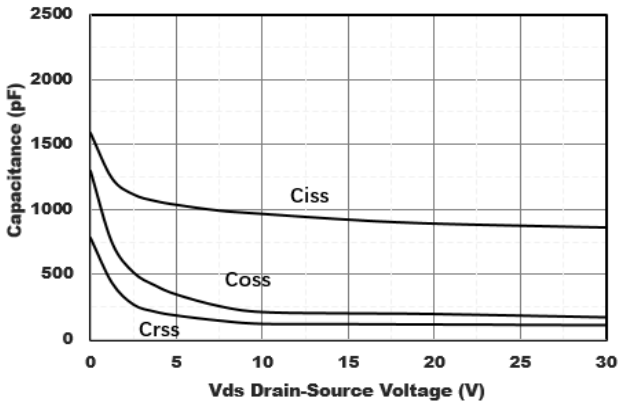


Figure3. Capacitance Characteristics

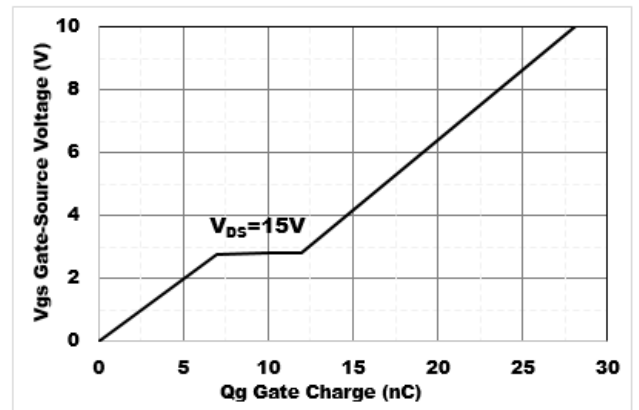


Figure4. Gate Charge

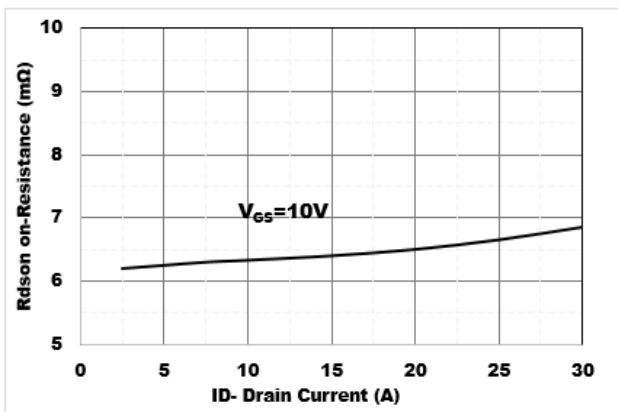


Figure5. Drain-Source on Resistance

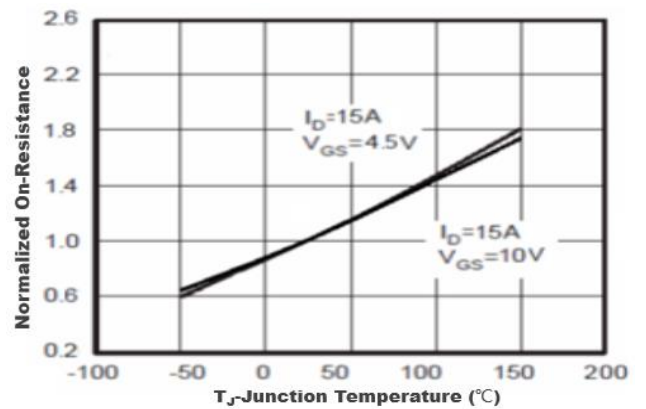


Figure6. Drain-Source on Resistance



# YJD50N03A

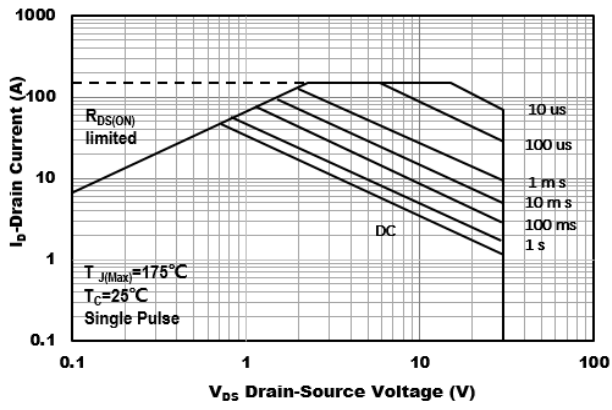


Figure7. Safe Operation Area

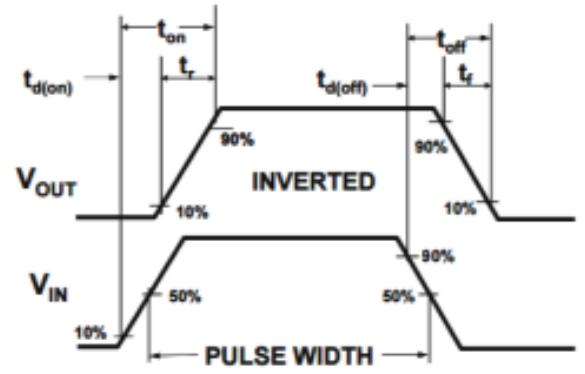
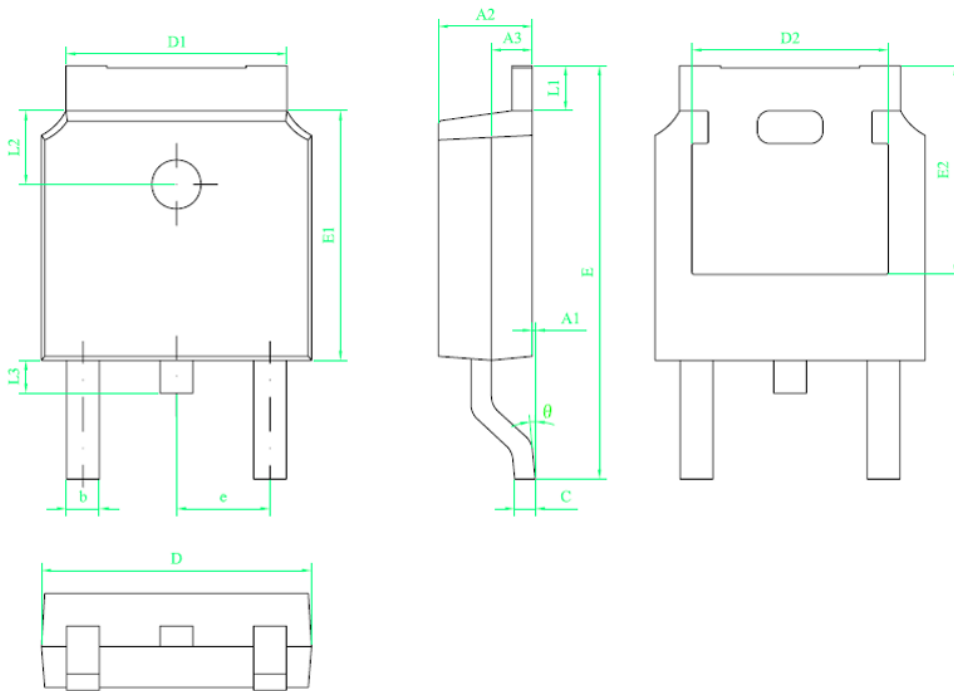


Figure8. Switching wave



# YJD50N03A

## ■ TO-252 Package information



| 符号 | 尺寸   |       |       |
|----|------|-------|-------|
|    | min  | nom   | max   |
| A1 | 0    | ---   | 0.10  |
| A2 | 2.20 | 2.30  | 2.40  |
| A3 | 0.90 | 1.00  | 1.10  |
| b  | 0.75 | ---   | 0.85  |
| c  | 0.50 | ---   | 0.60  |
| D  | 6.50 | 6.60  | 6.70  |
| D1 | 5.30 | 5.40  | 5.50  |
| D2 | 4.70 | 4.80  | 4.90  |
| E  | 9.90 | 10.10 | 10.30 |
| E1 | 6.00 | 6.10  | 6.20  |
| E2 | 5.20 | 5.30  | 5.40  |
| e  | 2.20 | 2.286 | 2.40  |
| L1 | 0.90 | ---   | 1.25  |
| L2 | 1.70 | 1.80  | 1.90  |
| L3 | 0.60 | 0.80  | 1.00  |
| θ  | 0°   | ---   | 8°    |

### 技术要求:

1. 树脂体不应有崩裂、缺损等缺陷;
2. 树脂上下部X、Y方向偏差不得超过0.20;
3. 胶体两端留胶总和宽度不超过0.50;
4. 所有单位为mm;



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