

Description

The HSP150N02 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

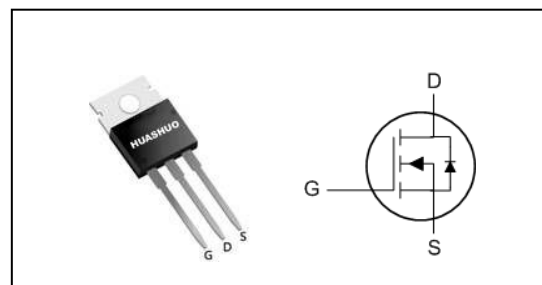
The HSP150N02 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Power Switching application
- Green Device Available
- Motion control application
- High efficiency synchronous rectification in SMPS

Product Summary

V_{DS}	150	V
$R_{DS(ON),typ}$	12	$m\Omega$
I_D	120	A

TO220 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V_1$	120	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V_1$	84	A
I_{DM}	Pulsed Drain Current ₂	420	A
EAS	Single Pulse Avalanche Energy ₃	1010	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation ₃	300	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ₁	---	60	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	---	0.55	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _{VDS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	150	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =60A	---	12	15	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	3	---	5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =150V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =150V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	3.2	---	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =120V, V _{GS} =10V, I _D =60A	---	135	---	nC
Q _{gs}	Gate-Source Charge		---	29	---	
Q _{gd}	Gate-Drain Charge		---	48	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =75V, V _{GS} =10V, R _G =3Ω I _D =60A	---	30	---	ns
T _r	Rise Time		---	39	---	
T _{d(off)}	Turn-Off Delay Time		---	75	---	
T _f	Fall Time		---	55	---	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	5782	---	pF
C _{oss}	Output Capacitance		---	569	---	
C _{rss}	Reverse Transfer Capacitance		---	318	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	120	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	420	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =60A, T _J =25°C	---	---	1.1	V
t _{rr}	Reverse Recovery Time	I _F =60A, dI/dt=100A/μs, T _J =25°C	---	47	---	nS
Q _{rr}	Reverse Recovery Charge		---	93	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.3mH
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

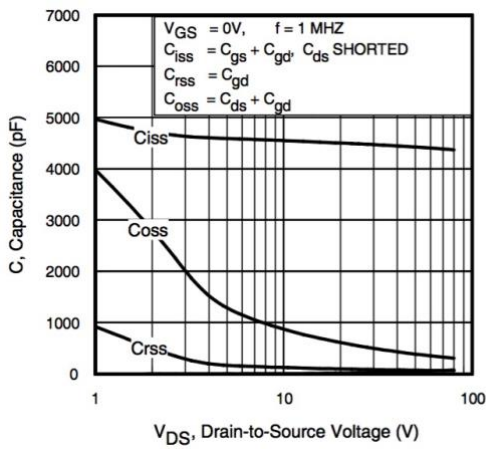


Fig.1 Typical Capacitance vs. Drain-Source Voltage

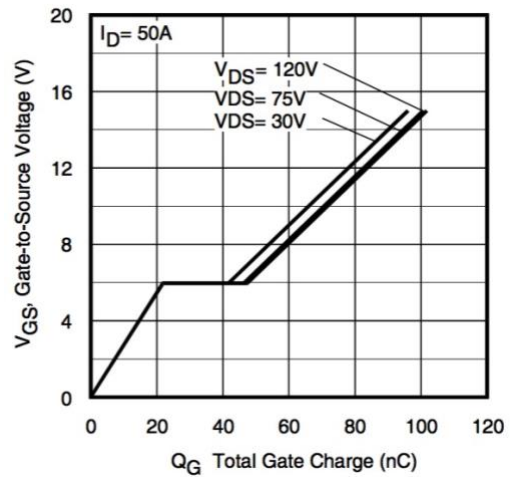


Fig.2 Typical Gate Charge vs. Gate-Source Voltage

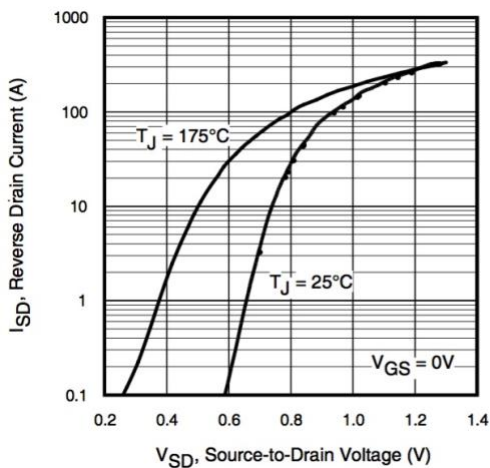


Fig.3 Typical Source-Drain Diode Forward Voltage

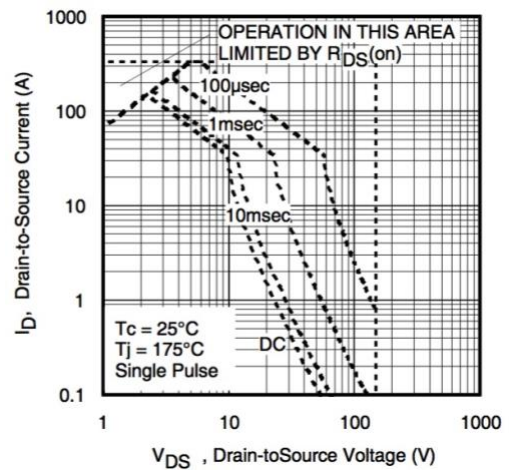


Fig.4 Maximum Safe Operating Area

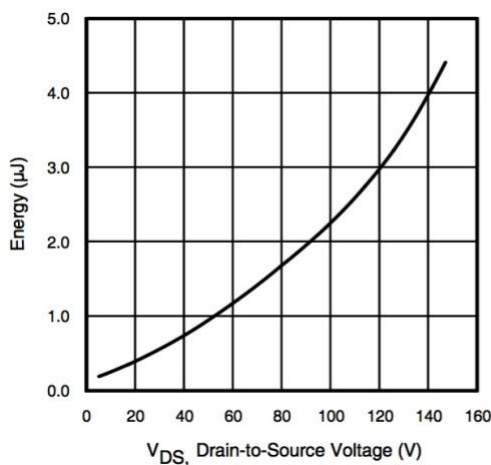


Fig.5 Typical Coss Stored Energy

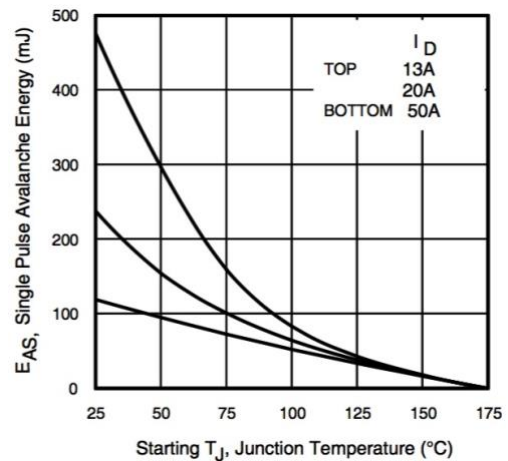


Fig.6 Maximum Avalanche Energy vs. Draincurrent

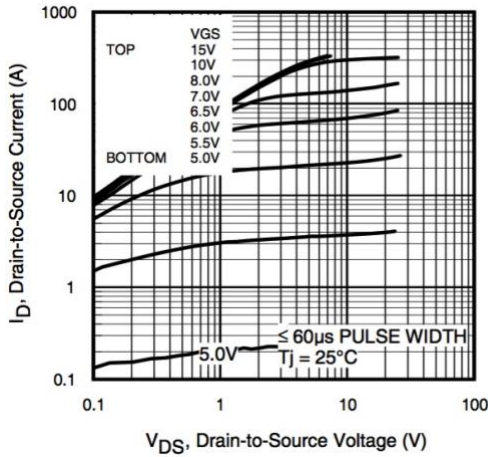


Fig.7 VDS, Drain-to-Source Voltage(V)

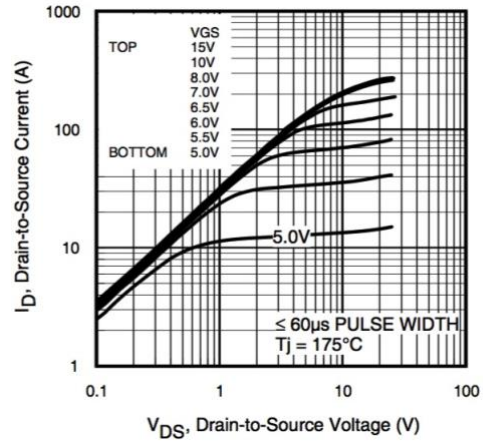


Fig.8 VDS, Drain-to-Source Voltage(V)

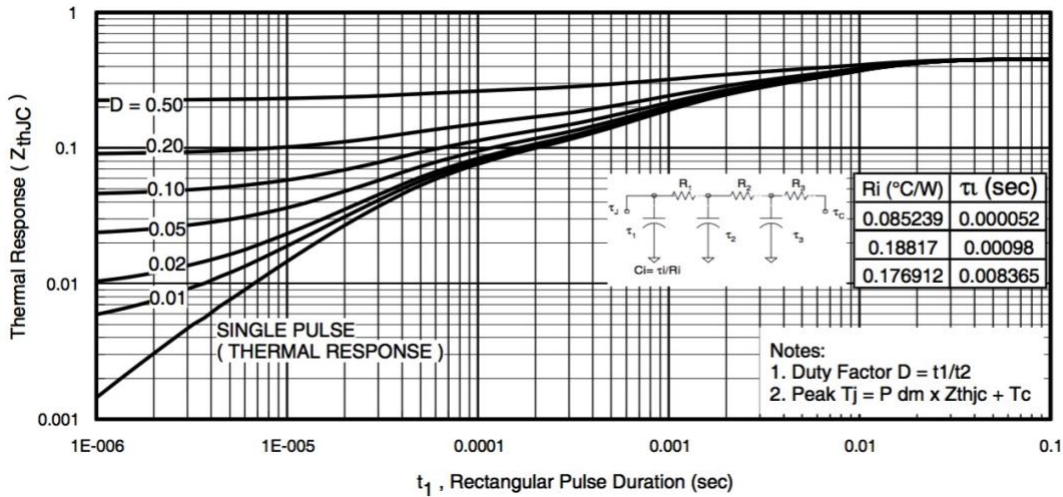


Fig.9 Maximum Effective Transient Thermal Impedance, Junction-Case

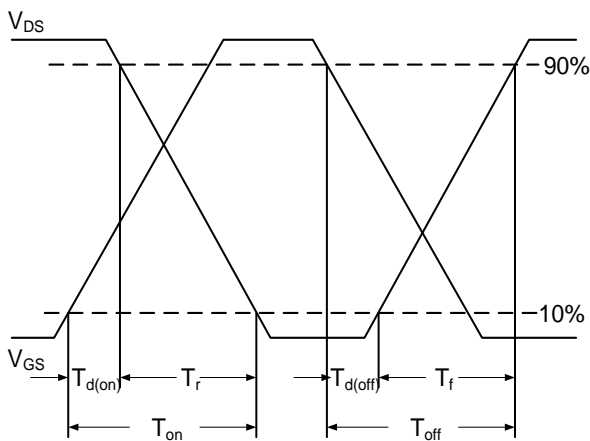


Fig.10 Switching Time Waveform

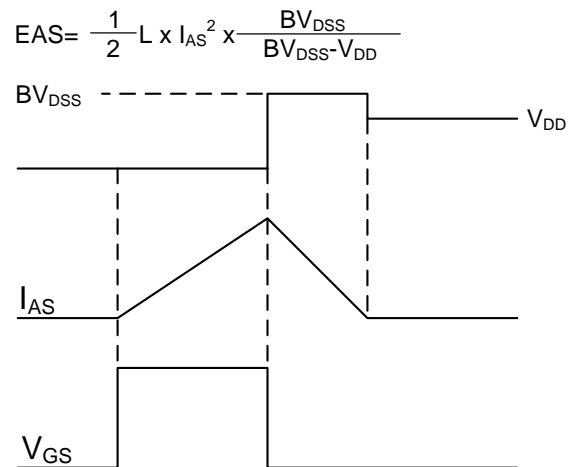
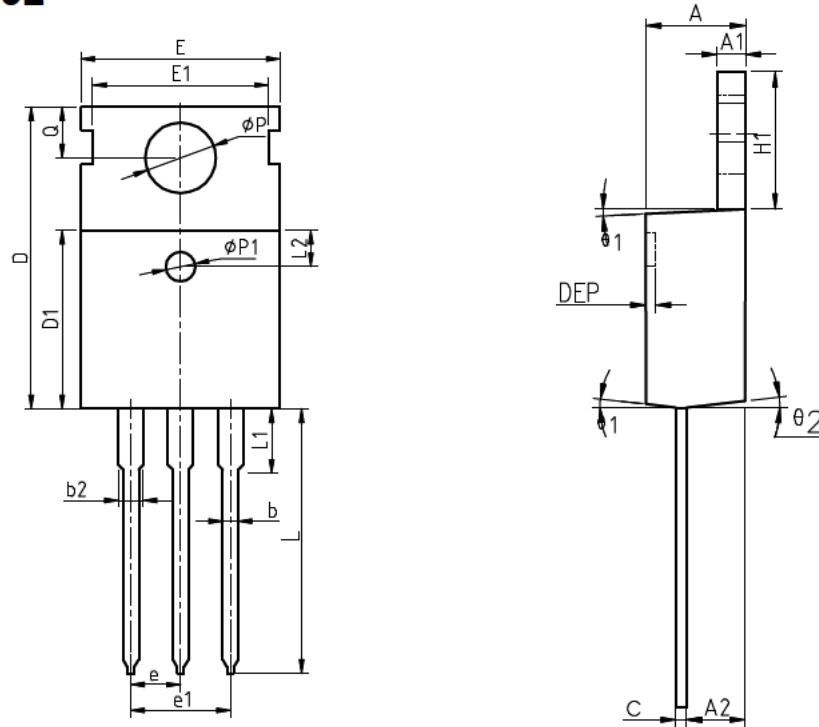


Fig.11 Unclamped Inductive Switching



Package Information

TO-220FB-3L



COMMON DIMENSIONS

SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.27	1.30	1.33	0.050	0.051	0.052
A2	2.35	2.40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1.27	1.36	0.046	0.050	0.054
c	0.48	0.50	0.56	0.019	0.020	0.022
D	15.40	15.60	15.80	0.606	0.614	0.622
D1	9.00	9.10	9.20	0.354	0.358	0.362
DEP	0.05	0.10	0.20	0.002	0.004	0.008
E	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8.70	-	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
e		2.54	BSC		0.100	BSC
e1		5.08	BSC		0.200	BSC
H1	6.40	6.50	6.60	0.252	0.256	0.260
L	12.75	13.50	13.65	0.502	0.531	0.537
L1	-	3.10	3.30	-	0.122	0.130
L2		2.50	REF		0.098	REF
P	3.50	3.60	3.63	0.138	0.142	0.143
P1	3.50	3.60	3.63	0.138	0.142	0.143
Q	2.73	2.80	2.87	0.107	0.110	0.113
theta 1	5°	7°	9°	5°	7°	9°
theta 2	1°	3°	5°	1°	3°	5°
theta 3	1°	3°	5°	1°	3°	5°

