

### Description

The HSM4052 is the high cell density trenched N-ch MOSFETs, which provide excellent R<sub>DS(ON)</sub> and gate charge for most of the synchronous buck converter applications.

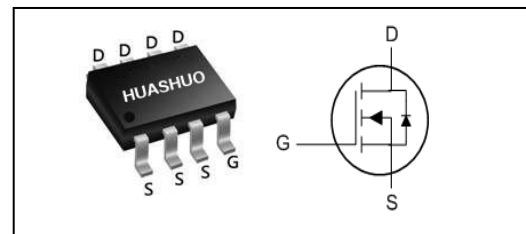
The HSM4052 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

### Product Summary

V <sub>DS</sub>	40	V
R <sub>DS(ON),typ</sub>	6.9	mΩ
I <sub>D</sub>	14	A

### SOP-8 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current <sub>1</sub>	14	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current <sub>1</sub>	11	A
I <sub>DM</sub>	Pulsed Drain Current <sub>2</sub>	60	A
EAS	Single Pulse Avalanche Energy <sub>3</sub>	48	mJ
I <sub>AS</sub>	Avalanche Current	31	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sub>4</sub>	2.5	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient (Steady State) <sub>1</sub>	---	50	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sub>1</sub>	---	20	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =12A	---	6.9	8.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	10.5	15	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.5	2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	1.7	---	Ω
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A	---	5.8	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	3	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	1.2	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω I <sub>D</sub> =1A	---	14.3	---	ns
T <sub>r</sub>	Rise Time		---	5.6	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	20	---	
T <sub>f</sub>	Fall Time		---	11	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	690	---	pF
C <sub>oss</sub>	Output Capacitance		---	193	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	38	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	14	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=31A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.



Typical Characteristics

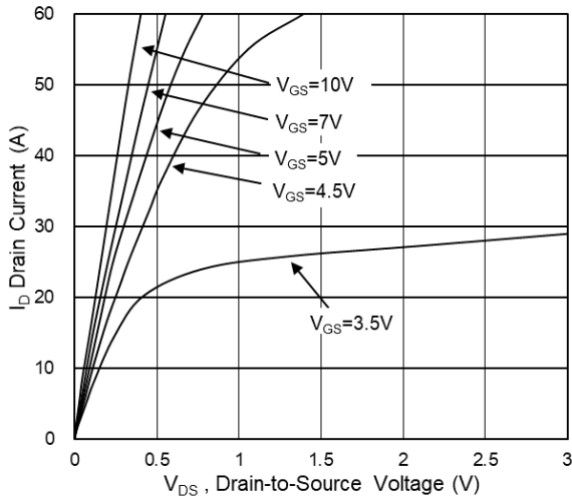


Fig.1 Typical Output Characteristics

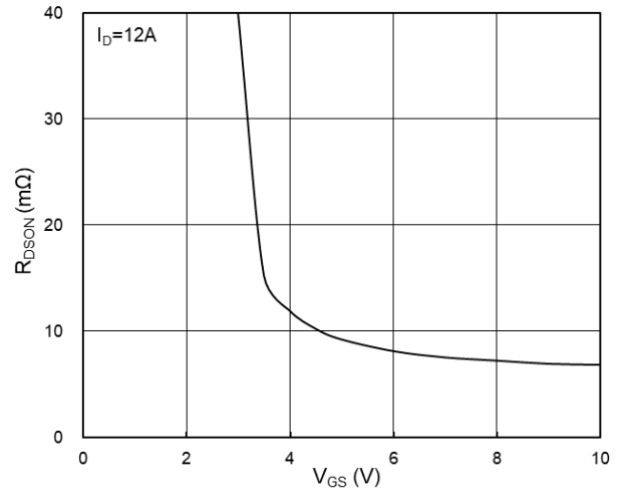


Fig.2 On-Resistance vs. G-S Voltage

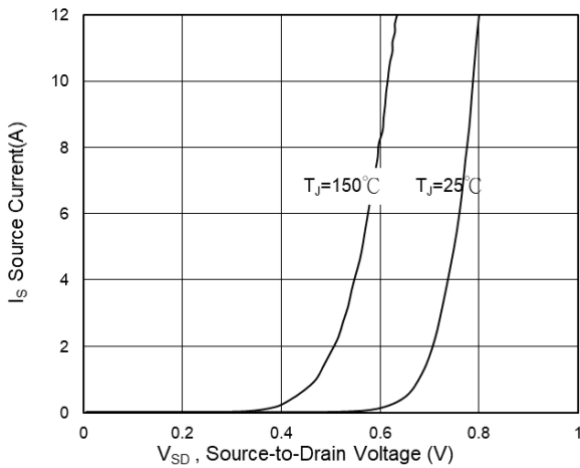


Fig.3 Source Drain Forward Characteristics

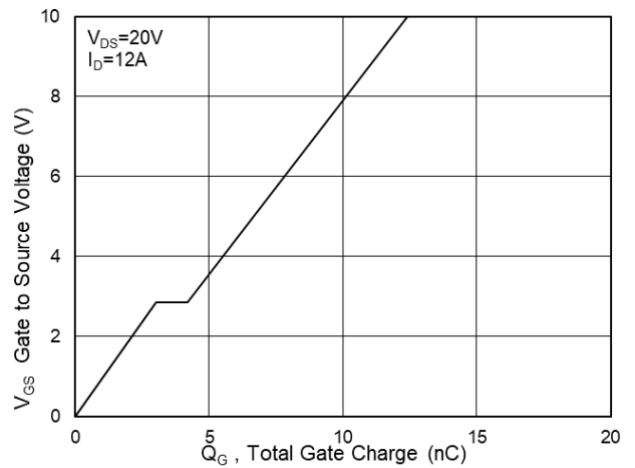


Fig.4 Gate-Charge Characteristics

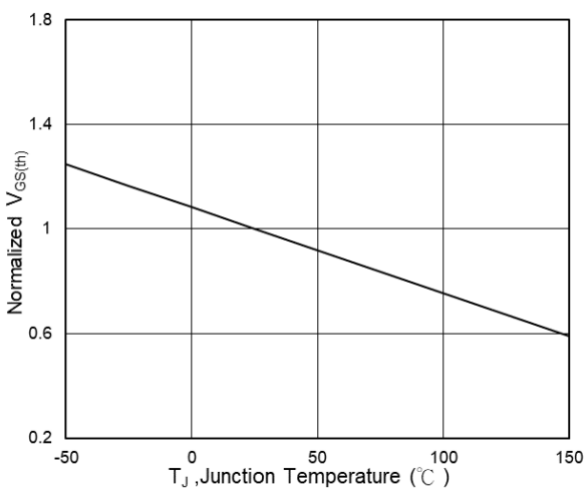


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

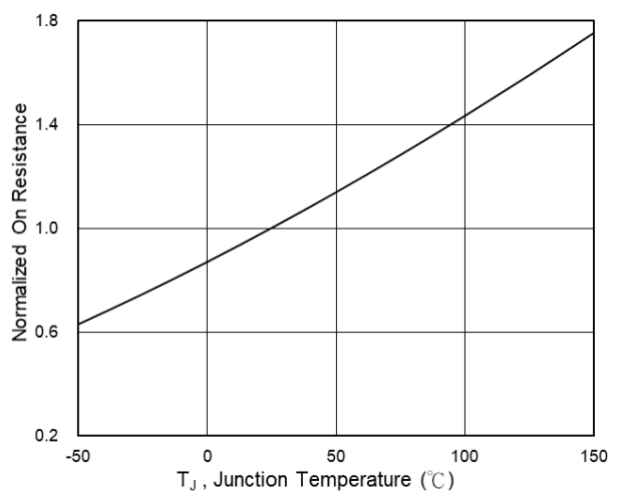
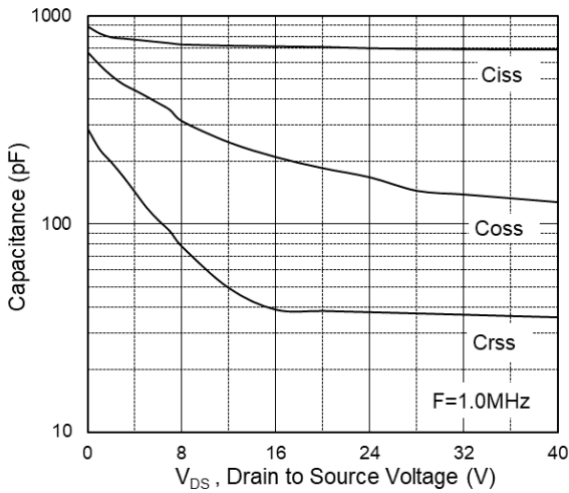


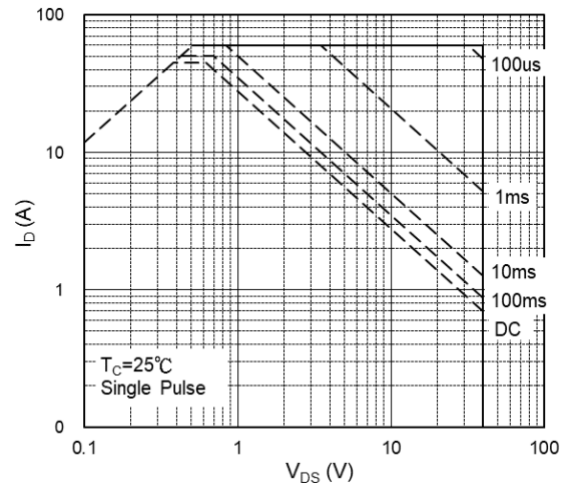
Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



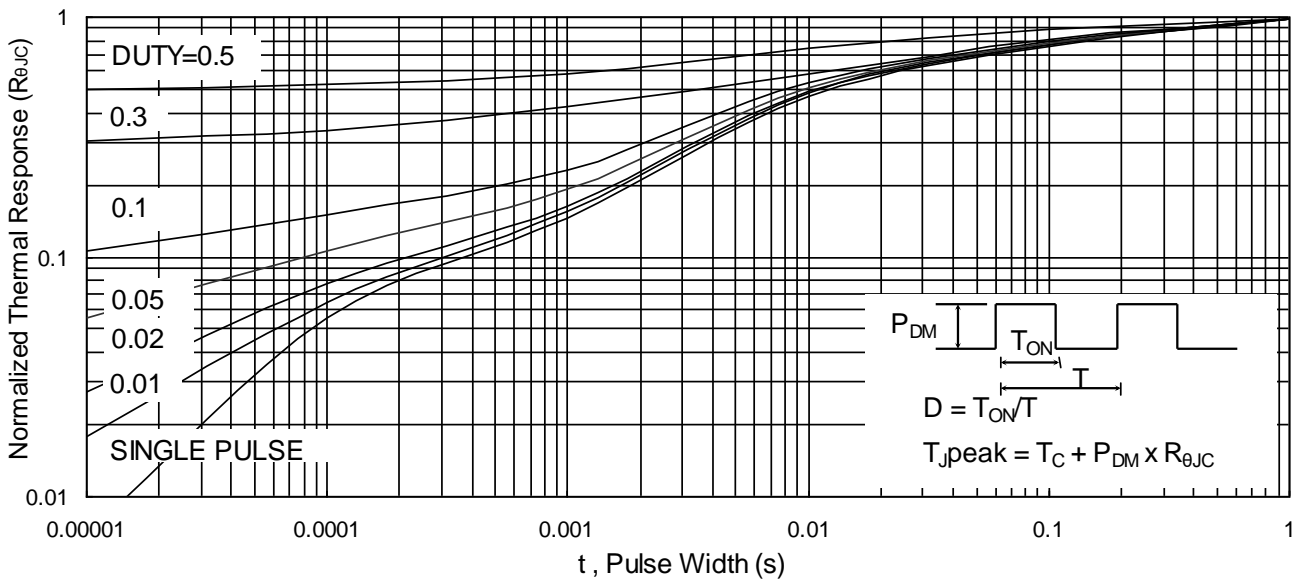
**N-Ch 40V Fast Switching MOSFETs**



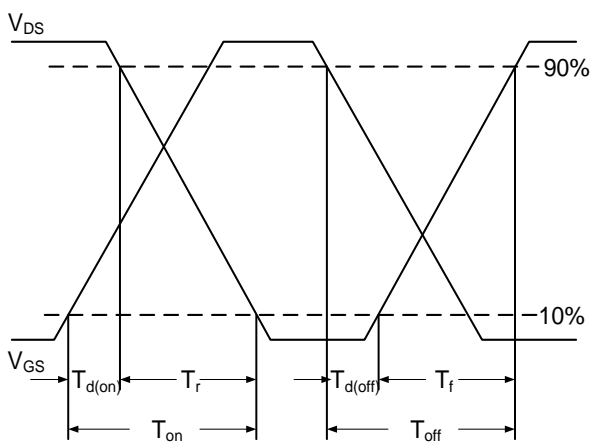
**Fig.7 Capacitance**



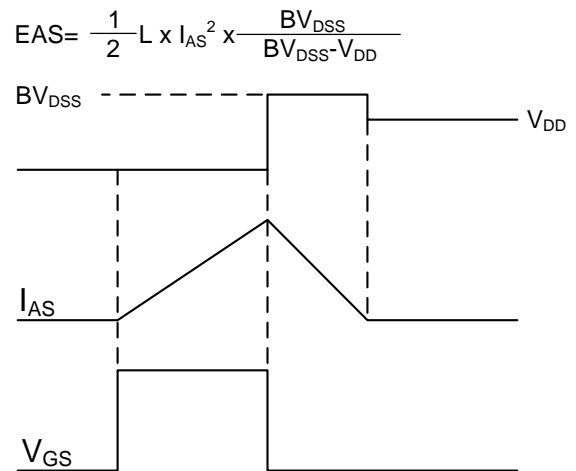
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Waveform**



**Ordering Information**

Part Number	Package code	Packaging
HSM4052	SOP-8	2500/Tape&Reel

