

### Features

- High Density Cell Design for Ultra Low RDS(ON)
- Fully Characterized Avalanche Voltage and Current
- Excellent Package for Good Heat Dissipation
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

### **Maximum Ratings**

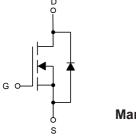
- Operating Junction Temperature : +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 125°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	150	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	4.0	Α
Pulsed Drain Current(note2)	I <sub>DM</sub>	16.0	Α
Maximum lead temperure for soldering purposes , 1/8"from case for 5 seconds	TL	260	°C

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

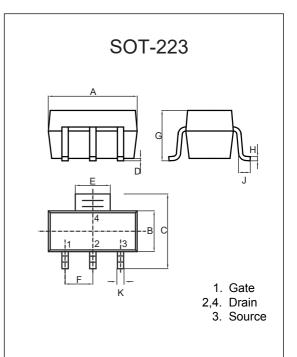
2. Repetitive Rating:Pulse width limited by maximum junction temperature.

## **Internal Structure**



Marking:	T04N15
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DIMENSIONS					
DIM	INCHES		MM		NOTE
DIN	MIN	MAX	MIN	MAX	NOTE
А	0.248	0.264	6.30	6.70	
В	0.130	0.146	3.30	3.70	
С	0.264	0.287	6.70	7.30	
D	0.001	0.004	0.02	0.10	
Е	0.114	0.122	2.90	3.10	
F	0.0	91	2.3	30	TYP.
G		0.071		1.80	
Н	0.009	0.014	0.23	0.35	
J	0.030		0.75		
Κ	0.026	0.033	0.66	0.84	



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Off Characteristics	l			1	<u> </u>	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	150			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V			1	μA
Gate-Source Leakage Current(Note 3)	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =9A			1.2	V
On Characteristics <sup>(Note 3)</sup>						
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.5	2	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =4.0A		0.130	0.160	Ω
Forward Transconductance	gfs	V <sub>DS</sub> =15V, I <sub>D</sub> =4.0A	5.0			S
Dynamic Characteristics <sup>(Note 4)</sup>				1		
Input Capacitance	C <sub>iss</sub>			900		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =25V, $V_{GS}$ =0V,f=1MHz		115		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			70		
Switching Characteristics <sup>(Note</sup>	4)			•	••	
Turn-On Delay Time	t <sub>d(on)</sub>			8.0		
Turn-On Rise Time	t <sub>r</sub>			10.0		20
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V,R <sub>L</sub> =75 $\Omega$ ,		20.0		ns
Turn-Off Fall Time	t <sub>f</sub>			15.0		
Total Gate Charge	Qg			19.0		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =75V, I <sub>D</sub> =1.5A, V <sub>GS</sub> =10V		5.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			7.0		

Note :

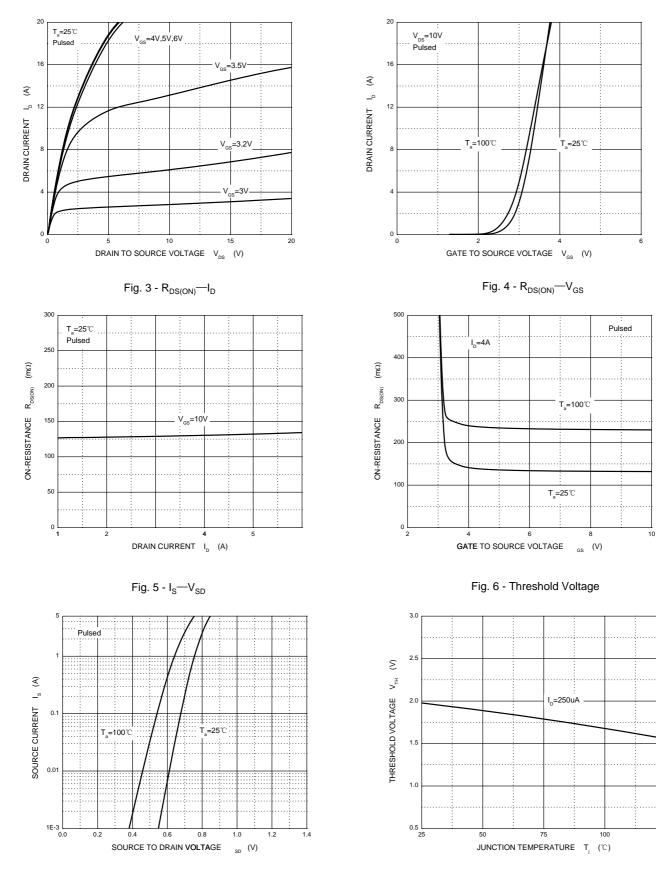
3. Pulse Test : Pulse width≤300 $\mu$ s, duty cycle ≤2%.

4. These parameters have no way to verify.



# **Curve Characteristics**

Fig. 1 - Output Characteristics





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## **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

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