NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE6080D uses advanced trench technology and design to provide excellent $R_{\text{DS}(\text{ON})}\hspace{0.5mm}\text{with low gate charge.}\hspace{0.5mm}\text{It}\hspace{0.5mm}$ can be used in a wide variety of applications.

General Features

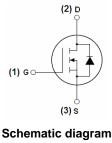
- V_{DS} =60V,I_D =80A $R_{DS(ON)}$ <8.5m Ω @ V_{GS} =10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- PWM
- Load Switching

100% UIS TESTED!

100% AVds TESTED!





Marking and pin assignment



TO-263-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6080D	NCE6080D	TO-263-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G S	±20	V
Drain Current-Continuous	I _D	80	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	56.5	А
Pulsed Drain Current	I _{DM}	320	Α
Maximum Power Dissipation	P _D	110	W
Derating factor		0.73	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	390	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	${\mathbb C}$

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	R _{eJC}	1.36	°C/W
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

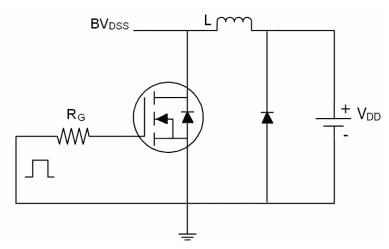
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2	2.8	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	7	8.5	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ 20\/\/ 0\/	-	4000	-	PF
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	290	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIDZ	-	210	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	8.5	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, R_L =1 Ω	-	7	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	40	-	nS
Turn-Off Fall Time	t _f		-	15	-	nS
Total Gate Charge	Qg	V 20VI 20A	-	90		nC
Gate-Source Charge	Q _{gs}	$V_{DS}=30V,I_{D}=20A,$	-	9		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	18		nC
Drain-Source Diode Characteristics	•		•			
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	80	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 20A	-	32	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	45	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			y LS+LD)	

Notes:

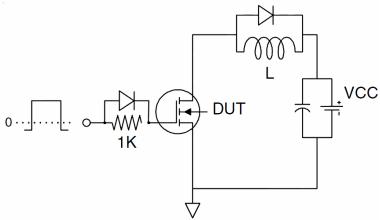
- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature}.$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E_{AS} condition : $Tj{=}25^{\circ}\!\mathrm{C}, V_{DD}{=}30V, V_{G}{=}10V, L{=}0.5mH, Rg{=}25\Omega$

Test circuit

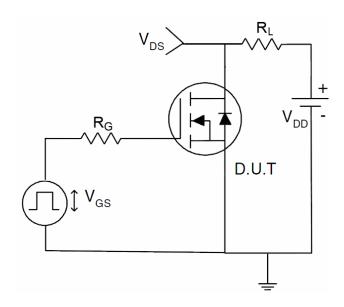
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

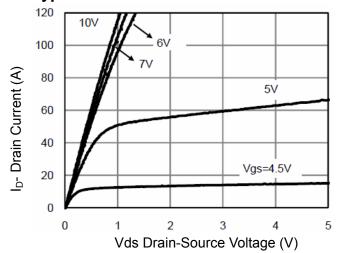


Figure 1 Output Characteristics

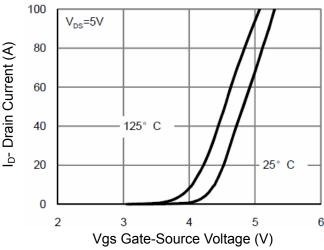


Figure 2 Transfer Characteristics

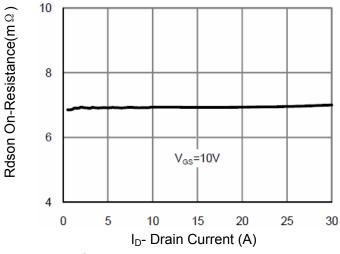


Figure 3 Rdson- Drain Current

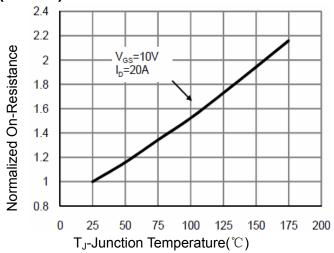


Figure 4 Rdson-JunctionTemperature

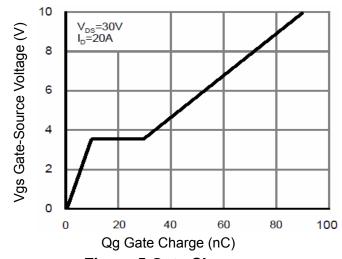


Figure 5 Gate Charge

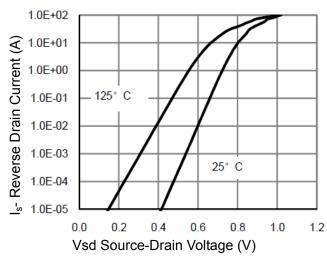
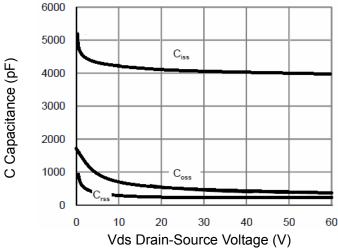


Figure 6 Source- Drain Diode Forward





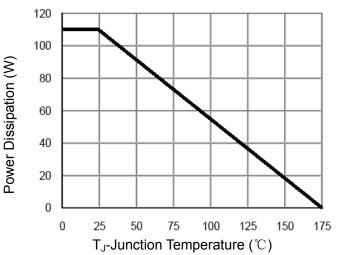
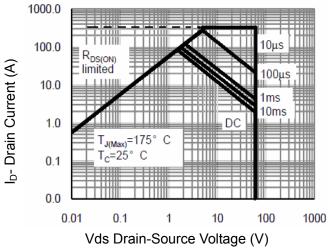


Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



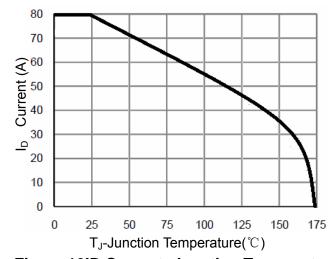
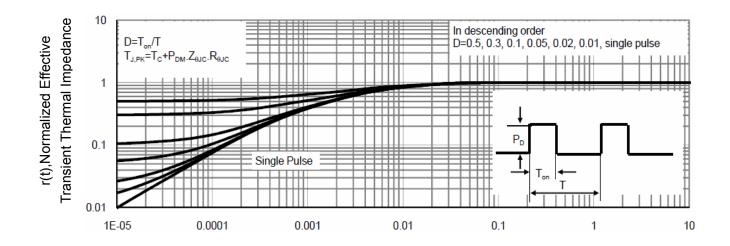


Figure 8 Safe Operation Area

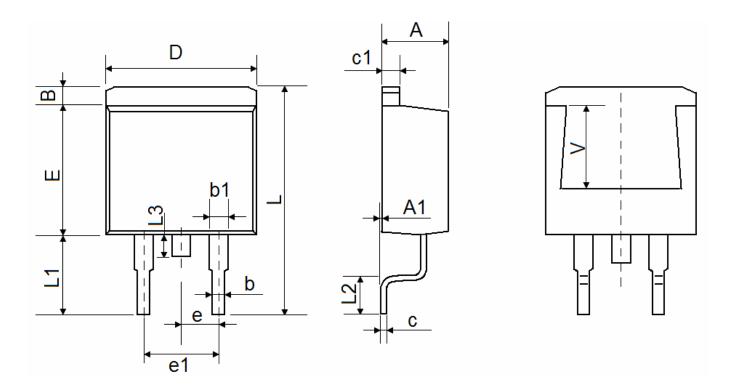
Figure 10ID Current- Junction Temperature



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

TO-263-2L Package Information



Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540	2.540 TYP.		TYP.	
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.60	0 REF	0.220 REF		

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