

600V N-Channel MOSFET

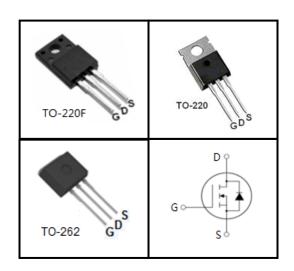
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device Package Markin		Marking		
CS10N60F	TO-220F	CS10N60F		
CS10N60P	TO-220	CS10N60P		
CS10N60K	TO-262	CS10N60K		



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter	Symbol	Value			Unit
raiametei		TO-220F	TO-220	TO-262	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	600		V	
Continuous Drain Current	I _D	10		А	
Pulsed Drain Current (note1)	I _{DM}	40		Α	
Gate-Source Voltage	V _{GSS}		±30		V
Single Pulse Avalanche Energy (note2)	E _{AS}	320		mJ	
Avalanche Current (note1)	I _{AS}	8		А	
Repetitive Avalanche Energy (note1)	E _{AR}	192		mJ	
Power Dissipation (T _C = 25°C)	P _D	65	14	40	W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C	

Thermal Resistance					
Parameter	Symbol	Value			l lmit
		TO-220F	TO-220	TO-262	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.92	0.89		00/14/
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		°C/W



CS10N60F,CS10N60P,CS1060K

Specifications T _J = 25°C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Тур.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	600			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 5.0A$		0.6	0.72	Ω
Dynamic						
Input Capacitance	C _{iss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$ f = 1.0MHz		1363		pF
Output Capacitance	C _{oss}			139		
Reverse Transfer Capacitance	C _{rss}			18		
Total Gate Charge	Q_g	$V_{DD} = 480V, I_{D} = 10A,$ $V_{GS} = 10V$	-	42		nC
Gate-Source Charge	Q_{gs}		1	6		
Gate-Drain Charge	Q_{gd}			22		
Turn-on Delay Time	t _{d(on)}			45		
Turn-on Rise Time	t _r	$V_{DD} = 300V, I_{D} = 10A,$	-	28		
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$	-	190		ns
Turn-off Fall Time	t _f			75		
Drain-Source Body Diode Character	istics					
Continuous Body Diode Current	I _s	T 05.00			10	А
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			40	^
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 5A$, $V_{GS} = 0V$			1.4	V
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 10A,$		552		ns
Reverse Recovery Charge	Q_{rr}	di _F /dt =100A /μs		2.76		μC

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 1. Output Characteristics (T_J = 25°C)

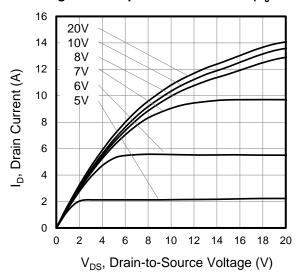


Figure 3. Drain Current vs. Temperature

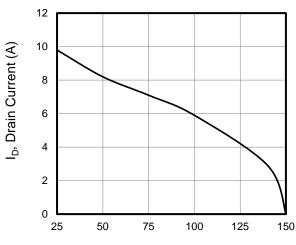


Figure 5. Transfer Characteristics

T_C, Case Temperature (A)

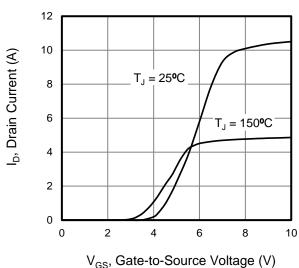


Figure 2. Body Diode Forward Voltage

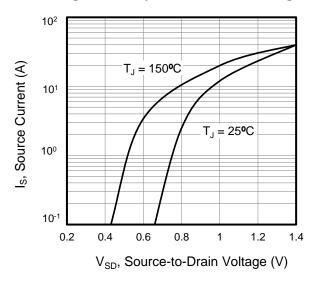


Figure 4. BV_{DSS} Variation vs. Temperature

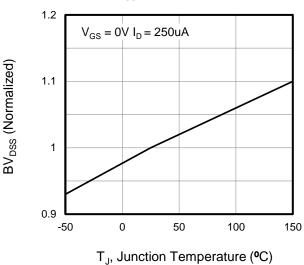
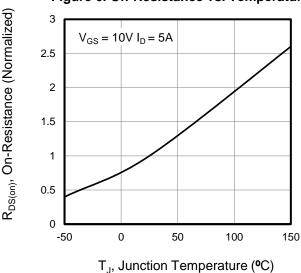


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

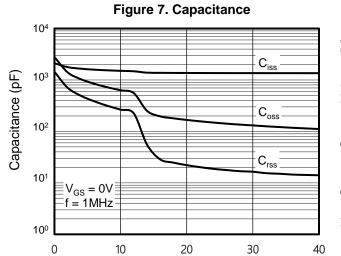


Figure 9. Transient Thermal Impedance

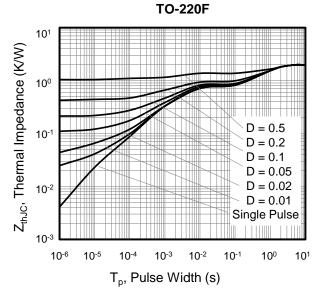


Figure 10. Transient Thermal Impedance

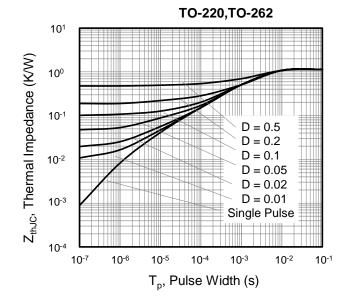




Figure A: Gate Charge Test Circuit and Waveform

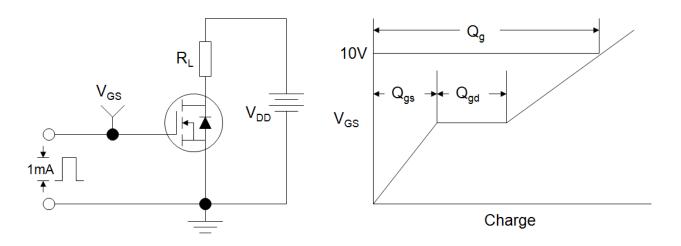


Figure B: Resistive Switching Test Circuit and Waveform

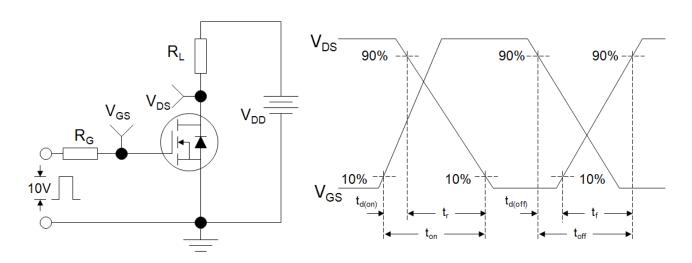
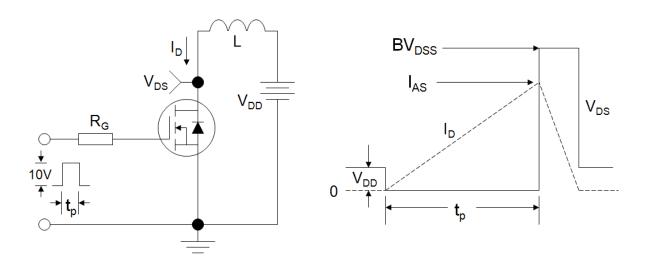
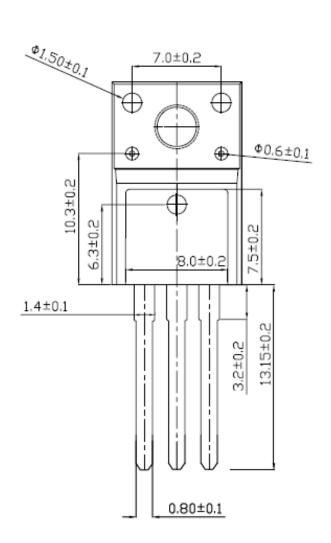


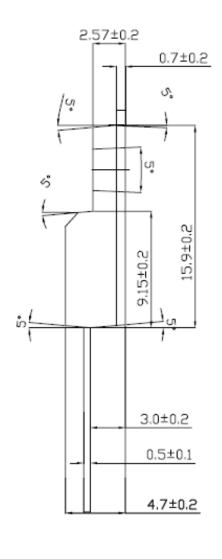
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





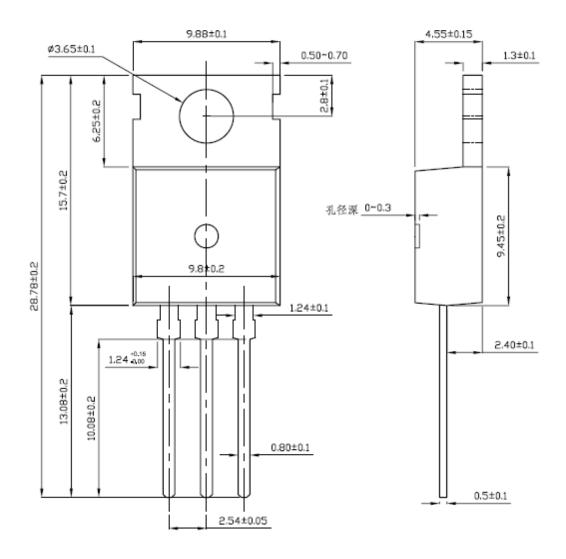
TO-220F





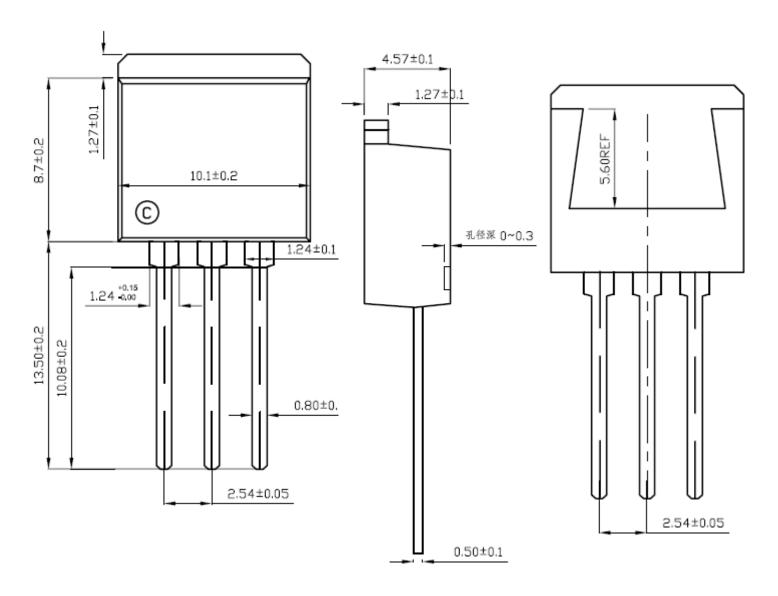


TO-220





TO-262



CS10N60F,CS10N60P,CS1060K

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