

Description

The SRV05-4L is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The SRV05-4L has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) with ±25kV air and ±20kV contact discharge. It is assembled into a 6-Pin lead-free SOT-23 6L package. The low capacitance array make it ideal for four high speed data and transmission line. This device is optimized for ESD protection of portable electronics.

Features

- Ultra low capacitance: 0.3pF typical (I/O to I/O)
- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Up to four data lines and one power line protects
- JEDEC SOT-23 6L package
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±25kV
 Contact discharge: ±20kV
 - IEC61000-4-5 (Lightning) 5A (8/20µs)
- RoHS Compliant

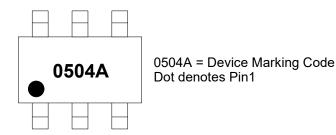
Mechanical Characteristics

- Package: SOT-23 6L
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound.
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram Below
- Marking Information: See Below

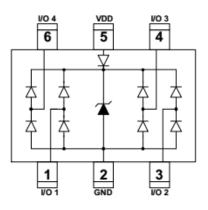
Applications

- USB 2.0 Power and Data lines protection
- Digital Visual Interface (DVI)
- Monitors and Flat Panel Displays
- Video Graphic Cards
- Notebook and PC Computers

Marking Information



Dimensions and Pin Configuration



Circuit and Pin Schematic

Ordering Information

Part Number	Packaging	Reel Size	
SRV05-4L	3000/Tape & Reel	7 inch	



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	75	W
Peak Pulse Current (8/20µs)	IPP	5	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	Vesd	±25 ±20	kV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

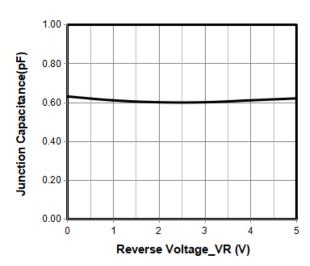
Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Any I/O pin to ground
Breakdown Voltage	Vbr	6			V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	I _R			0.5	μA	VRWM = 5V, any I/O pin to ground
Clamping Voltage	Vc			10	V	IPP = 1A (8 x 20µs pulse), any I/O pin to ground
Clamping Voltage	Vc			15	V	IPP = 5A (8 x 20µs pulse), any I/O pin to ground
Junction Capacitance	Сл		0.3	0.4	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	Сл			0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

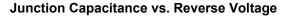
Note 1: I/O pins are Pin 1, 3, 4 and 6

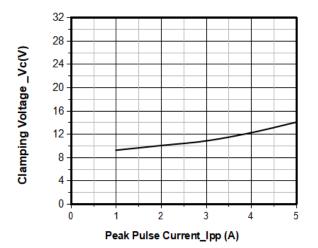


SRV05-4L

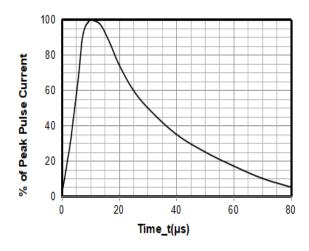


Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

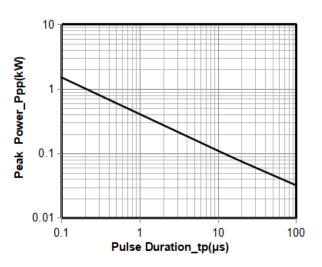




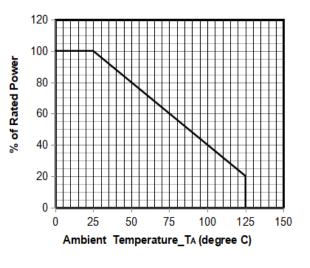
Clamping Voltage vs. Peak Pulse Current



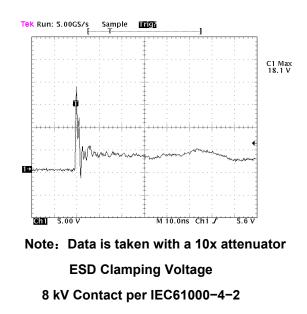
8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time

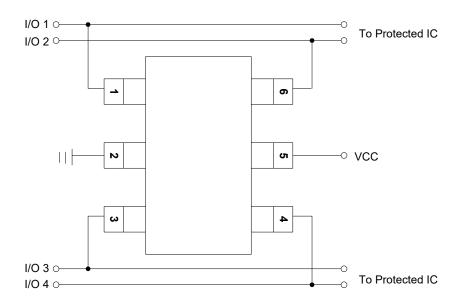


Power Derating Curve

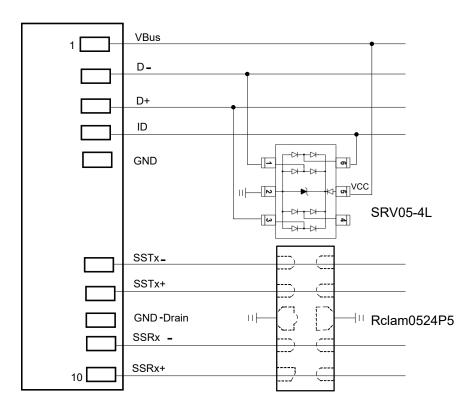




The SRV05-4L is designed to protect four data lines from transient over -voltages by clamping them to fixed reference. When the voltage on the protected line exceeds the reference voltage (plus diode VF) the steering diodes are forward biased, conducting the transient current away from the sensitive circuitry. Data lines are connected at pins 1, 3, 4 and 6. The negative reference (REF1) is connected at pin 2. This pin should be connected directly to a ground plane on the board for best results. The path length is kept as short as possible to minimize parasitic inductance. The positive reference (REF2) is connected at pin 5.

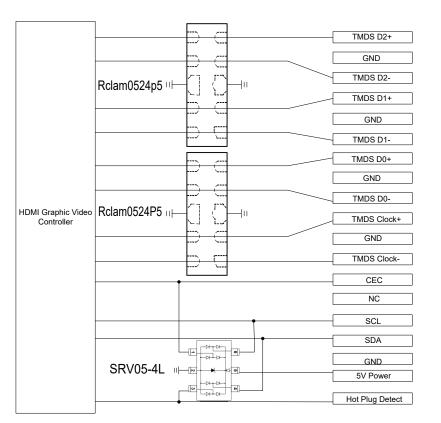


On USB 3.0 Port Application

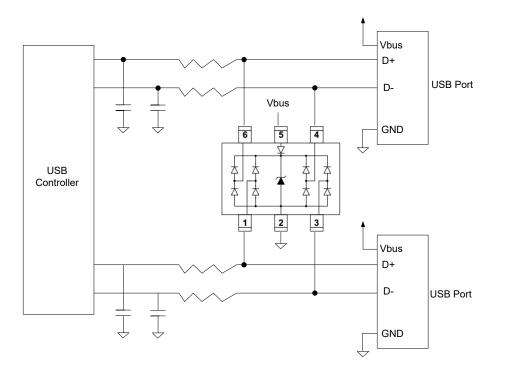




On HDMI Port Application



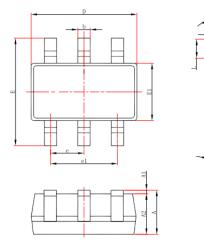
On USB Port Application





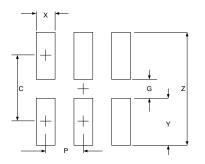
SRV05-4L

SOT-23 6L Package Outline Drawing



Sumb a l	Dimensions Ir	n Millimeters	Dimensions	In Inches	
Symbol	Min.	Max.	Min.	Max.	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E1	1.500	1.700	0.059	0.067	
E	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Suggested Land Pattern



OVM	DIMENSIONS			
SYM	MILLIMETERS	INCHES		
С	2.50	0.098		
G	1.40	0.055		
Р	0.95	0.037		
Х	0.60	0.024		
Y	1.10	0.043		
Z	3.60	0.141		

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