

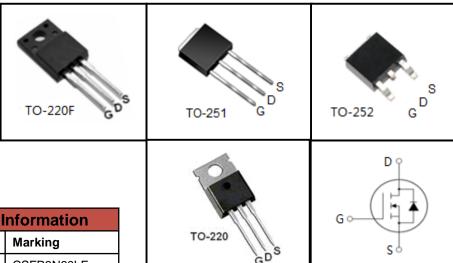
600V N-Channel MOSFET

FEATURES

- Fast switching
- Integrate fast recovery diode
- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Motor Controls
- Power Factor Correction (PFC)



Device Marking and Package Information						
Device	Package	Marking				
CSFR3N60LF	TO-220F	CSFR3N60LF				
CSFR3N60LP	TO-220	CSFR3N60LP				
CSFR3N60LU	TO-251	CSFR3N60LU				
CSFR3N60LD	TO-252	CSFR3N60LD				

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted								
Parameter	Comple at		Unit					
raiametei	Symbol	TO-220F	TO-220	TO-251	TO-252	Onit		
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	600			٧			
Continuous Drain Current	I _D	3				Α		
Pulsed Drain Current (note1)	I _{DM}	12				Α		
Gate-Source Voltage	V _{GSS}	±30			V			
Single Pulse Avalanche Energy (note2)	E _{AS}	192.2			mJ			
Avalanche Current (note1)	I _{AS}	3			Α			
Repetitive Avalanche Energy (note1)	E _{AR}	115.3			mJ			
Power Dissipation (T _C = 25°C)	P_{D}	25		30		W		
Operating Junction and Storage Temperature Range	T_J,T_stg	-55~+150				°C		

Thermal Resistance						
Barrantas	Symbol	Value				l lmi4
Parameter		TO-220F	TO-251	TO-252	TO-220	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	5		4.2		12/\\\
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5		60		K/W



CSFR3N60LF,CSFR3N60LP CSFR3N60LU,CSFR3N60LD

Specifications $T_J = 25^{\circ}C$, unless otherwise noted									
Parameter	Symbol	Toot Conditions	Value			Unit			
Parameter	rameter Symbol Test Conditions		Min.	Тур.	Max.	Unit			
Static									
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ			
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±100	nA			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	٧			
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_D = 1.25A$		3.2	3.8	Ω			
Dynamic									
Input Capacitance	C _{iss}	$V_{GS} = 0V$,		333		pF			
Output Capacitance	C _{oss}	$V_{DS} = 25V$,		36					
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		5					
Total Gate Charge	Q_g			12		nC			
Gate-Source Charge	Q_{gs}	$V_{DD} = 480V, I_{D} = 2.5A,$ $V_{GS} = 10V$		1.9					
Gate-Drain Charge	Q_{gd}	65 -		6.4					
Turn-on Delay Time	t _{d(on)}			34		ns			
Turn-on Rise Time	t _r	$V_{DD} = 300V, I_{D} = 2.5A,$		7					
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		65					
Turn-off Fall Time	t _f			25					
Drain-Source Body Diode Character	istics								
Continuous Body Diode Current	Is	T 0500			3	A			
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			10				
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 1.5\text{A}, V_{GS} = 0\text{V}$			1.4	V			
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 3A,$		59		ns			
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		0.05		μC			

Notes

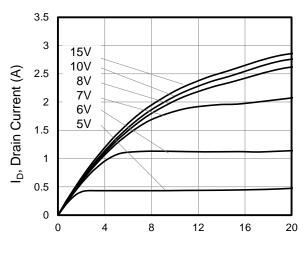
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Is, Source Current (A)

Figure 1. Output Characteristics (T_J = 25°C)



V_{DS}, Drain-to-Source Voltage (V)

Figure 3. Drain Current vs. Temperature

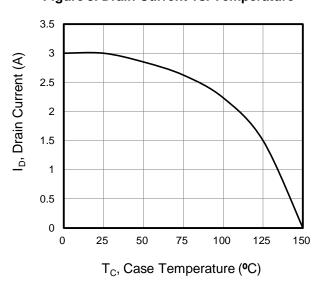


Figure 5. Transfer Characteristics

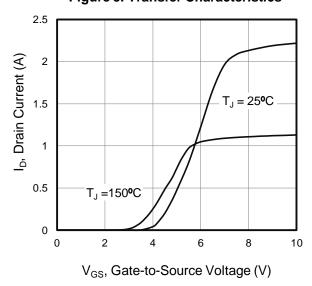


Figure 2. Body Diode Forward Voltage

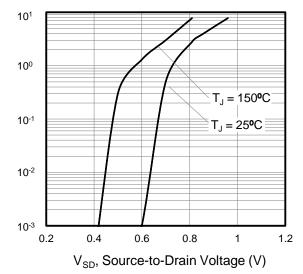


Figure 4. Power Dissipation vs. Temperature TO-220,TO-251,TO-252

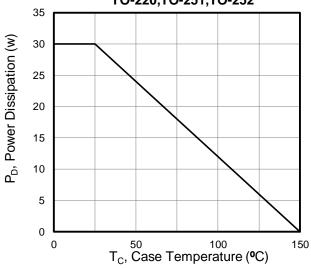
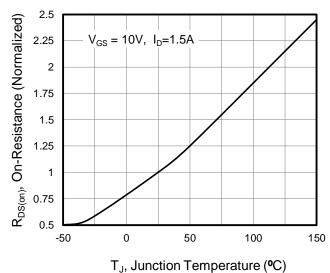


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25$ °C, unless otherwise noted

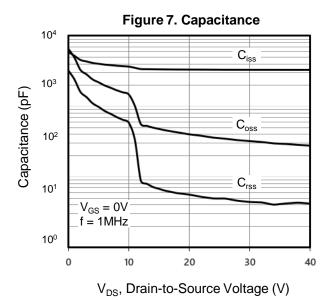
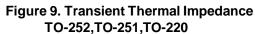
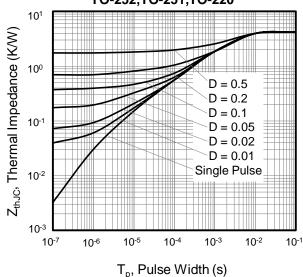


Figure 8. Gate Charge $V_{DD} = 120V$ $V_{DD} = 300V$ $V_{DD} = 480V$ $V_{DD} = 480V$ $V_{DD} = 480V$ $V_{DD} = 480V$





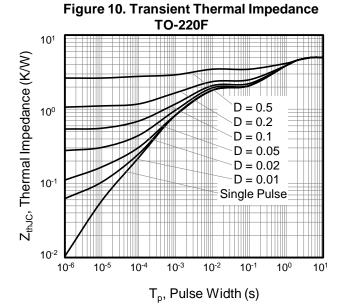




Figure A: Gate Charge Test Circuit and Waveform

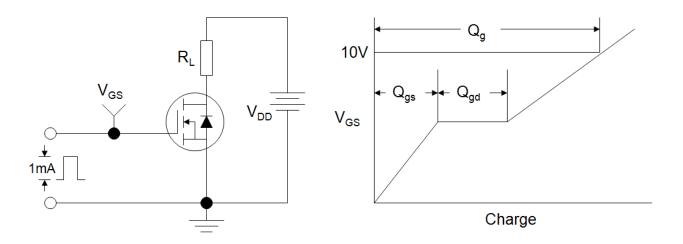


Figure B: Resistive Switching Test Circuit and Waveform

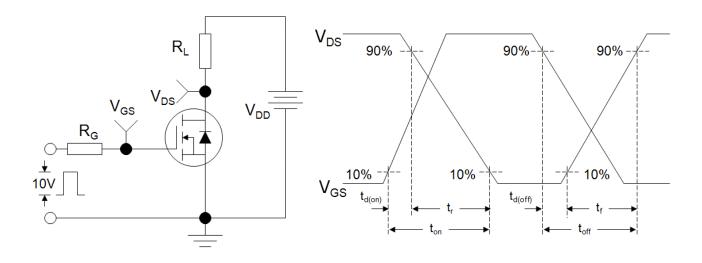
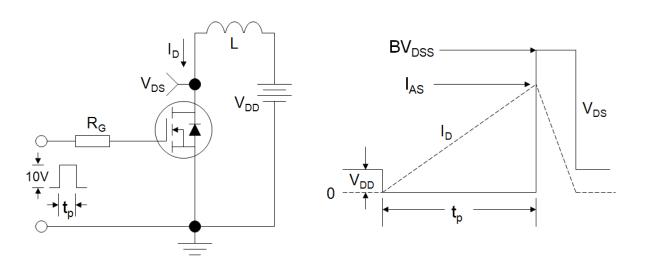
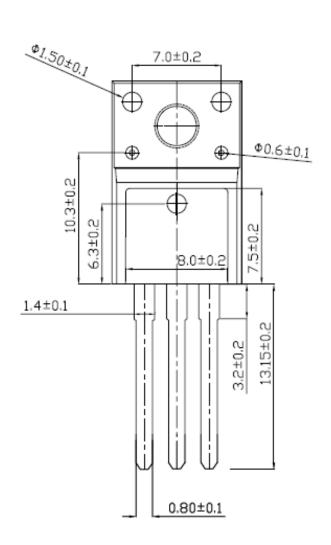


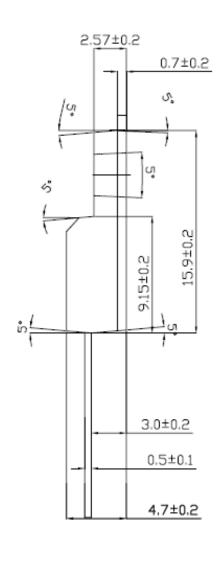
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





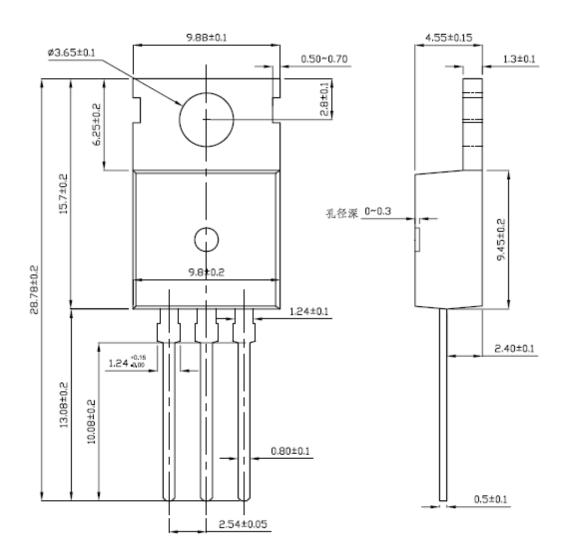
TO-220F





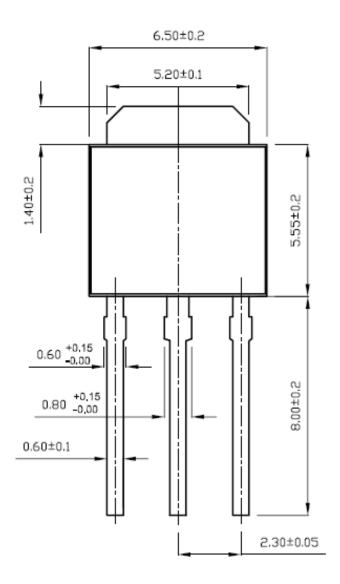


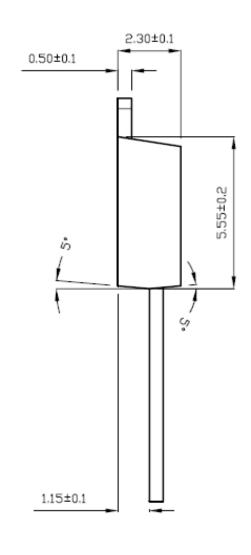
TO-220





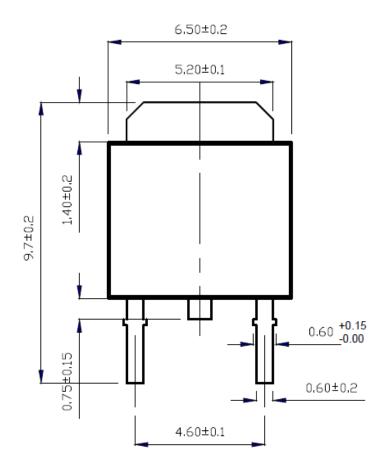
TO-251

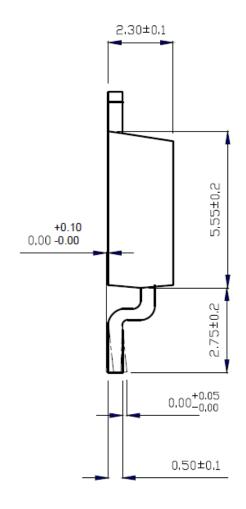






TO-252





CSFR3N60LF,CSFR3N60LP CSFR3N60LU,CSFR3N60LD

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