

# **650V Super-junction Power MOSFET**

### **Description**

#### 650V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

#### **Features**

- Ultra-fast body diode
- Very low FOM RDS(on)×Qg
- Easy to use/drive
- 100% avalanche tested
- RoHS compliant

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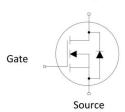
### **Applications**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LLC Half-bridge
- Charger

Drain

TO-252







### **Device Marking and Package Information**

Device	Package	Marking			
TPD65R700MFD	TO-252	65R700MFD			
Key Performance Parameters					
Parameter	Value	Unit			
V <sub>DS</sub> @ T <sub>j,max</sub>	700	V			
R <sub>DS(on),max</sub>	0.7	Ω			
$Q_{g,typ}$	14	nC			

יטי	•	,
I <sub>D,pulse</sub>	21	А
E <sub>OSS</sub> @ 400V	1.62	μJ
Body Diode di <sub>F</sub> /dt	500	A/µs
t <sub>rr</sub>	129	ns
Q <sub>rr</sub>	0.71	μС
I	11	A



<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted						
Parameter		Symbol	Value	Unit		
Continuous Dusin Comment	T <sub>C</sub> = 25°C		l <sub>D</sub>	7	^	
Continuous Drain Current	T <sub>C</sub> = 100°C			4.2	A	
Pulsed Drain Current	(	(note1)	I <sub>D,pulse</sub>	21	Α	
Gate-Source Voltage			$V_{GSS}$	±30	V	
Single Pulse Avalanche Energy (note2)		note2)	E <sub>AS</sub>	142	mJ	
Repetitive Avalanche Energy (note2)		E <sub>AR</sub>	0.21	mJ		
Avalanche Current			I <sub>AR</sub>	1.3	А	
MOSFET dv/dt Ruggedness, V <sub>DS</sub> = 0480V		dv/dt	50	V/ns		
Power Dissipation For TO-252			$P_{D}$	63	W	
Continuous Diode Forward Current			I <sub>S</sub>	7	^	
Diode Pulsed Current (note1)		(note1)	I <sub>S,pulse</sub>	21	_ A	
Reverse Diode dv/dt (note3)		(note3)	dv/dt	15	V/ns	
Maximum Diode Commutation Speed (note3)		(note3)	di <sub>f</sub> /dt	500	A/us	
Operating Junction and Storage Temperature Range			$T_J,T_stg$	-55~+150	°C	

Thermal Resistance For TO-252				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	2.0	°C/W	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	C/VV	



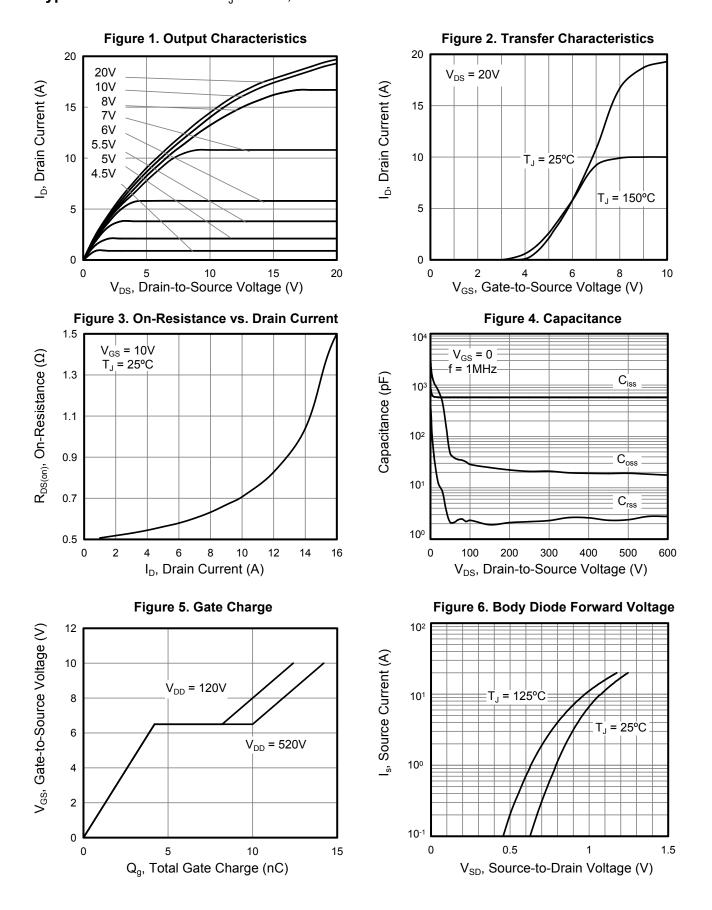
			Value			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max. Unit	
Static Characteristics	•			•		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	650			٧
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 650V$ , $V_{GS} = 0V$ , $T_{J} = 25^{\circ}C$			1	μΑ
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
Drain-Source On-State-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5A		0.61	0.7	Ω
Gate Resistance	$R_G$	f = 1.0MHz open drain		7		Ω
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	\/ - 0\/		563		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 100V,$		24		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		2.2		
Total Gate Charge	$Q_g$			14		
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 5200V, I_{D} = 7A,$ $V_{GS} = 10V$		4		nC
Gate-Drain Charge	$Q_{gd}$	00		6		
Turn-on Delay Time	t <sub>d(on)</sub>			57		
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 7A,		62		
Turn-off Delay Time	$t_{d(off)}$	$R_G = 25\Omega$		85		ns
Turn-off Fall Time	t <sub>f</sub>			44		
Drain-Source Body Diode Characte	ristics					
Body Diode Forward Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}$ , $I_{SD} = 3.5 \text{ A}$ , $V_{GS} = 0\text{V}$		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			129		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$V_R = 400V, I_F = 7A,$ $di_F/dt = 100A/\mu s$		0.71		μC
Peak Reverse Recovery Current	I <sub>rrm</sub>	,		11		Α

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 1.3A,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical  $R_{\rm G}$



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted





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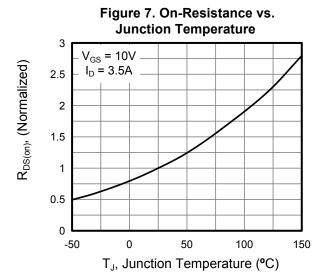


Figure 9. Transient Thermal Impedance For TO-252

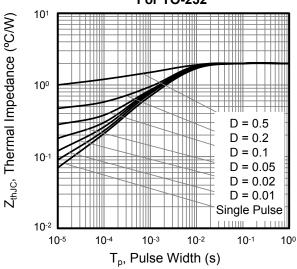


Figure 11. Typ. Coss Stored Energy

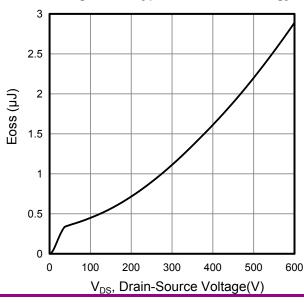


Figure 8.Breakdown voltage vs. Junction Temperature

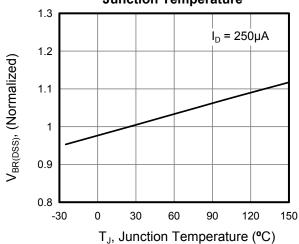


Figure 10. Safe Operation Area For TO-252

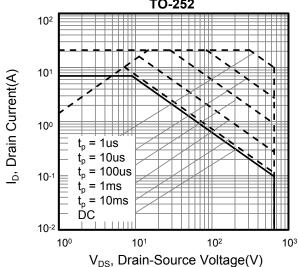




Figure A: Gate Charge Test Circuit and Waveform

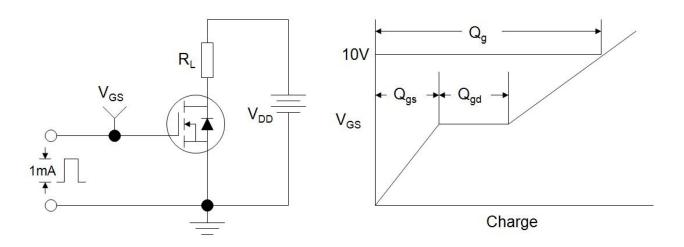


Figure B: Resistive Switching Test Circuit and Waveform

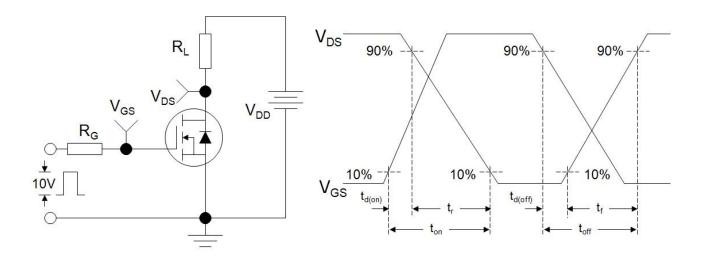
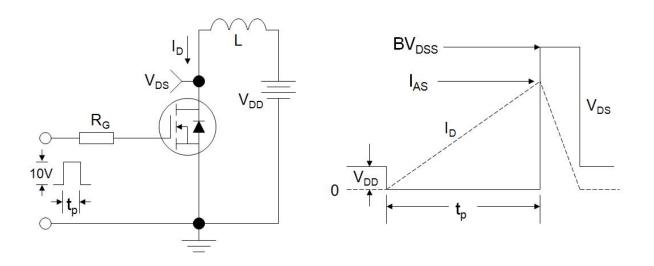
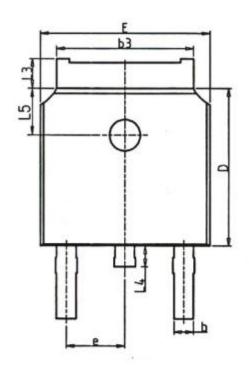


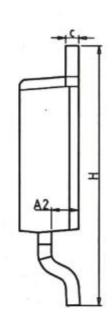
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

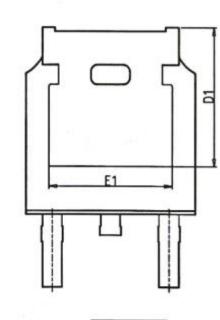


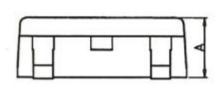


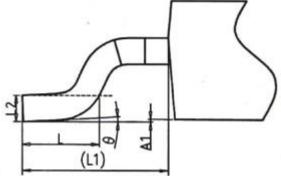
TO-252











Unit:mm					
Symbol	Min. Nom Max.				
Α	2.20	2.30	2.40		
A1	0.00	-	0.20		
A2	0.97	0.97 1.07			
b	0.68 0.78		0.90		
b3	5.20	5.33	5.50		
С	0.43	0.53	0.63		
D	5.98	6.10	6.22		
D1	5.30 REF				
E	6.40	6.60	6.80		
E1	4.63	-	-		

Unit:mm				
Symbol	Min.	Nom	Max.	
е		2.286 BSC		
Н	9.40 10.10 10.50			
L	1.38	1.50	1.75	
L1	2.90 REF			
L2	0.51 BSC			
L3	0.88	1.28		
L4	1	-	1.00	
L5	1.65 1.80 1.95			
θ	0°	-	8°	



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