

600V N-Channel MOSFET

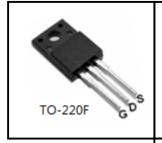
FEATURES

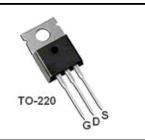
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

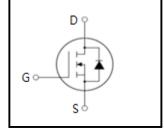
APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package	Marking	
CS12N60F	TO-220F	CS12N60F	
CS12N60P	TO-220	CS12N60P	







Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Paramatan.	Symbol	Va	11		
Parameter		TO-220F	TO-220	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	600		٧	
Continuous Drain Current	I _D	12		А	
Pulsed Drain Current (note1)	I _{DM}	48		А	
Gate-Source Voltage	V _{GSS}	±30		V	
Single Pulse Avalanche Energy (note2)	E _{AS}	405		mJ	
Avalanche Current (note1)	I _{AR}	9		А	
Repetitive Avalanche Energy (note1)	E _{AR}	263		mJ	
Power Dissipation (T _C = 25°C)	P _D	65	85	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C	

Thermal Resistance				
Parameter	Symbol	Va	l lmit	
Farameter		TO-220F	TO-220	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	1.92	1.2	0000
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	80	°C/W



Specifications $T_J = 25^{\circ}C$, unless otherwise noted								
Parameter	Symbol	Total October 1985 and	Value			11.24		
		Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA		
Gate-Source Leakage	$I_{\rm GSS}$	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, V_{DS} = 250\mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 6A$		0.4	0.62	Ω		
Dynamic								
Input Capacitance	C _{iss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$ f = 1.0MHz		1703		pF		
Output Capacitance	C _{oss}			162				
Reverse Transfer Capacitance	C _{rss}			19				
Total Gate Charge	Q_g	$V_{DD} = 480V, I_{D} = 12A,$ $V_{GS} = 10V$		49		nC		
Gate-Source Charge	Q_{gs}			7.5				
Gate-Drain Charge	Q_{gd}	55		23.5				
Turn-on Delay Time	t _{d(on)}			46				
Turn-on Rise Time	t _r	$V_{DD} = 300V, I_{D} = 12A,$		32		ns		
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		212				
Turn-off Fall Time	t _f			54				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	Is				12	A		
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			48			
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 12A$, $V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_S = 12.0A,$		616		ns		
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		3		μC		

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width $\leq 300 \mu s$, Duty Cycle $\leq 1\%$



Typical Characteristics $T_J = 25$ °C, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

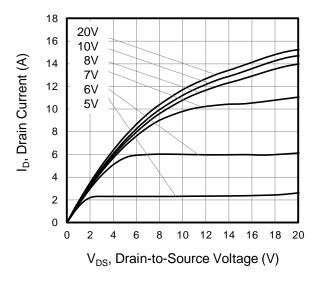


Figure 2. Body Diode Forward Voltage

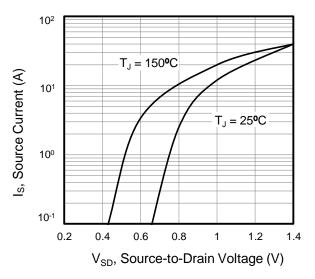


Figure 3. Drain Current vs. Temperature

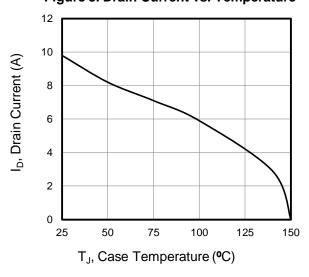


Figure 4. BV_{DSS} Variation vs. Temperature

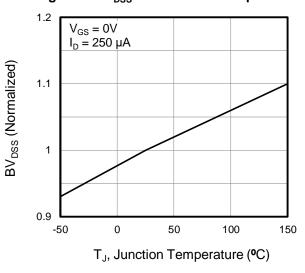


Figure 5. Transfer Characteristics

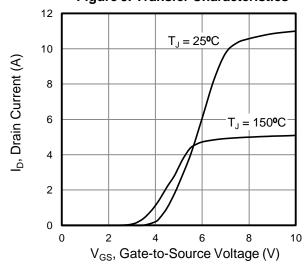
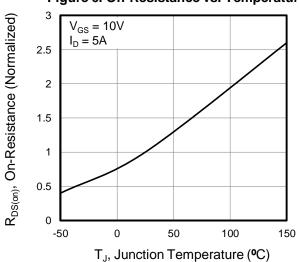


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

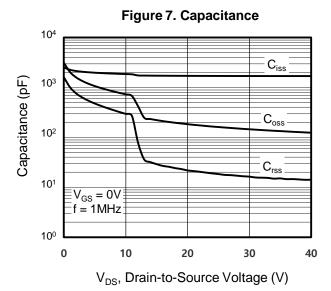


Figure 9. Transient Thermal Impedance

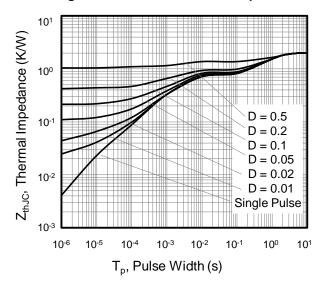




Figure A: Gate Charge Test Circuit and Waveform

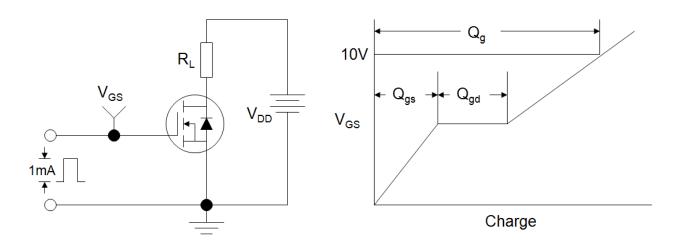


Figure B: Resistive Switching Test Circuit and Waveform

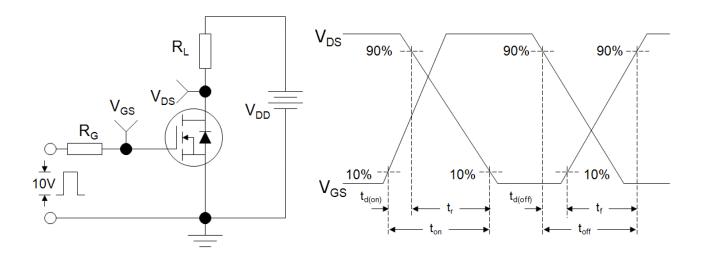
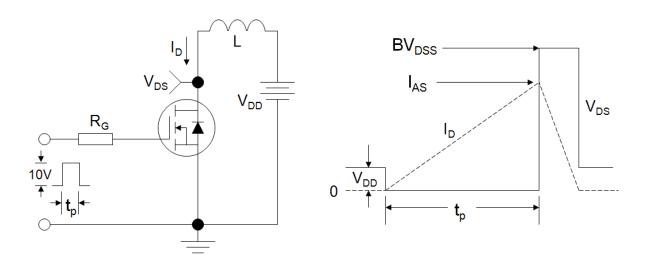
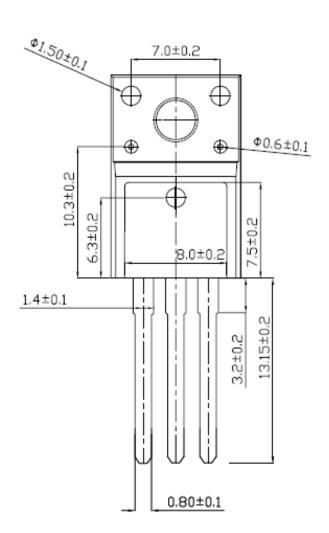


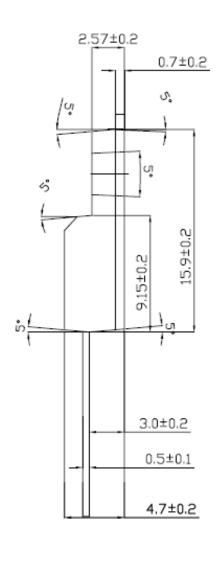
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





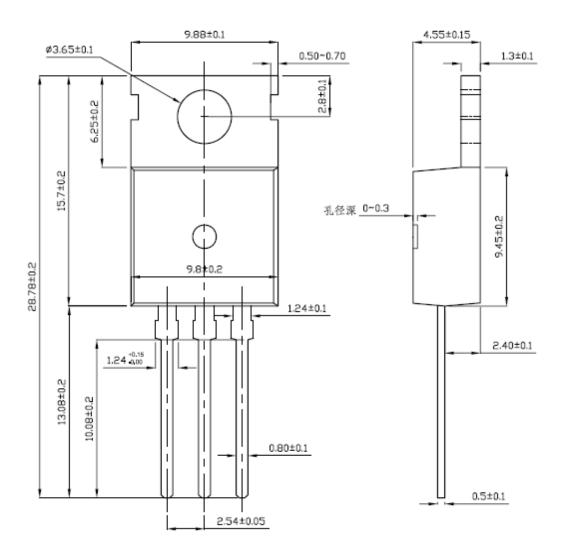
TO-220F







TO-220





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