

4N65K-TC 4A, 650V N-CHANNEL POWER MOSFET

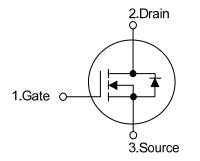
DESCRIPTION

The UTC **4N65K-TC** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristic. This power MOSFET is usually used in high speed switching applications including power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} \le 2.5\Omega$ @ $V_{GS}=10V$, $I_D=2.0A$
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

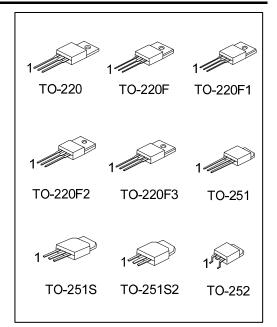
SYMBOL



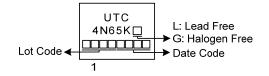
ORDERING INFORMATION

Ordering	Daakaga	Pin Assignment			Deaking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N65KL-TA3-T	4N65KG-TA3-T	TO-220	G	D	S	Tube	
4N65KL-TF1-T	4N65KG-TF1-T	TO-220F1	G	D	S	Tube	
4N65KL-TF2-T	4N65KG-TF2-T	TO-220F2	G	D	S	Tube	
4N65KL-TF3-T	4N65KG-TF3-T	TO-220F	G	D	S	Tube	
4N65KL-TF3T-T	4N65KG-TF3T-T	TO-220F3	G	D	S	Tube	
4N65KL-TM3-T	4N65KG-TM3-T	TO-251	G	D	S	Tube	
4N65KL-TMS2-T	4N65KG-TMS2-T	TO-251S2	G	D	S	Tube	
4N65KL-TN3-R	4N65KG-TN3-R	TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							

4N65K <u>G</u> - <u>TA3</u> - <u>T</u>			(1) T: Tube, R: Tape Reel	
		(1)Packing Type	(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2,	
		(2)Package Type	TF3: TO-220F, TF3T: TO-220F3, TM3: TO-251,	
		(3)Green Package	TMS: TO-251S, TMS2: TO-251S2, TN3: TO-252	
		(1)	(3) G: Halogen Free and Lead Free, L: Lead Free	
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MARKING





PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	650	V	
Gate-Source Voltage		V _{GSS}	±30	V	
Drain Current	Continuous	I _D	4.0	А	
Drain Current	Pulsed (Note2)	I _{DM}	16	А	
Avalanche Energy	Single Pulsed (Note3)	E _{AS}	113	mJ	
Peak Diode Recovery c	lv/dt (Note4)	dv/dt	3.79	V/ns	
	TO-220		106	W	
	TO-220F/TO-220F1		36	w	
Power Dissipation	TO-220F2/TO-220F3			••	
	TO-251/TO-251S TO-251S2/TO-252		50	W	
	TO-220	PD	0.84	W/°C	
Derate above 25°C	TO-220F/TO-220F1 TO-220F2/TO-220F3		0.29	W/°C	
	TO-251/TO-251S TO-251S2/TO-252		0.40	W/°C	
Junction Temperature		TJ	+150	°C	
Operating Temperature		T _{OPR}	-55 ~ +150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C	

■ ABSOLUTE MAXIMUM RATINGS (T_c = 25°C, unless otherwise specified)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

- 3. L=25mH, I_{AS} =3.0A, V_{DD} =50V, R_G =25 Ω , Starting T_J = 25°C
- 4. I_{SD}≤4.0A, di/dt≤200A/µs, V_{DD}≤ BV_{DSS}, Starting T_J = 25°C

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3	θ _{JA}	62.5	°C/W
	TO-251/TO-251S TO-251S2/TO-252		83	°C/W
Junction to Case	TO-220		1.18	°C/W
	TO-220F/TO-220F1 TO-220F3	<u> </u>	3.4	°C/W
	TO-220F2	θ _{JC}	3.57	°C/W
	TO-251/TO-251S TO-251S2/TO-252		2.5 (Note)	°C/W

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.



PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	МАХ	UNIT
OFF CHARACTERISTICS		OTHEOL				100 0 0	0.111
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250µA	650			V
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$			10	μA
Forwar			V _{GS} = 30 V, V _{DS} = 0 V			100	nA
Gate-Source Leakage Current	everse	1000	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS} / \triangle T_J$	I _D =250µA, Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resista	ance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 2.0A		2.2	2.5	Ω
DYNAMIC CHARACTERISTICS		<u> </u>					
Input Capacitance Output Capacitance		CISS			600		pF
		C _{OSS}	V _{DS} =25V, V _{GS} =0V, f=1MHz		53.8		pF
Reverse Transfer Capacitance		C _{RSS}			3.2		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge		Q_{G}			13		nC
Gate-Source Charge		Q_{GS}	V _{DS} =100V, V _{GS} =10V, I _D =3.0A I _G = 1mA (Note1, 2)		3.6		nC
Gate-Drain Charge		Q_{GD}	$I_G = IIIIA (INOLE I, Z)$		2		nC
Turn-On Delay Time		t _{D(ON)}			30		ns
Turn-On Rise Time		t _R	V _{DS} =100V, V _{GS} =10V, I _D =2.0A,		10		ns
Turn-Off Delay Time		t _{D(OFF)}	R _G =25Ω (Note1, 2)		60		ns
Turn-Off Fall Time		t _F			50		ns
SOURCE- DRAIN DIODE RATINGS	AND (CHARACTERIS	STICS				
Maximum Continuous Drain-Source	Diode					4.0	^
Forward Current		I _S				4.0	A
Maximum Pulsed Drain-Source Diode	Drain-Source Diode					16	А
Forward Current		I _{SM}				10	А
Drain-Source Diode Forward Voltage		V_{SD}	V _{GS} = 0 V, I _S = 4.0A			1.4	V
Reverse Recovery Time		t _{rr}	V _{GS} = 0V, I _S = 4.0A, 23		230		nS
Reverse Recovery Charge		Q _{rr}	dI _F / dt =100A/µs (Note 1)		1.6		μC

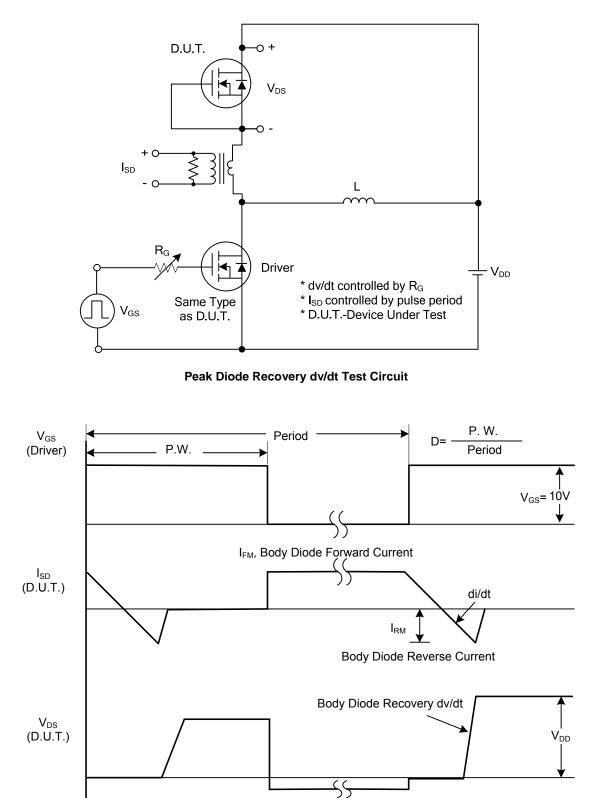
■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

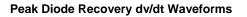
Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

2. Essentially independent of operating temperature.



TEST CIRCUITS AND WAVEFORMS





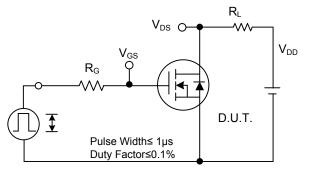
Body Diode

Forward Voltage Drop

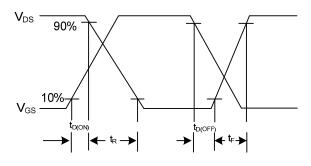


Power MOSFET

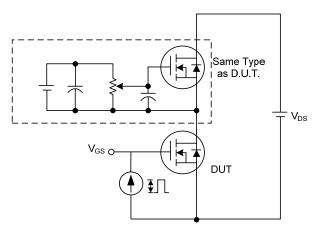
TEST CIRCUITS AND WAVEFORMS



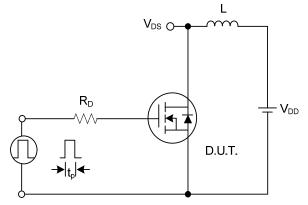
Switching Test Circuit



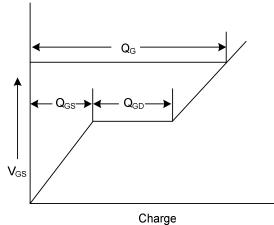
Switching Waveforms



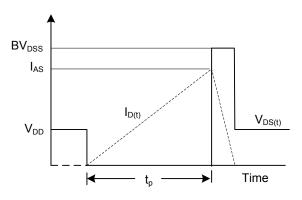
Gate Charge Test Circuit

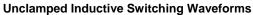


Unclamped Inductive Switching Test Circuit



Gate Charge Waveform

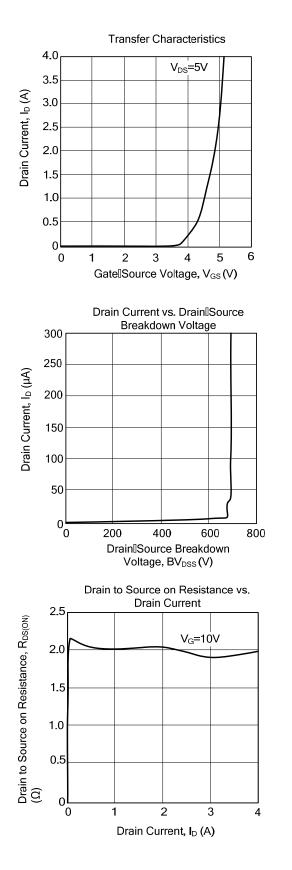


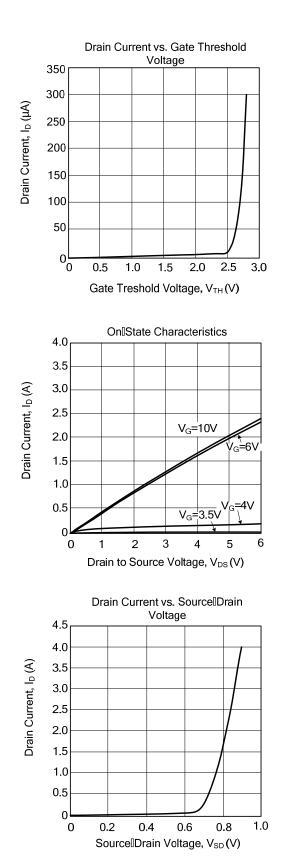




Power MOSFET

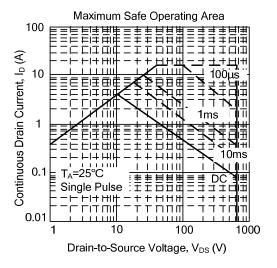
TYPICAL CHARACTERISTICS







TYPICAL CHARACTERISTICS (Cont.)



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