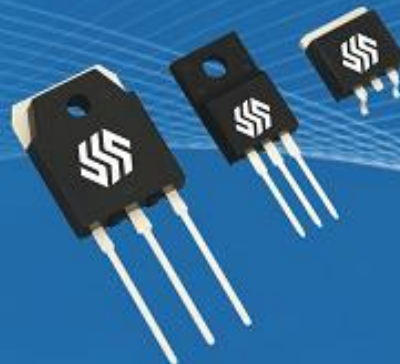




SUPER-SEMI



SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

650V Super Junction Power MOSFET Gen- II
SST65R280S2

Rev. 1.1
Sep. 2019

www.supersemi.com.cn



SST65R280S2

650V N-Channel Super-Junction MOSFET Gen-II

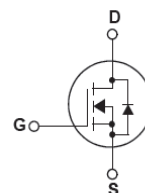
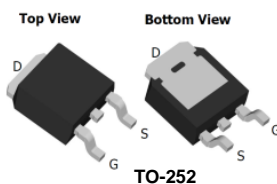
Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- Multi-Epi process SJ-FET
- 700V @T_J = 150 °C
- Typ. RDS(on) = 0.23Ω
- Ultra Low Gate Charge (typ. Q_g = 28nC)
- 100% avalanche tested

SST65R280S2



Absolute Maximum Ratings

Symbol	Parameter	SST65R280S2	Unit
V _{DSS}	Drain-Source Voltage	650	V
I _D	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	15* 9.5*	A
I _{DM}	Drain Current - Pulsed (Note 1)	58	A
V _{GSS}	Gate-Source voltage	±30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	235	mJ
I _{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by T _J max)	2.8	A
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15	V/ns
dVds/dt	Drain Source voltage slope (V _{ds} =480V)	50	V/ns
P _D	Power Dissipation (TC = 25°C)	120	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/16" from Case for 10 Seconds	260	°C

* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75

Thermal Characteristics

Symbol	Parameter	SST65R280S2	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	1	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	°C/W



Electrical Characteristics TC = 25°C unless otherwise noted

SST65R280S2 650V N-Channel Super-Junction MOSFET Gen-II

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	650	-	-	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	-	700	-	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.6	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650V, V _{GS} = 0V -T _C = 125°C	-	-	1 100	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	3.0	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 7.5A	-	0.23	0.28	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 100V, V _{GS} = 0V, f = 1.0MHz	-	1050	-	pF
C _{oss}	Output Capacitance		-	37	-	pF
C _{rss}	Reverse Transfer Capacitance		-	1.1	-	pF
Q _g	Total Gate Charge	V _{DS} = 400V, I _D = 8A, V _{GS} = 10V (Note 4)	-	28	-	nC
Q _{gs}	Gate-Source Charge		-	6.2	-	nC
Q _{gd}	Gate-Drain Charge		-	9.8	-	nC
R _g	Gate resistance	f=1 MHz, open drain	-	13	-	Ω
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} = 400V, I _D = 8A R _G = 15Ω, V _{GS} = 12V (Note 4)	-	17	-	ns
t _r	Turn-On Rise Time		-	18	-	ns
t _{d(off)}	Turn-Off Delay Time		-	89	-	ns
t _f	Turn-Off Fall Time		-	20	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	15	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	58	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 15A	-	0.9	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, V _{DS} = 400V, I _S = 8A, di _f /dt = 100A/μs	-	285	-	ns
Q _{rr}	Reverse Recovery Charge		-	3.1	-	μC
I _{rrm}	Peak Reverse Recovery Current		-	22	-	A

NOTES:

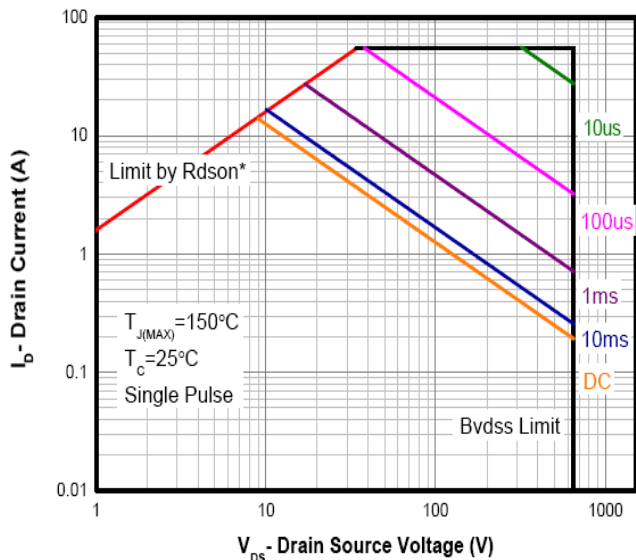
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I_D = I_{AS}, V_{DD} = 50V, Starting T_J = 25 °C
3. I_{SD} ≤ I_D, di/dt ≤ 200A/μs, V_{DP} ≤ BV_{DSS}, Starting T_J = 25 °C
4. Essentially Independent of Operating Temperature Typical Characteristics



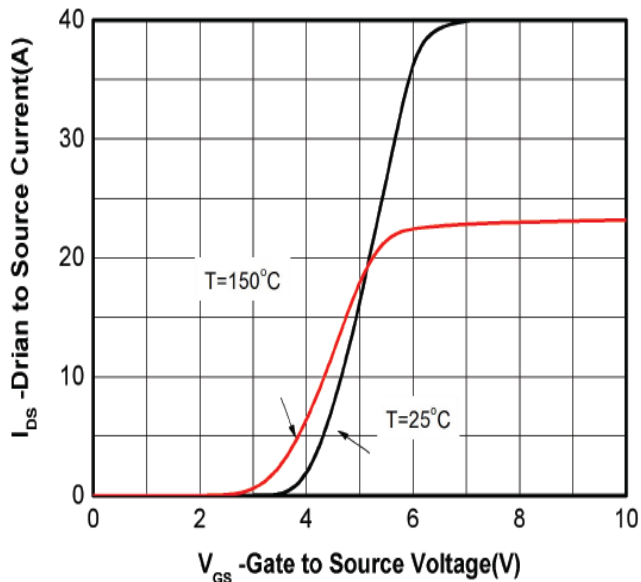
Typical Performance Characteristics

SST65R280S2 650V N-Channel Super-Junction MOSFET Gen-II

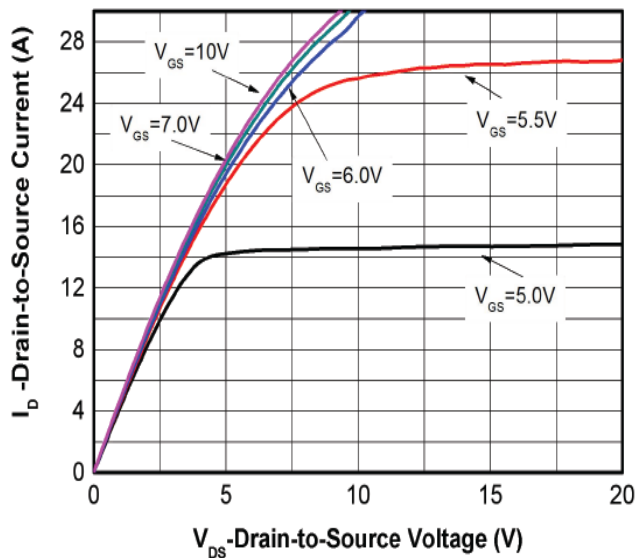
Safe operating area TC=25 °C
Non FullPAK



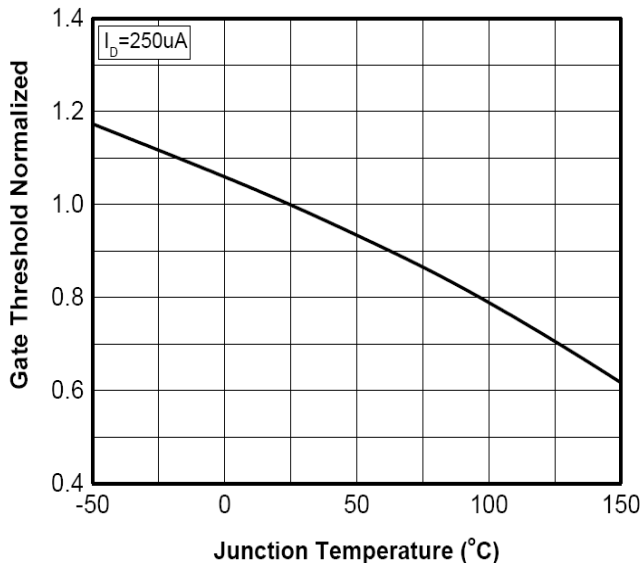
Typ. transfer characteristics



Typ. output characteristics $T_j=25\text{ }^\circ\text{C}$



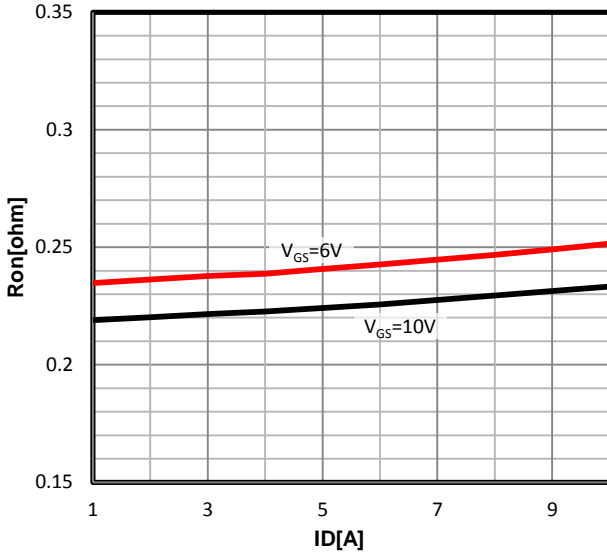
Normalized $V_{GS(th)}$ characteristics



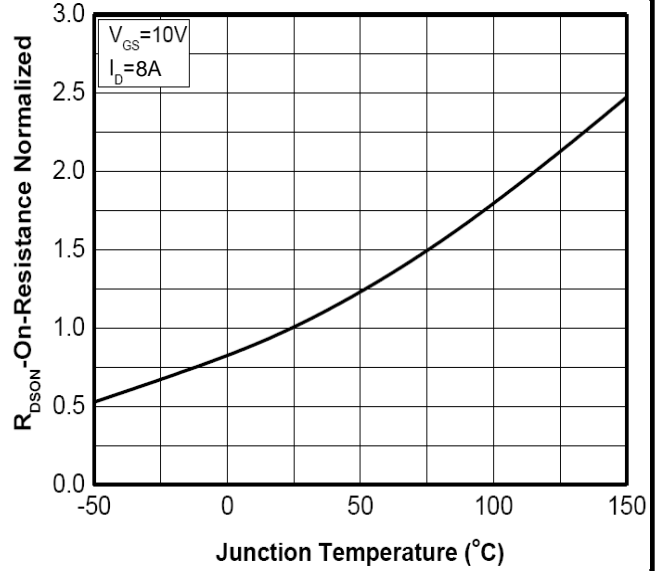


Typical Performance Characteristics

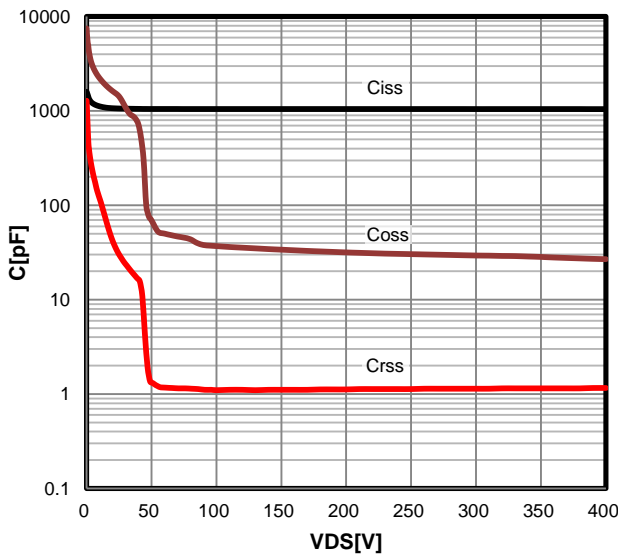
Typ. drain-source on-state resistance



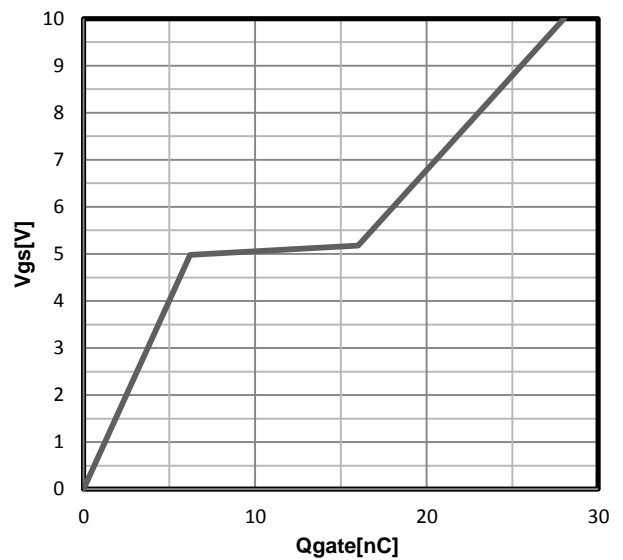
Normalized on resistance vs temperature



Typ. capacitances



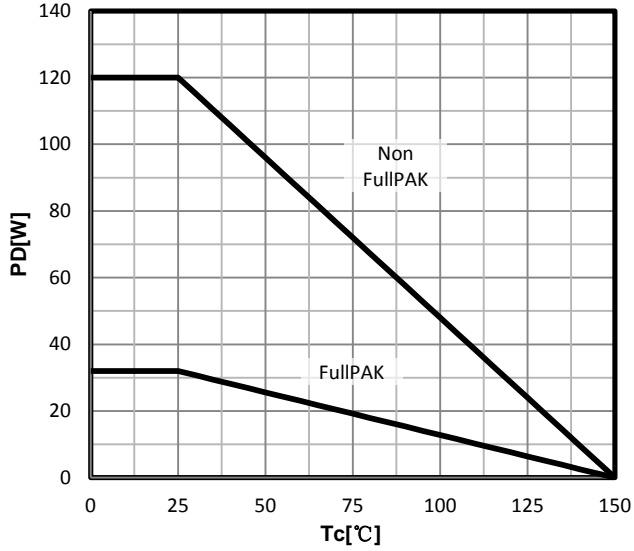
Typ. gate charge characteristics



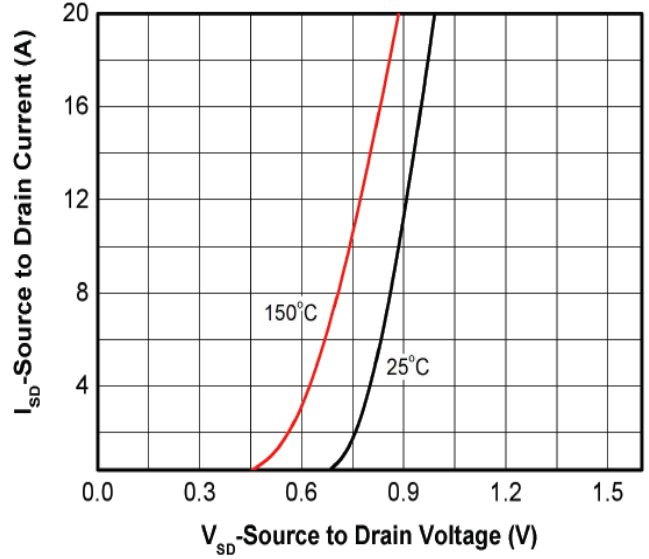


Typical Performance Characteristics

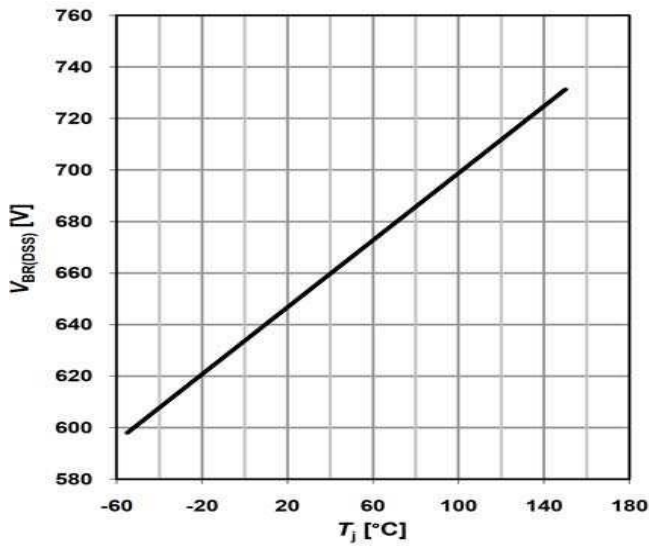
Power dissipation



Forward characteristics of reverse diode



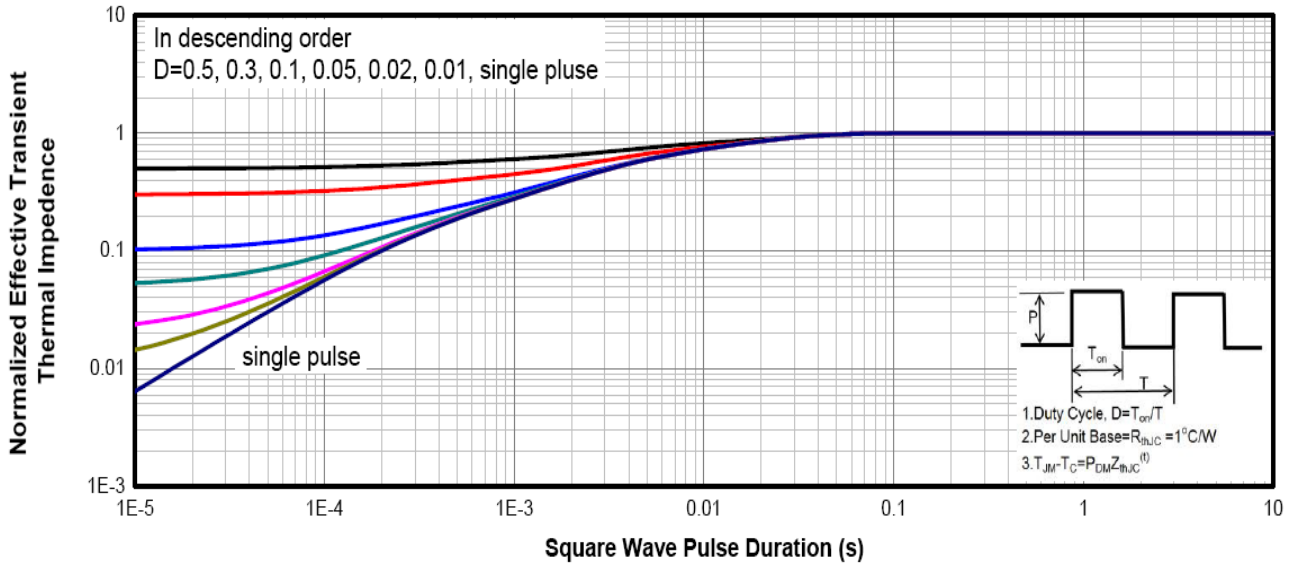
Drain-source breakdown voltage





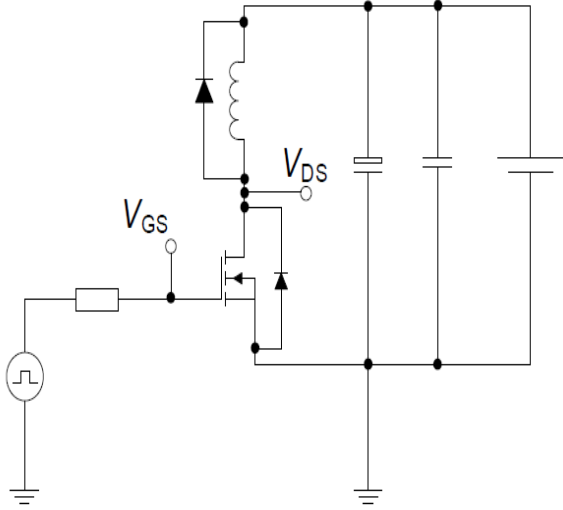
Typical Performance Characteristics

Max. transient thermal impedance
Non FullPAK

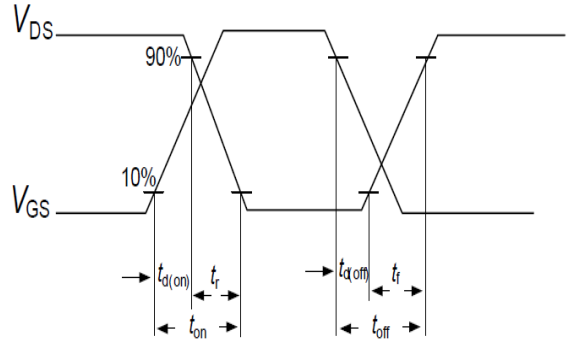


Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

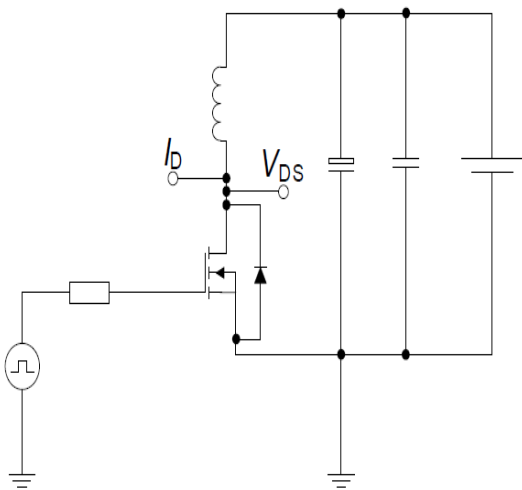


Switching time waveform

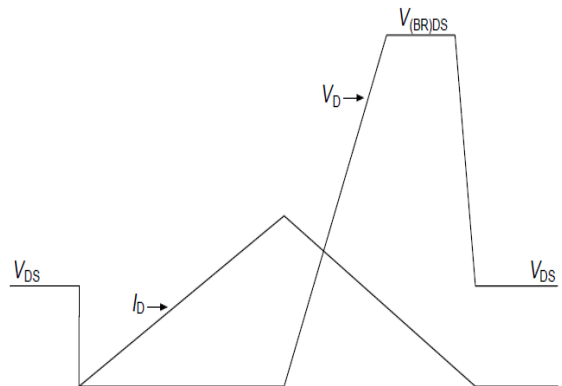


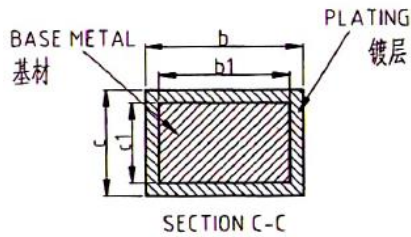
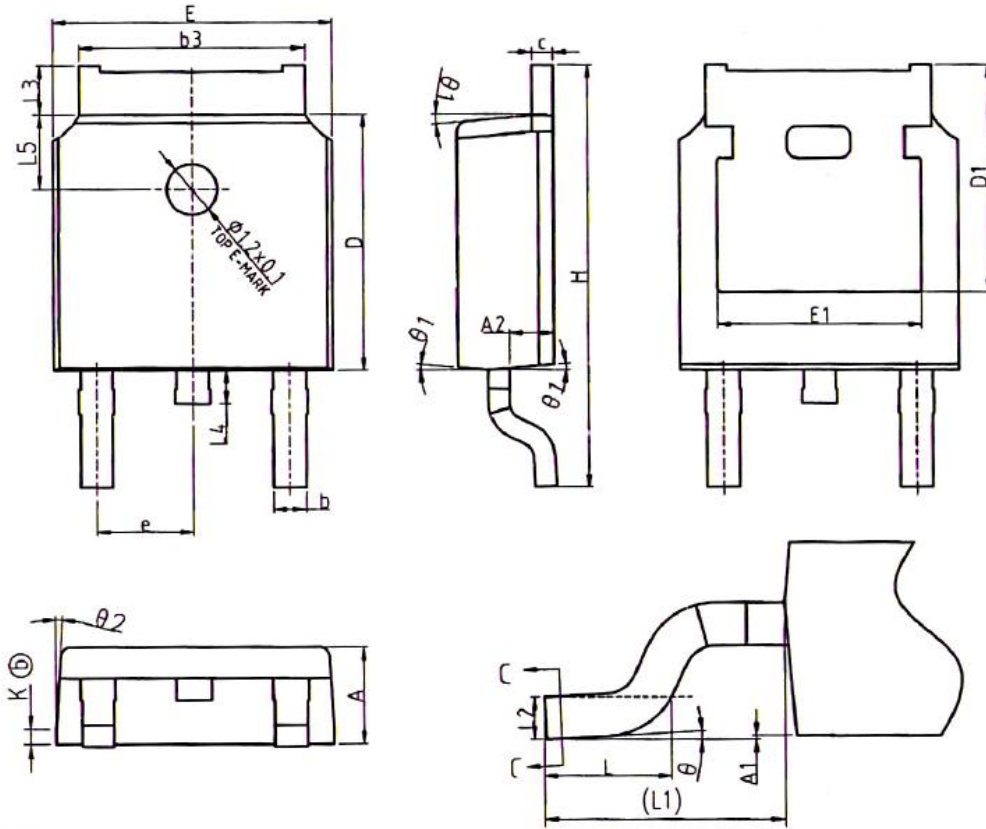
Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit



Unclamped inductive waveform





COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	9.90	10.10	10.30
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.51BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	1.70	1.80	1.90
θ	0°	-	8°
$\theta 1$	5°	7°	9°
$\theta 2$	5°	7°	9°
K	0.40REF		



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