74AUP3G04

Low-power triple inverter

Rev. 9 — 24 July 2019

Product data sheet

1. General description

The 74AUP3G04 provides a low-power, low-voltage triple inverting buffer.

Schmitt trigger action at all inputs makes the circuit tolerant of slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- · High noise immunity
- · Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- · Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



Low-power triple inverter

3. Ordering information

Table 1. Ordering information

Type number	Package								
	Temperature range	Name	Description	Version					
74AUP3G04DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1					
74AUP3G04GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm	SOT833-1					
74AUP3G04GF	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1 x 0.5 mm	SOT1089					
74AUP3G04GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm	SOT1116					
74AUP3G04GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1.0 x 0.35 mm	SOT1203					

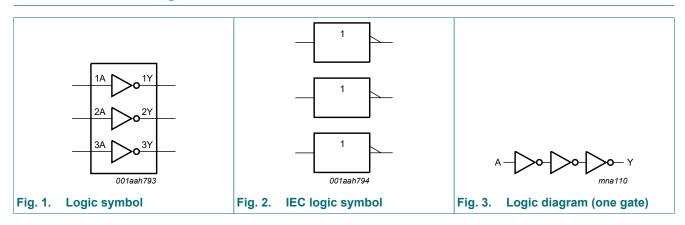
4. Marking

Table 2. Marking codes

Type number	Marking code[1]
74AUP3G04DC	p04
74AUP3G04GT	p04
74AUP3G04GF	p4
74AUP3G04GN	p4
74AUP3G04GS	p4

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

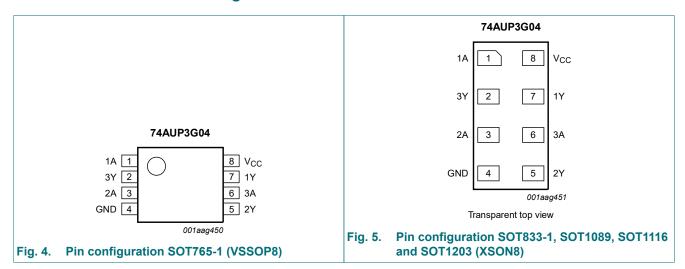
5. Functional diagram



Low-power triple inverter

6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
1A, 2A, 3A	1, 3, 6	data input
1Y, 2Y, 3Y	7, 5, 2	data output
GND	4	ground (0 V)
Vcc	8	supply voltage

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
nA	nY
L	Н
Н	L

Low-power triple inverter

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
V _I	input voltage	[1]	-0.5	+4.6	V
lok	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	V _O = 0 V to V _{CC}	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT833-1 (XSON8) packages: Ptot derates linearly with 3.1 mW/K above 68 °C.

For SOT1089 (XSON8) packages: Ptot derates linearly with 4.0 mW/K above 88 °C.

For SOT1116 (XSON8) packages: Ptot derates linearly with 4.2 mW/K above 90 °C.

For SOT1203 (XSON8) packages: Ptot derates linearly with 3.6 mW/K above 81 °C.

9. Recommended operating conditions

Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
V _I	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	-	200	ns/V

^[2] For SOT765-1 (VSSOP8) packages: Ptot derates linearly with 4.9 mW/K above 99 °C.

Low-power triple inverter

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	25 °C				1	
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH}$ or V_{IL}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
lį	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.2	μΑ
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μΑ
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	V_{CC} = 0 V to 3.6 V; V_I = GND or V_{CC}	-	1.0	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.8	-	pF

Symbo	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} =	-40 °C to +85 °C				-	
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I_{O} = -3.1 mA; V_{CC} = 2.3 V	1.85	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.67	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.5	μA
ΔI _{OFF}	additional power-off leakage current	V ₁ or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	μΑ
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ
T _{amb} =	-40 °C to +125 °C				'	
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V

Low-power triple inverter

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μΑ
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μΑ
ΔI _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	ter Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
				Min	Typ[1]	Max	Min	Max	Min	Max	
$C_L = 5 p$	F							•			
t _{pd}	propagation	nA to nY; see Fig. 6	[2]								
	delay	V _{CC} = 0.8 V		-	16.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V		2.4	5.0	10.3	2.1	11.4	2.1	12.6	ns
		V _{CC} = 1.4 V to 1.6 V		1.8	3.6	6.4	1.6	7.4	1.6	8.2	ns
		V _{CC} = 1.65 V to 1.95 V		1.5	2.9	5.0	1.4	5.9	1.4	6.5	ns
		V _{CC} = 2.3 V to 2.7 V		1.2	2.4	3.9	1.1	4.5	1.1	5.0	ns
		V _{CC} = 3.0 V to 3.6 V		1.1	2.1	3.2	1.0	3.9	1.0	4.3	ns

7 / 17

Low-power triple inverter

Symbol	Parameter	arameter Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 10	pF									
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	19.8	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	5.9	12.2	2.6	13.7	2.6	15.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.2	7.5	2.1	8.7	2.1	9.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.5	5.9	1.8	7.0	1.8	7.7	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	2.9	4.6	1.5	5.4	1.5	6.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	2.7	3.8	1.4	4.5	1.4	5.0	ns
C _L = 15	pF						'	'	'	•
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	23.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	6.7	13.0	3.0	15.8	3.0	17.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.7	8.6	2.4	10.0	2.4	11.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.0	6.7	2.1	8.0	2.1	8.8	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.3	5.1	1.8	6.1	1.8	6.8	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.1	4.2	1.8	5.0	1.8	5.5	ns
C _L = 30	pF									
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
·	delay	V _{CC} = 0.8 V	-	33.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.4	8.9	16.0	4.0	19.0	4.0	20.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.6	6.3	10.8	3.2	12.9	3.2	14.2	ns
		V _{CC} = 1.65 V to 1.95 V	3.2	5.3	9.0	2.9	10.5	2.9	11.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.9	4.5	6.5	2.6	7.6	2.6	8.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.9	4.2	5.4	2.6	6.2	2.6	6.9	ns
C _L = 5 p	F, 10 pF, 15 pF	and 30 pF								
C _{PD}	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3][4]								
	dissipation	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF
	capacitance	V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.5	-	-	_	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	_	-	_	pF

- All typical values are measured at nominal V_{CC}.
- [1] [2]
- [2] t_{pd} is the same as t_{PLH} and t_{PHL}.
 [3] All specified values are the average typical values over all stated loads.
- [4] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = load capacitance in pF;

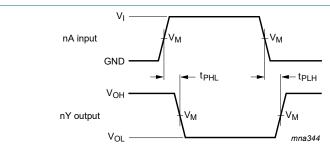
V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Low-power triple inverter

11.1. Waveforms and test circuit



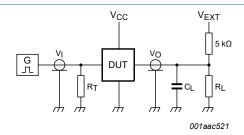
Measurement points are given in Table 9.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 6. The data input (nA) to output (nY) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input					
V _{CC}	V _M	V_{M} V_{I} $t_{r} = t_{f}$					
0.8 V to 3.6 V	0.5V _{CC}	0.5V _{CC}	V _{CC}	≤ 3.0 ns			



Test data is given in <u>Table 10</u>.

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig. 7. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}		
V _{CC}	C _L	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF, and 30 pF	5 kΩ or 1 MΩ	open	GND	2V _{CC}

[1] For measuring enable and disable times, R_L = 5 k Ω . For measuring propagation delays, setup and hold times, and pulse width, R_L = 1 M Ω .

Low-power triple inverter

12. Package outline

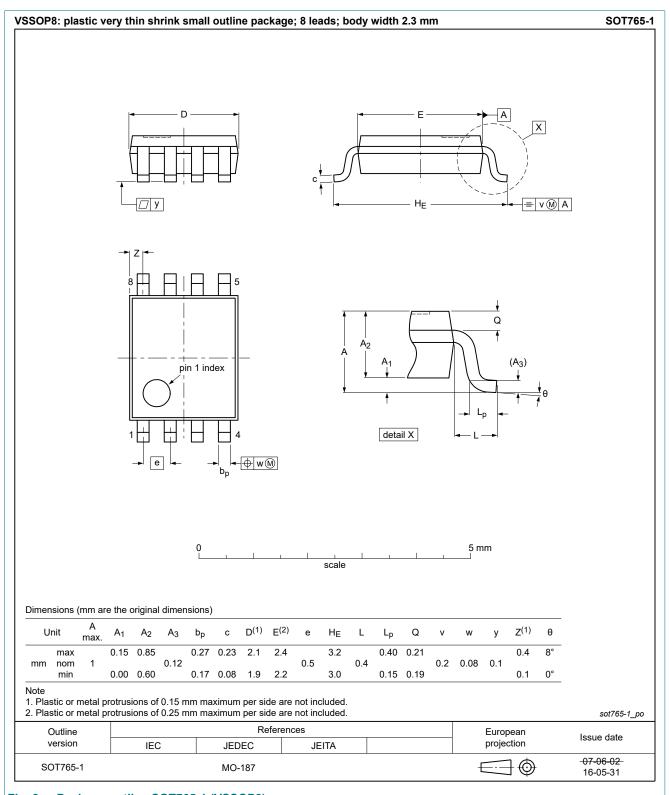


Fig. 8. Package outline SOT765-1 (VSSOP8)

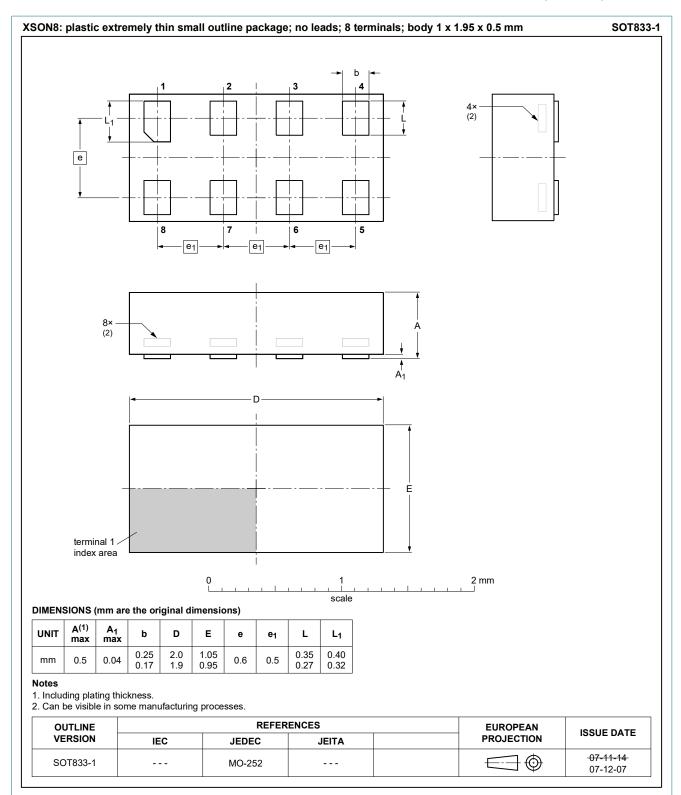


Fig. 9. Package outline SOT833-1 (XSON8)

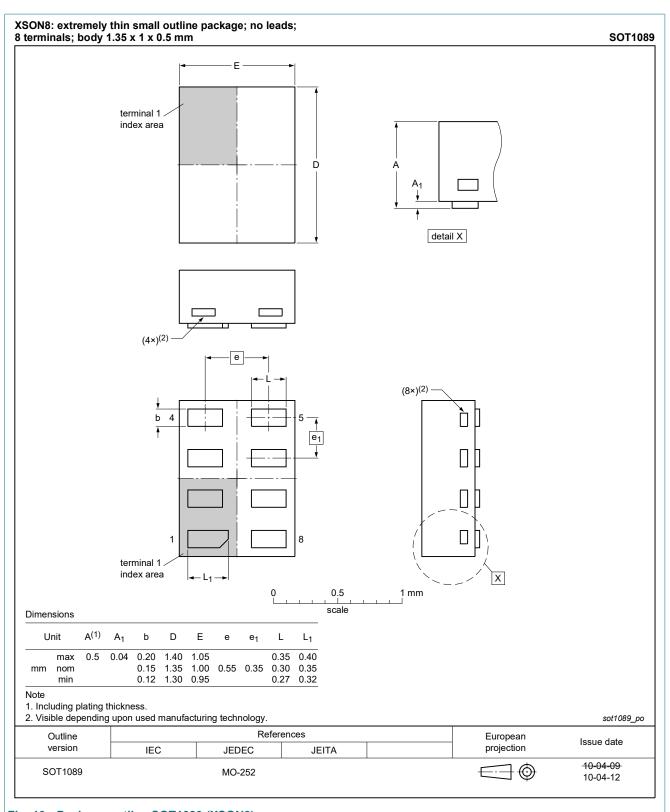


Fig. 10. Package outline SOT1089 (XSON8)

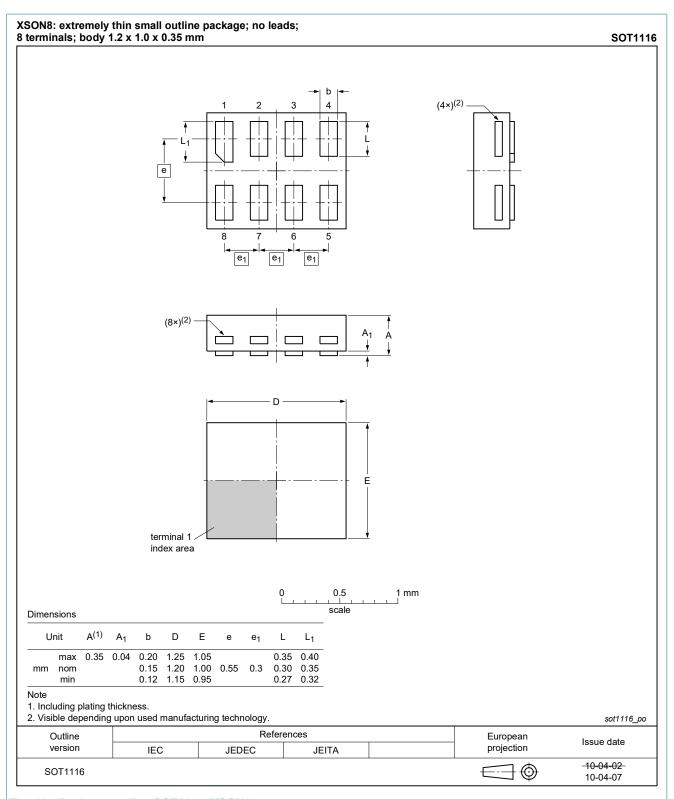


Fig. 11. Package outline SOT1116 (XSON8)

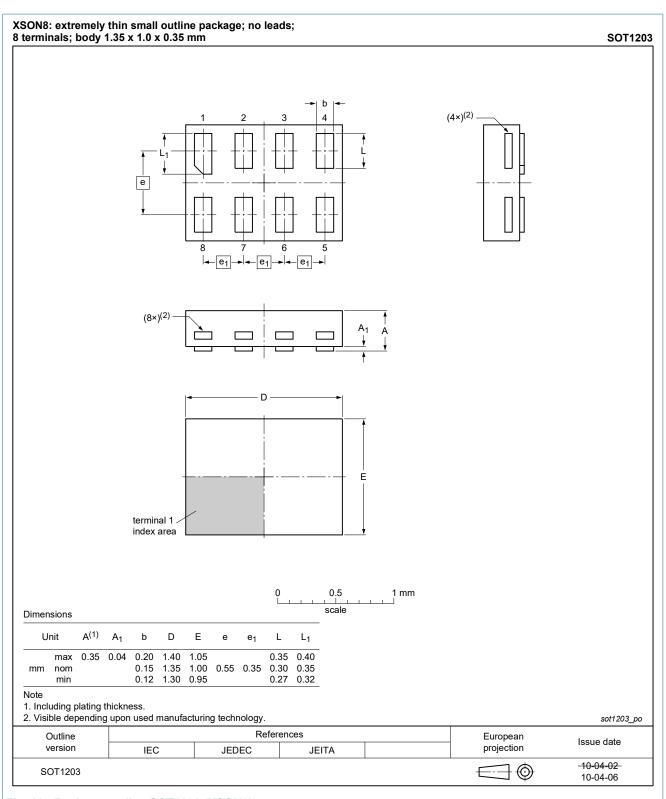


Fig. 12. Package outline SOT1203 (XSON8)

Low-power triple inverter

13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP3G04 v.9	20190724	Product data sheet	-	74AUP3G04 v.8	
Modifications:	Layout of <u>Tab</u>	74AUP3G04GM (SOT902-2/Xole 8 Dynamic characteristics unting values for P _{tot} total power	pdated.		
74AUP3G04 v.8	20190425	Product data sheet	-	74AUP3G04 v.7	
Modifications:	Nexperia. Legal texts ha Type number	this data sheet has been rede eve been adapted to the new of 74AUP3G04GD (SOT996-2/X ine drawing SOT765-1 and SC	company name where (SON8) removed.	, ,	
74AUP3G04 v.7	20130129	Product data sheet	-	74AUP3G04 v.6	
Modifications:	For type number 74AUP3G04GD XSON8U has changed to XSON8.				
74AUP3G04 v.6	20120614	Product data sheet	-	74AUP3G04 v.5	
74AUP3G04 v.5	20111209	Product data sheet	-	74AUP3G04 v.4	
74AUP3G04 v.4	20100730	Product data sheet	-	74AUP3G04 v.3	
74AUP3G04 v.3	20091008	Product data sheet	-	74AUP3G04 v.2	
74AUP3G04 v.2	20080313	Product data sheet	-	74AUP3G04 v.1	
74AUP3G04 v.1	20070524	Product data sheet	-	-	

15 / 17

Low-power triple inverter

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Low-power triple inverter

Contents

1.	General description	1
2.	Features and benefits	1
3.	Ordering information	2
4.	Marking	2
5.	Functional diagram	2
6.	Pinning information	3
6.1	. Pinning	3
6.2	. Pin description	3
7.	Functional description	3
8.	Limiting values	4
9.	Recommended operating conditions	4
10.	Static characteristics	5
11.	Dynamic characteristics	7
11.	Waveforms and test circuit	9
12.	Package outline	10
13.	Abbreviations	15
14.	Revision history	15
15.	Logal information	46
	Legal information	10

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