

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020MD-6 (SOT1220) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Trench MOSFET technology
- · Side wettable flanks for optical solder inspection
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Exposed drain pad for excellent thermal conduction

3. Applications

- Charging switch for portable devices
- DC-to-DC converters
- Power management in battery-driven portable devices
- · Hard disk and computing power management

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-12	V
V _{GS}	gate-source voltage	_		-8	-	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-15	А
Static chara	acteristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = -4.5 V; I _D = -10 A; T _j = 25 °C		-	10	11.5	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².

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5. Pinning information

Table 2	. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain		
3	G	gate		G-UFTY
4	S	source	3 8 4	s s
5	D	drain	Transparent top view	017aaa257
6	D	drain	DFN2020MD-6 (SOT1220)	
7	D	drain		
8	S	source		

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMPB10UP		plastic, leadless thermal enhanced ultra thin small outline package with side-wettable flanks (SWF); 6 terminals; 0.65 mm pitch; 2 mm x 2 mm x 0.65 mm body	SOT1220			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMPB10UP	6G

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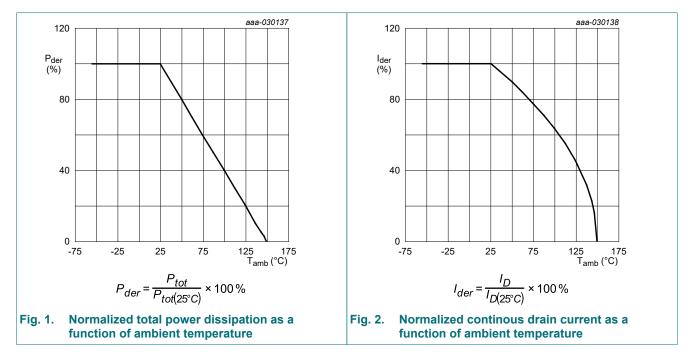
8. Limiting values

Table 5. Limiting values

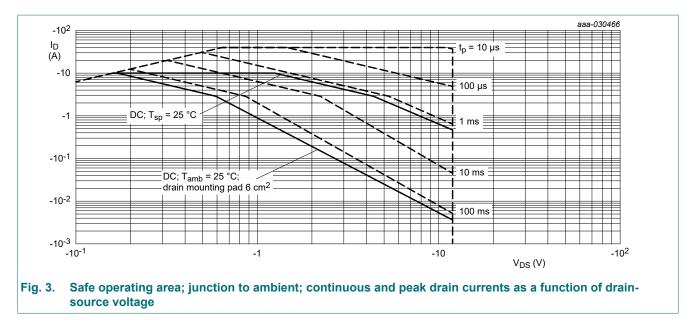
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-12	V
V _{GS}	gate-source voltage	_		-8	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-15	A
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-10	A
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-6.3	A
I _{DM}	peak drain current	T_{amb} = 25 °C; pulsed; $t_p \le 10 \ \mu s$		-	-40	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[1]	-	1.7	W
		T _{amb} = 25 °C; t ≤ 5 s	[1]	-	3.5	W
		T _{sp} = 25 °C		-	13	W
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode					- 1
I _S	source current	T _{amb} = 25 °C	[1]	-	-1.6	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².



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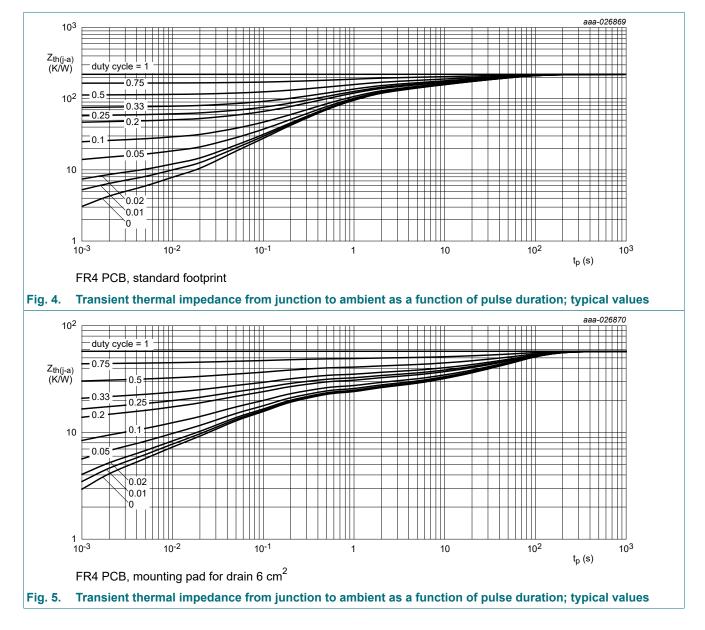


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	235	270	K/W
			[2]	-	67	74	K/W
		in free air, t ≤ 5 s	[2]	-	33	36	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	5	10	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

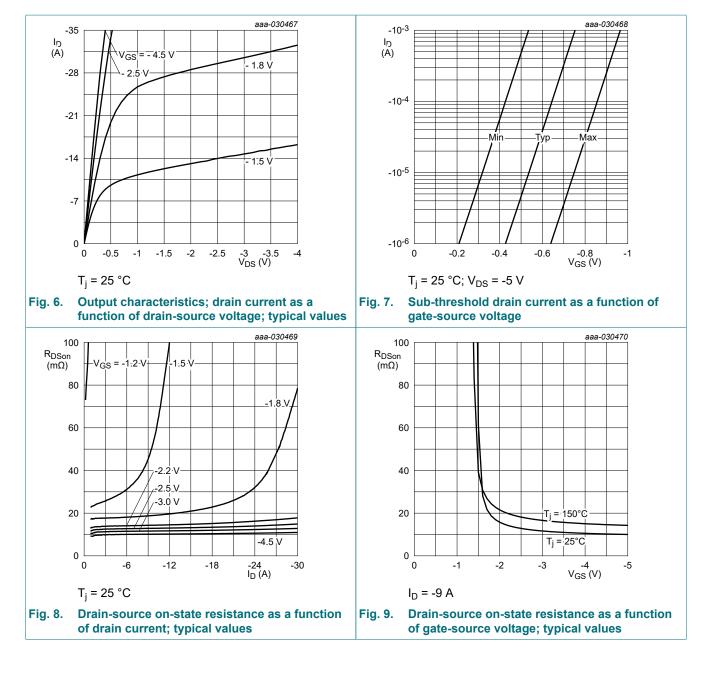
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².



10. Characteristics

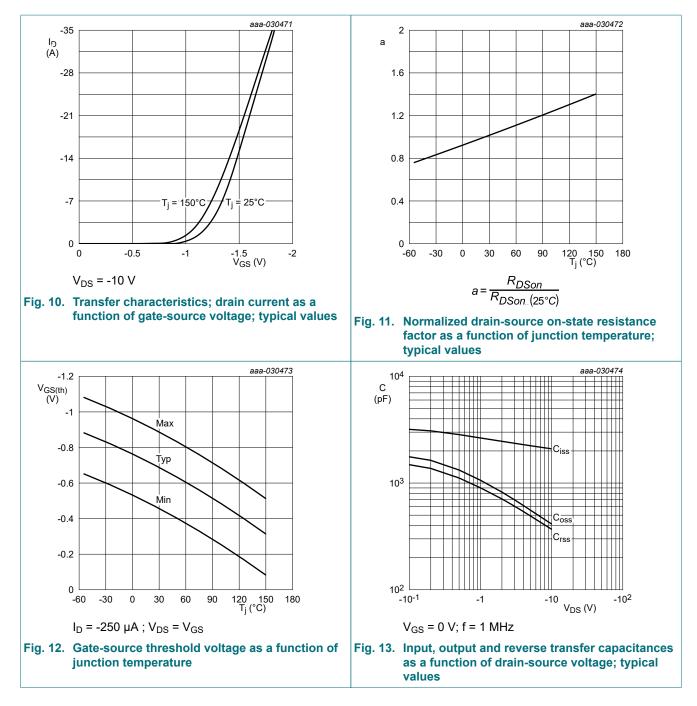
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-12	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = -250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	0.47	0.7	0.9	V
I _{DSS}	drain leakage current	V _{DS} = -12 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
R _{DSon}	drain-source on-state	V _{GS} = -4.5 V; I _D = -10 A; T _j = 25 °C	-	10	11.5	mΩ
	resistance	V _{GS} = -4.5 V; I _D = -10 A; T _j = 150 °C	-	14	16	mΩ
		V _{GS} = -2.5 V; I _D = -6 A; T _j = 25 °C	-	12	15.5	mΩ
		V _{GS} = -1.8 V; I _D = -4 A; T _j = 25 °C	-	17	22	mΩ
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -10 A; T _j = 25 °C	-	42	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	-	13.2	-	Ω
Dynamic ch	naracteristics	· · ·	i	•		
Q _{G(tot)}	total gate charge	$V_{DS} = -6 V; I_D = -10 A; V_{GS} = -4.5 V;$	-	27	40	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	3.4	-	nC
Q _{GD}	gate-drain charge		-	7.6	-	nC
C _{iss}	input capacitance	V _{DS} = -6 V; f = 1 MHz; V _{GS} = 0 V;	-	2200	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	509	-	pF
C _{rss}	reverse transfer capacitance		-	454	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -6 V; I_{D} = -10 A; V_{GS} = -4.5 V;	-	4	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	15	-	ns
t _{d(off)}	turn-off delay time	1 [-	149	-	ns
t _f	fall time] [-	100	-	ns
Source-drai	in diode	· · ·				
V _{SD}	source-drain voltage	I _S = -1.6 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.6	-1.2	V

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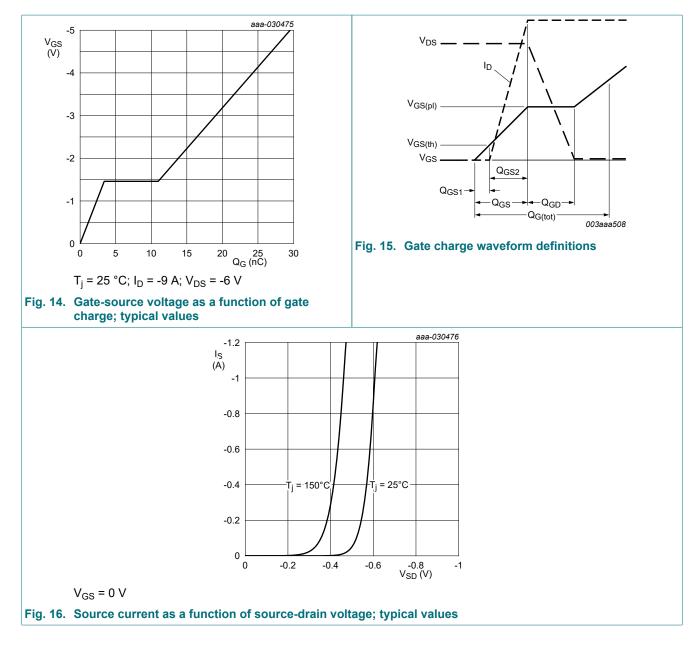


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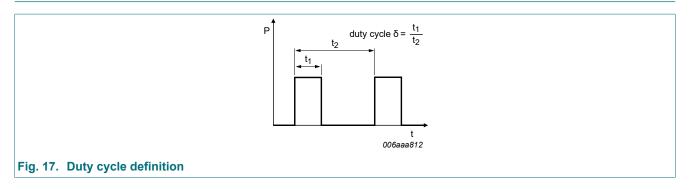
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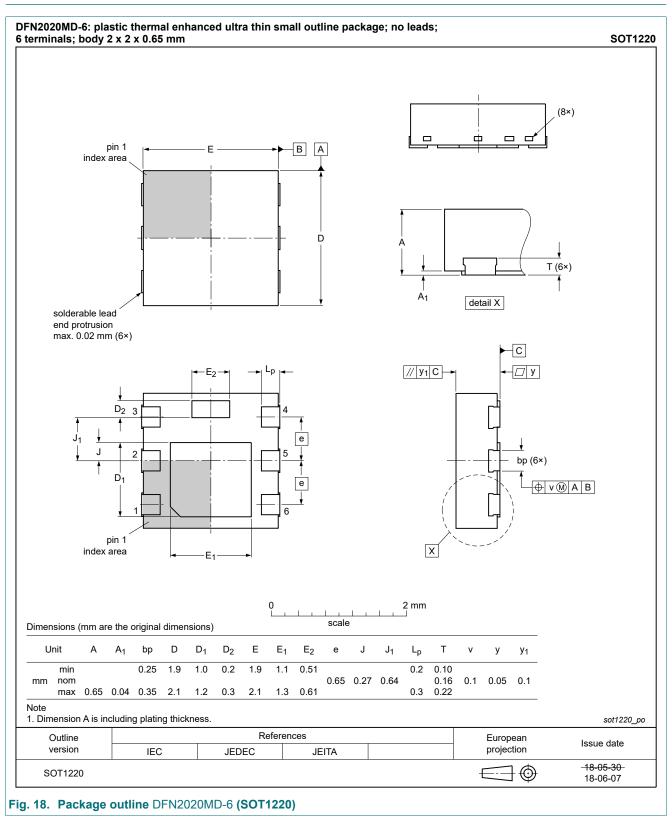
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11. Test information

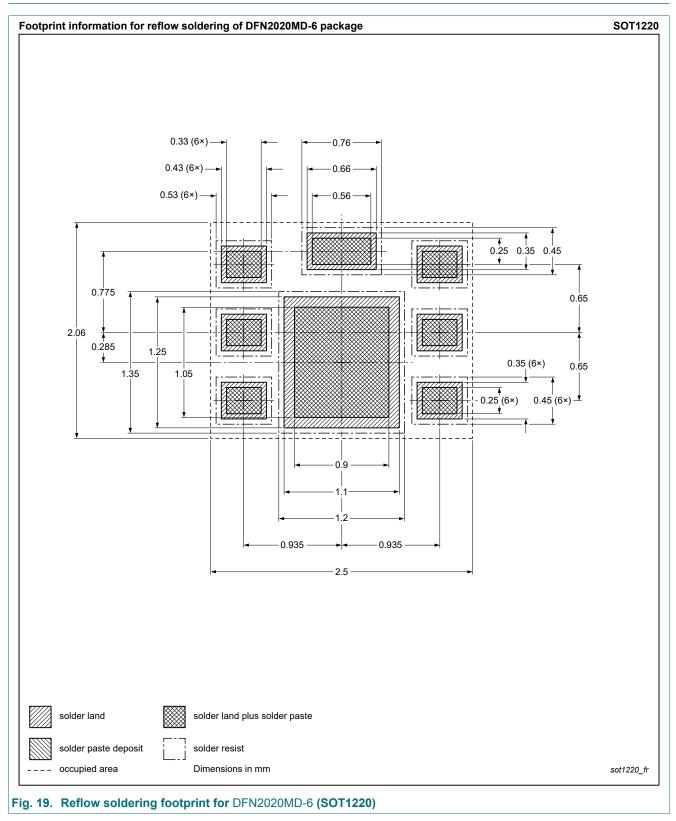


12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMPB10UP v.2	20200124	Product data sheet	-	PMPB10UP v.1			
Modifications:	Limiting values: unit corrected at P _{tot} = 13 W						
PMPB10UP v.1	20191216	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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