



# PESD3V3T1BL

## Bidirectional ESD protection diode

21 November 2018

Product data sheet

## 1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients.

The device is housed in a SOD882 leadless ultra small Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Bidirectional ESD protection of one line
- Ultra small SMD plastic package 1 x 0.6 x 0.48 mm
- ESD protection up to 30 kV
- Very high surge robustness;  $I_{PP} = 12\text{ A}$  for 8/20  $\mu\text{s}$ ; average measured
- AEC-Q101 qualified

## 3. Applications

- ESD and surge protection for interface lines

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	3.3	V
$I_{PPM}$	rated peak pulse current	$t_p = 8/20\text{ }\mu\text{s}$	[1]	-	10	A
$V_{CL}$	clamping voltage	$I_{PPM} = 10\text{ A}$ ; $t_p = 8/20\text{ }\mu\text{s}$ ; $T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	9.3	11	V

[1] Non-repetitive current pulse 8/20  $\mu\text{s}$  exponential decay waveform according to IEC 61000-4-5.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode 1 [1]	<p>Transparent top view DFN1006-2 (SOD882)</p>	<p>sym045</p>
2	K2	cathode 2		

[1] The marking band indicates the cathode

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD3V3T1BL	DFN1006-2	plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PESD3V3T1BL	L6

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$I_{PPM}$	rated peak pulse current	$t_p = 8/20 \mu s$	[1]	-	10	A
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-55	150	°C
$T_{stg}$	storage temperature			-65	150	°C
<b>ESD maximum ratings</b>						
$V_{ESD}$	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV

[1] Non-repetitive current pulse 8/20  $\mu s$  exponential decay waveform according to IEC 61000-4-5.

[2] Device stressed with ten non-repetitive ESD pulses.

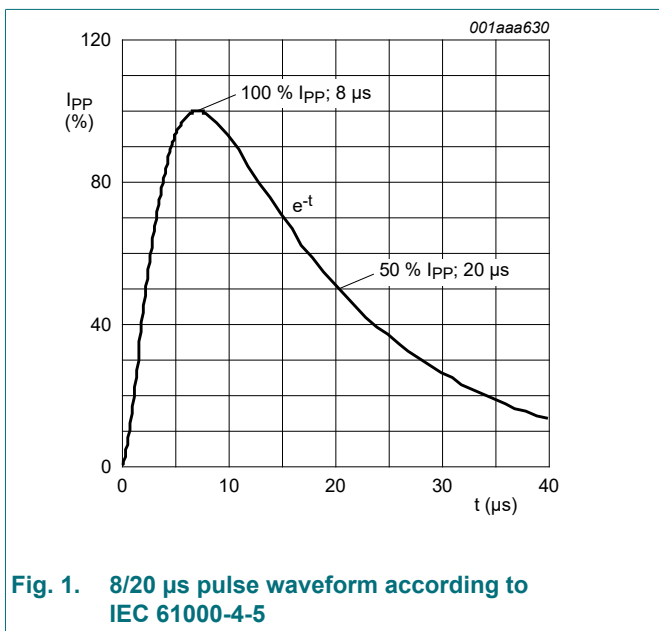


Fig. 1. 8/20  $\mu s$  pulse waveform according to IEC 61000-4-5

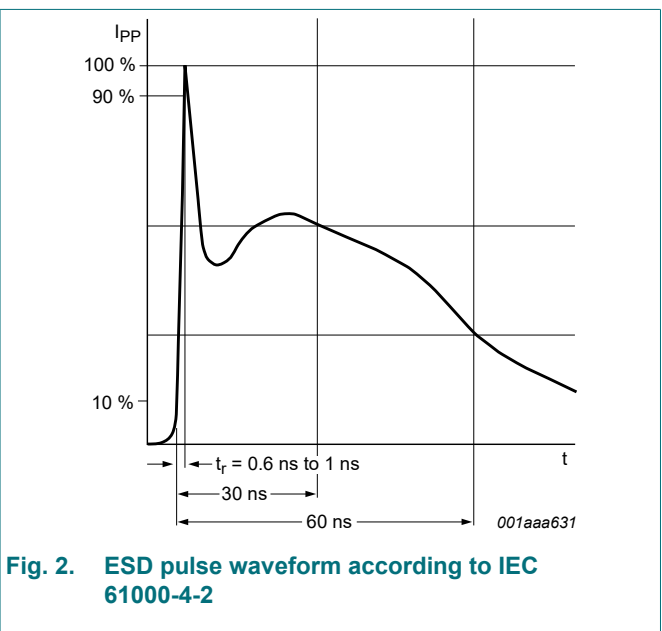


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25\text{ °C}$	-	-	3.3	V	
$V_{BR}$	breakdown voltage	$I_R = 5\text{ mA}; T_{amb} = 25\text{ °C}$	4.7	5.5	8.7	V	
$I_{RM}$	reverse leakage current	$V_{RWM} = 3.3\text{ V}; T_{amb} = 25\text{ °C}$	-	0.1	50	nA	
$C_d$	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ °C}$	-	20	25	pF	
$V_{CL}$	clamping voltage	$I_{PP} = 1\text{ A}; t_p = 8/20\text{ }\mu\text{s}; T_{amb} = 25\text{ °C}$	[1]	-	6.5	-	V
		$I_{PPM} = 10\text{ A}; t_p = 8/20\text{ }\mu\text{s}; T_{amb} = 25\text{ °C}$	[1]	-	9.3	11	V
		$I_{PP} = 16\text{ A}; t_p = 100\text{ ns}; T_{amb} = 25\text{ °C}$	[2]	-	9.5	-	V
$R_{dyn}$	dynamic resistance	$I_R = 10\text{ A}; T_{amb} = 25\text{ °C}$	[2]	-	0.12	-	$\Omega$
		$I_R = -10\text{ A}; T_{amb} = 25\text{ °C}$	[2]	-	0.21	-	$\Omega$

- [1] Non-repetitive current pulse 8/20  $\mu\text{s}$  exponential decay waveform according to IEC 61000-4-5.
- [2] Non-repetitive current pulse, Transmission Line Pulse (TLP)  $t_p = 100\text{ ns}$ ; square pulse; ANSI/ESD STM5.5.1-2008.

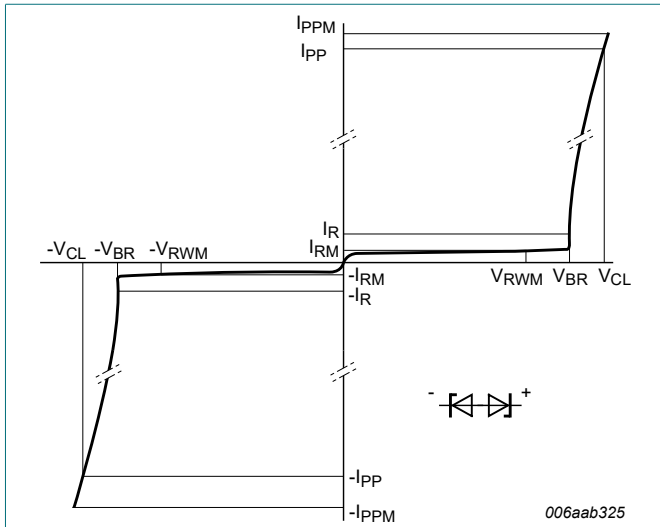


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

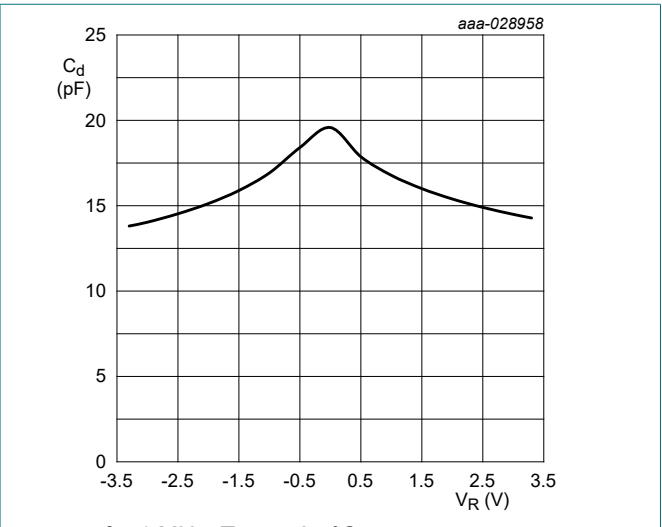
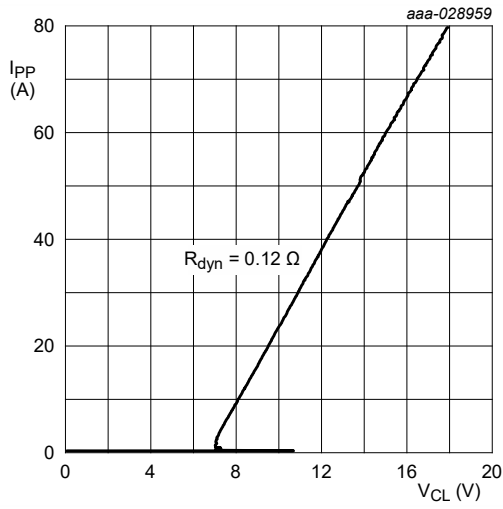
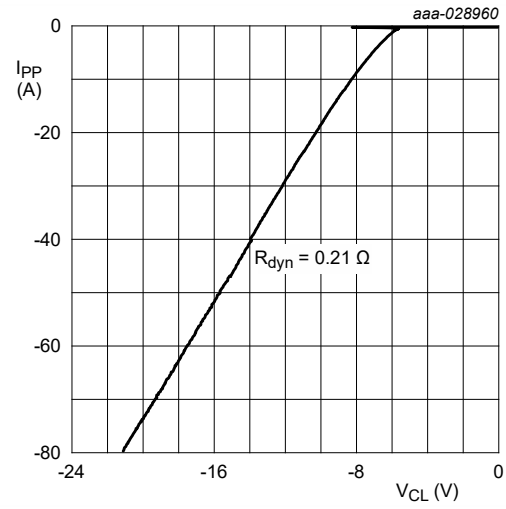


Fig. 4. Diode capacitance as a function of reverse voltage; typical values



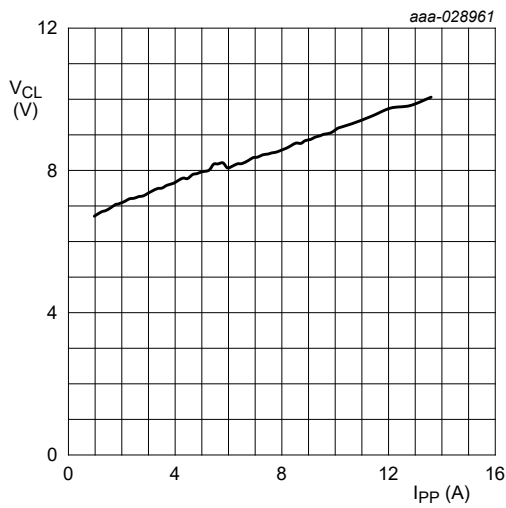
$t_p = 100$  ns; Transmission Line Pulse (TLP)

**Fig. 5. Positive clamping voltage (TLP); typical values**



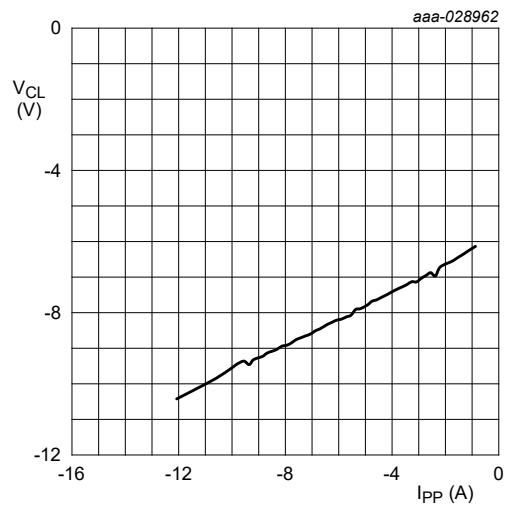
$t_p = 100$  ns; Transmission Line Pulse (TLP)

**Fig. 6. Negative clamping voltage (TLP); typical values**



IEC 61000-4-5;  $t_p = 8/20$   $\mu$ s; positive pulse

**Fig. 7. Positive clamping voltage (8/20  $\mu$ s pulse); typical values**



IEC 61000-4-5;  $t_p = 8/20$   $\mu$ s; negative pulse

**Fig. 8. Negative clamping voltage (8/20  $\mu$ s pulse); typical values**

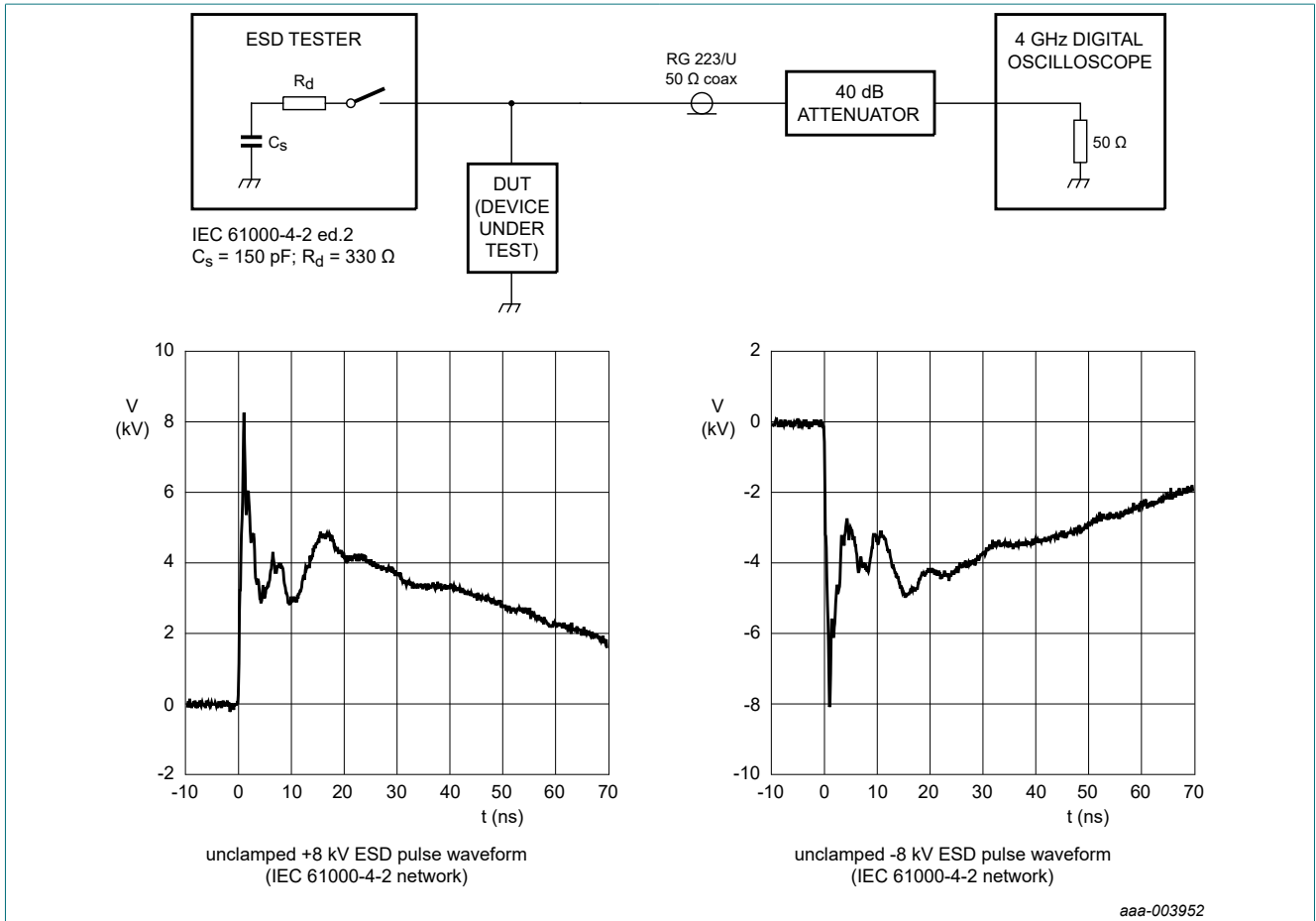


Fig. 9. ESD clamping test setup and waveforms

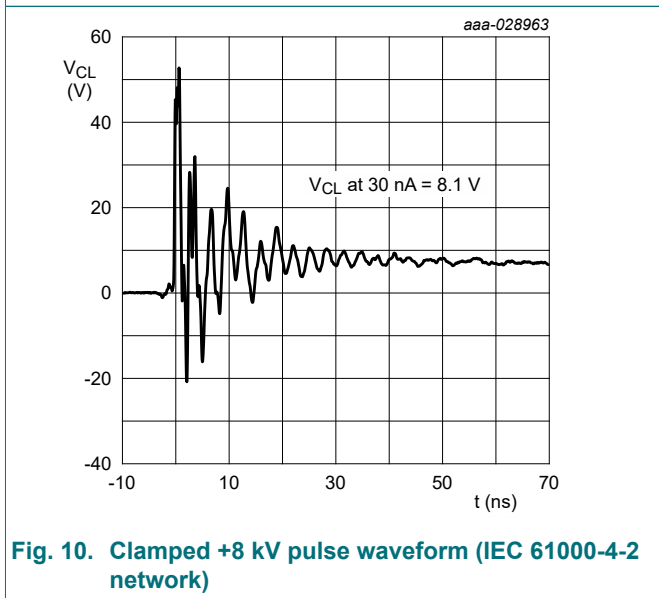


Fig. 10. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

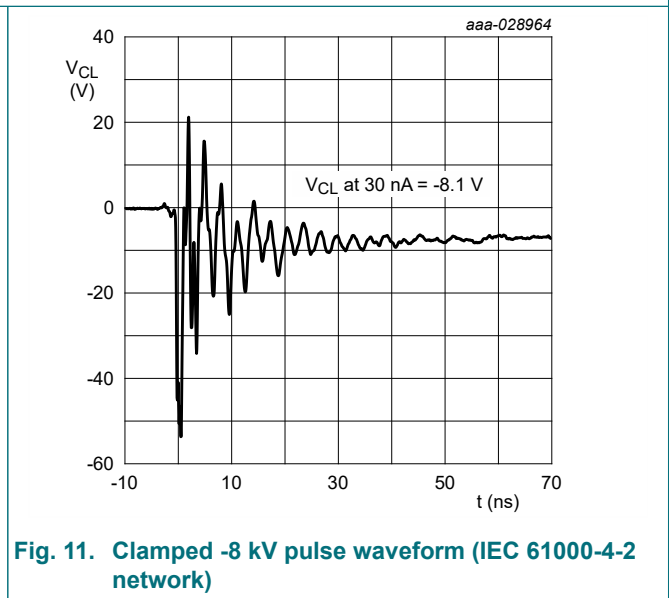


Fig. 11. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)

## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

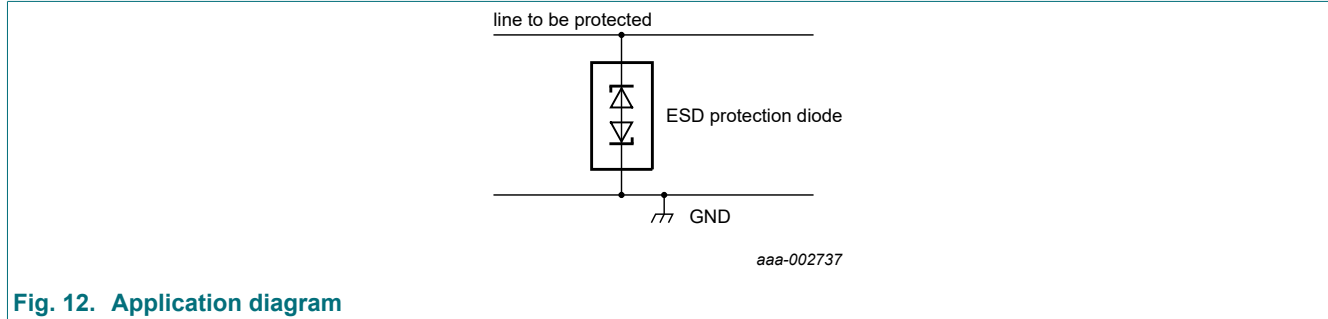


Fig. 12. Application diagram

### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

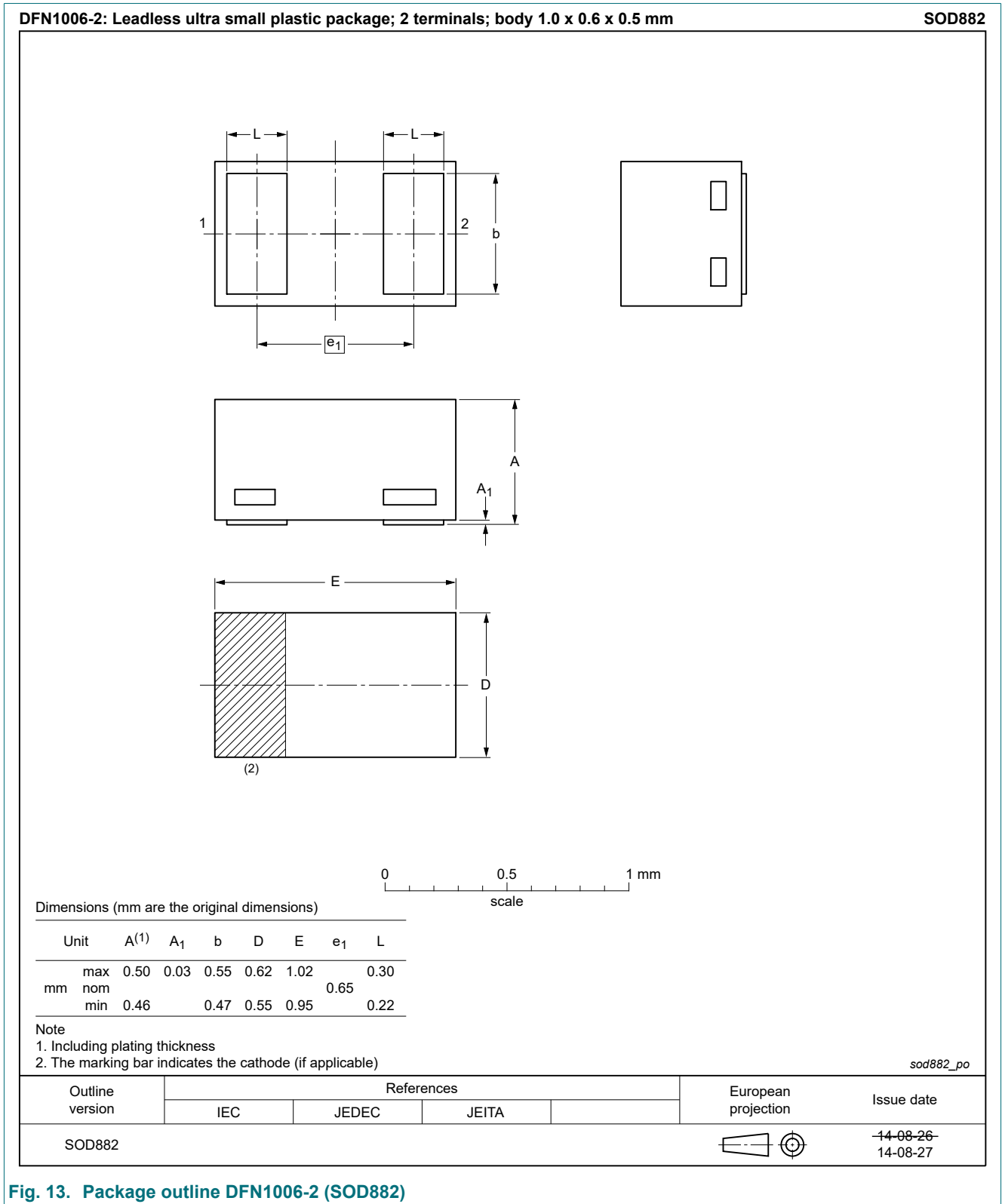
1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



**Fig. 13. Package outline DFN1006-2 (SOD882)**

### 13. Soldering

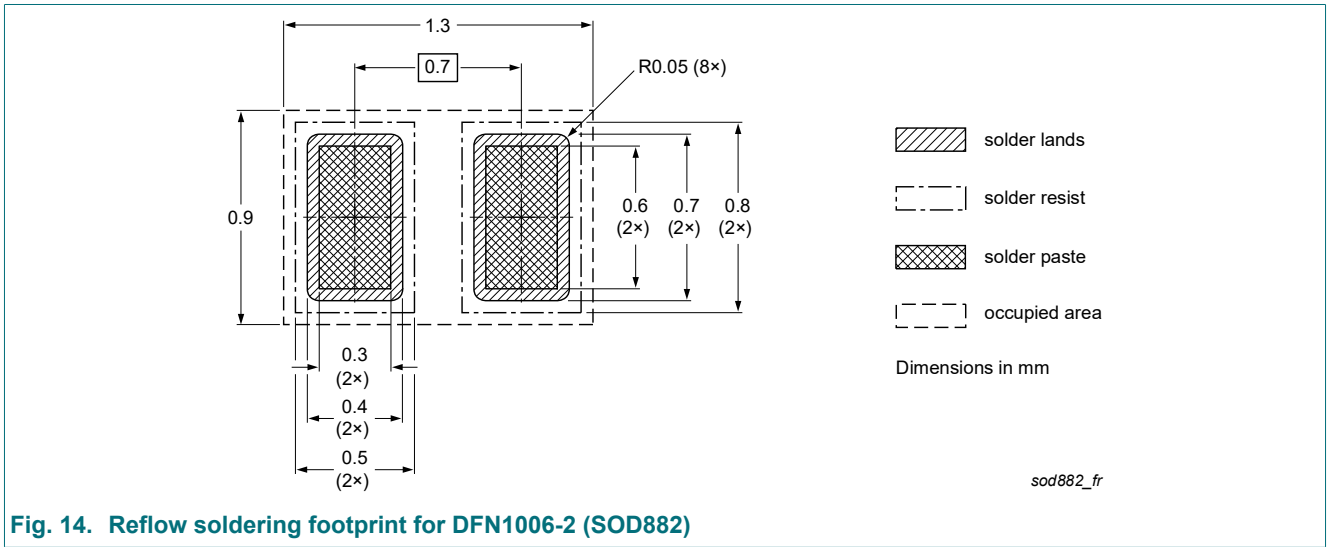


Fig. 14. Reflow soldering footprint for DFN1006-2 (SOD882)



## 14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD3V3T1BL v.1	20181121	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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