

PESD2CANFD27V-U

ESD protection for In-vehicle networks

16 April 2020

Product data sheet

1. General description

ESD protection device in a small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package designed to protect two automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 27 V
- Low clamping voltage: V_{CL}= 33 V at I_{PP} = 1 A
- ESD protection up to 20 kV (IEC 61000-4-2)
- Ultra low capacitance: C_d = 6 pF
- ESD protection up to 20 kV (ISO 10605; C = 150 pF; R = 330 Ω)
- High temperature capability: T_i = 175 °C
- · AEC-Q101 qualified

3. Applications

ESD protection for In-vehicle network lines in automotive environments

- CAN-FD
- CAN
- FlexRay
- SENT

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	-	2.5	Α
V_{CL}	clamping voltage	$I_{PPM} = 1 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[3] [2]	-	33	44	V

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	<u></u> 3	K1 14 15
2	K2	cathode (diode 2)		N CC
3	СС	common cathode		K2
			1 2 SC-70 (SOT323)	006aaa155

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PESD2CANFD27V-U	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PESD2CANFD27V-U	Z3%

[1] % = placeholder for manufacturing site code

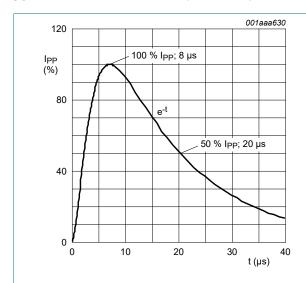
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	$t_p = 8/20 \mu s$	[1] [2]	-	2.5	Α
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
ESD maximu	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	20	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 Ω	[2] [3]	-	17	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 Ω	[2] [3]	-	20	kV

- According to IEC 61000-4-5. Measured from pin 1 or 2 to pin 3.
- Device stressed with ten non-repetitive ESD pulses.



8/20 µs pulse waveform according to Fig. 1. IEC 61000-4-5

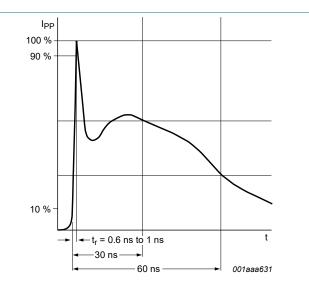


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
V_{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	28	-	38	V
I _{RM}	reverse leakage current	V _{RWM} = 27 V; T _{amb} = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 2.5 V; T _{amb} = 25 °C	[1]	-	5.2	6	pF
		f = 1 MHz; V _R = -2.5 V; T _{amb} = 25 °C	[1]	-	5.2	6	pF
$\Delta C_d/C_d$	diode capacitance	f = 1 MHz; V _R = 2.5 V; T _{amb} = 25 °C	[2]	-	0.5	-	%
	matching	f = 1 MHz; V _R = -2.5 V; T _{amb} = 25 °C	[2]	-	0.5	-	%
V _{CL}	clamping voltage	I _{PPM} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[3] [1]	-	33	44	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[4] [1]	-	0.8	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- ΔC_d is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

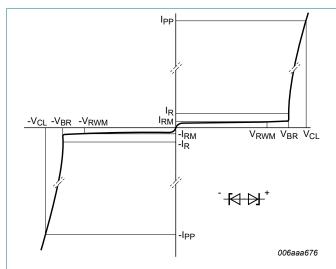


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

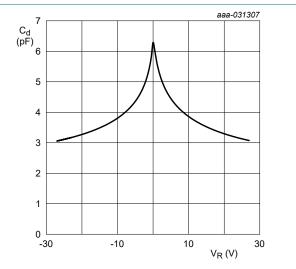
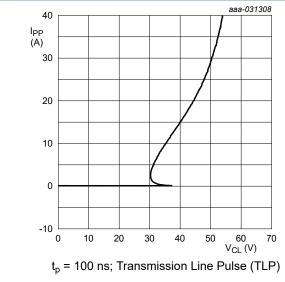
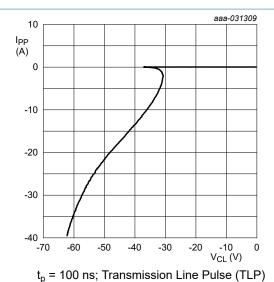


Fig. 4. Diode capacitance as a function of reverse voltage; typical values





tp = 100 fls, framsifilission Line i dise (1Li)

Fig. 5. Positive clamping voltage (TLP); typical values



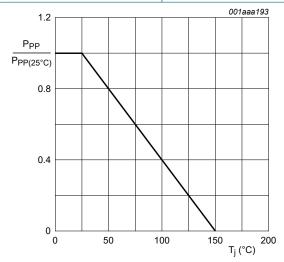
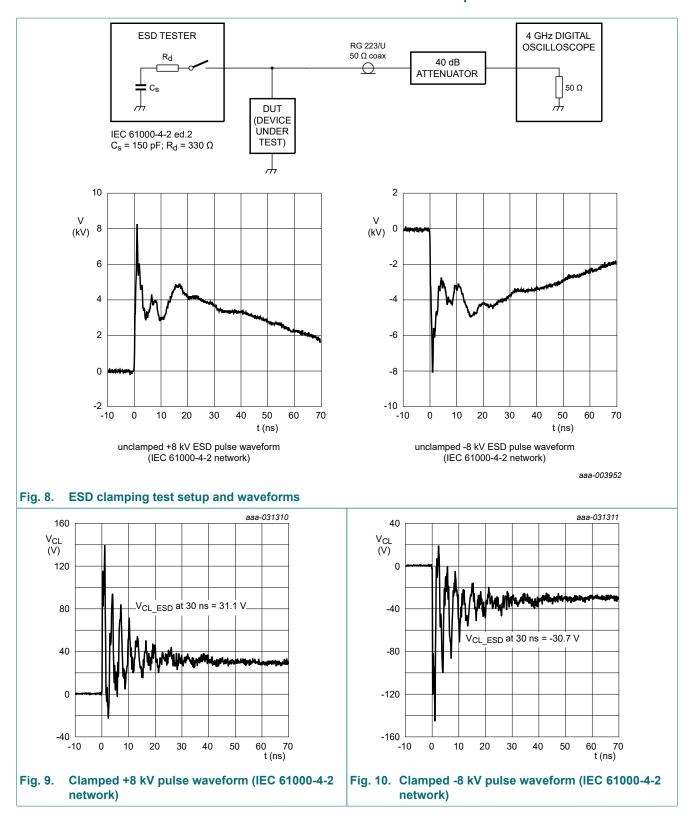


Fig. 7. Relative variation of peak pulse power as a function of junction temperature; typical values



10. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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11. Package outline

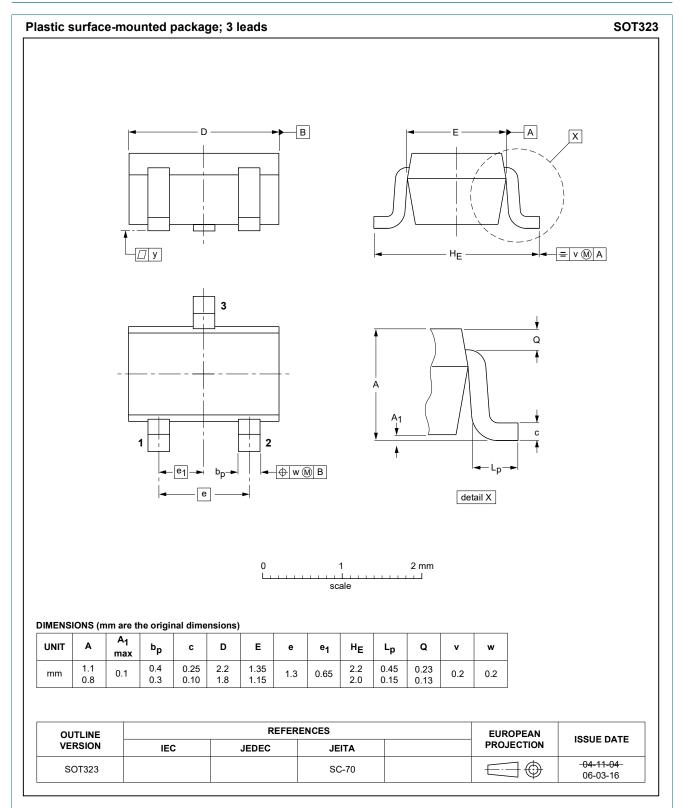
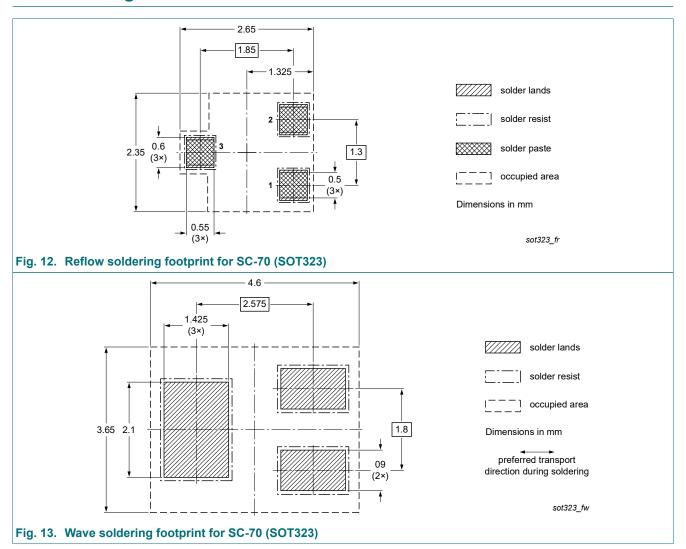


Fig. 11. Package outline SC-70 (SOT323)

12. Soldering



13. Revision history

Table 7. Revision history

Table III to Holon Inote	•						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD2CANFD27V-U v.2	20200416	Product data sheet	-	PESD2CANFD27V-U v.1			
Modifications:	 Updated with the late 	Updated with the latest measurements.					
PESD2CANFD27V-U v.1	20190801	Objective data sheet	-	-			

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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