TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

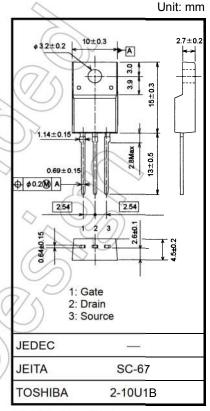
TK15A60U

Switching Regulator Applications

- Low drain-source ON-resistance: R_{DS} (ON) = 0.24 Ω (typ.)
- High forward transfer admittance: |Yfs| = 8.5 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 600 V)
- Enhancement mode: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage Gate-source voltage		V _{DSS}	600	(V)	
		V _{GSS}	±30		
6 - 1	DC (Note 1)	ID	15		
Drain current	Pulse (Note 1)	I _{DP}	30	A	
Drain power dissipation (Tc = 25°C)		PD	40	V w	
Single pulse avalanche energy (Note 2)		E _{AS}	81	mJ	
Avalanche current		I _{AR}	15	A	
Repetitive avalanche energy (Note 3)		EAR	4	Lm	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		Tstg	-55 to 150	√ °C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

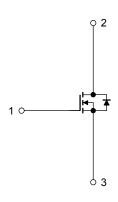
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	3.125	°C/W
Thermal resistance, channel to ambient	Rth (ch-a)	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 0.63 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2008-02

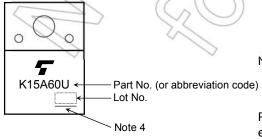
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		_	100	μΑ
Drain-source break	down voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	s -	2	V
Gate threshold vol	tage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 7.5 A	1	0.24	0.3	Ω
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 10 V, I _D = 7.5 A	3.0	8.5	0.—	S
Input capacitance		C _{iss}		\mathcal{D}	950	S-	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	47		pF
Output capacitance		Coss		⁷ —	2300	Ø	
Switching time	Rise time	t _r	10 V ID = 7.5 A VOUT		37	7	
	Turn-ON time	t _{on}	$\begin{array}{c c} V_{GS} & & \\ 0 & V & \\ \hline & 50 & \Omega & \\ \end{array}$ $R_{L} = 40 & \Omega$	-6	80	> —	ns
	Fall time	t _f		A	(8))_	
	Turn-OFF time	t _{off}	$V_{DD} \approx 300 \text{ V}$ Duty $\leq 1\%$, $t_W = 10 \text{ μs}$	7	105	_	
Total gate charge		Qg		\ -	17	-	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 15 A	/_	10	5	nC
Gate-drain charge		Q _{gd}		-	7	100	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)) l _{DR}	(7) -	:	2,—2	15	Α
Pulse drain reverse current (Note 1)	IDRP	_	_	-	30	Α
Forward voltage (diode)	VDSF	I _{DR} = 15 A, V _{GS} = 0 V	_	2-	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V,		530	9	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	9.0		μС

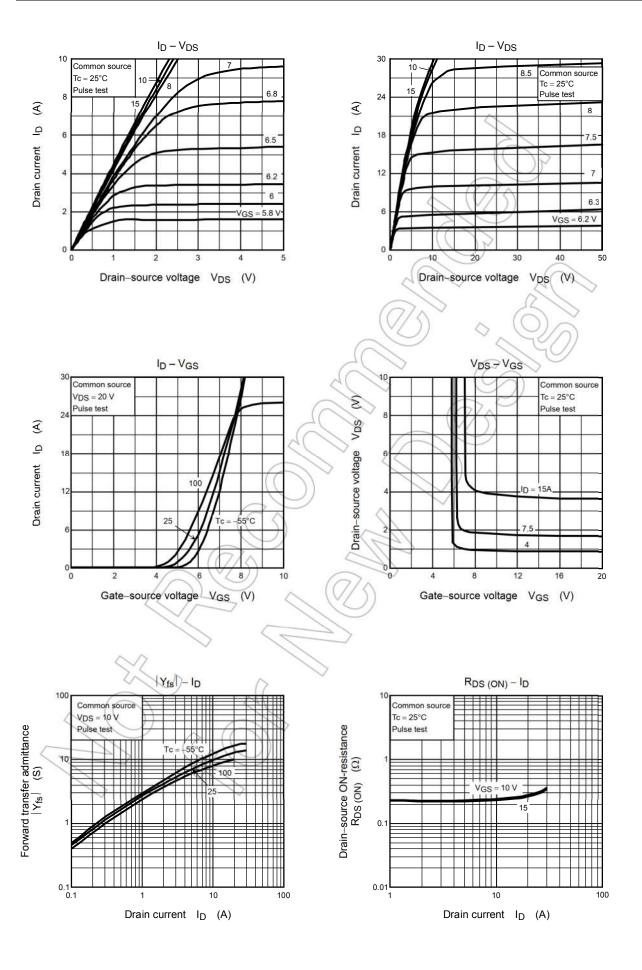
Marking

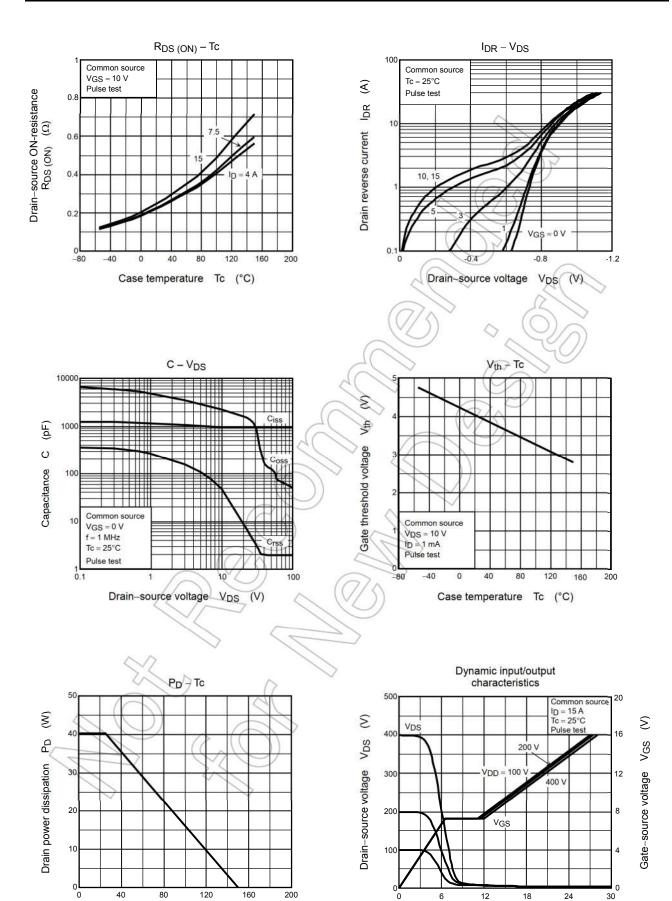


Note 4: A dot marking for identifying the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

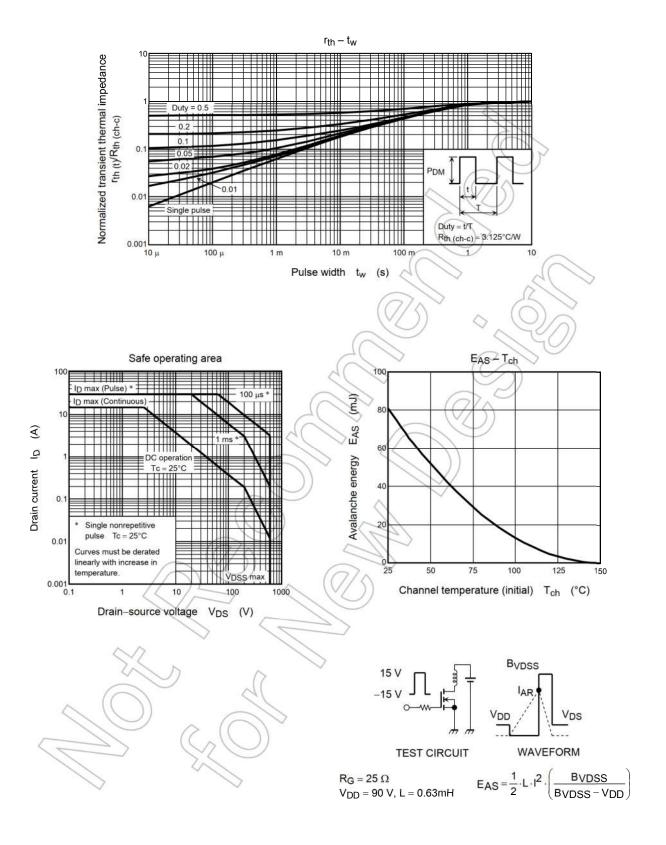
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Case temperature Tc (°C)

Total gate charge Q_g (nC)



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