### NCE N-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE30ND07S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> =30V,I<sub>D</sub> =7A

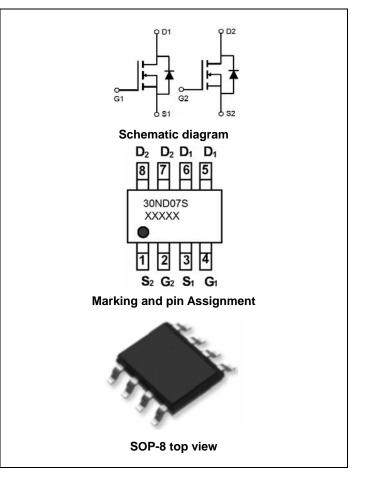
 $R_{DS(ON)}$  < 23m $\Omega$  @  $V_{GS}$ =10V

 $R_{DS(ON)}$  < 40m $\Omega$  @  $V_{GS}$ =4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



**Package Marking and Ordering Information** 

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30ND07S	NCE30ND07S	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	7	Α	
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	4.95	Α	
Pulsed Drain Current	I <sub>DM</sub>	40	А	
Maximum Power Dissipation	P <sub>D</sub>	2	W	
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}\!\mathbb{C}$	

#### **Thermal Characteristic**

Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62.5	85	°C/W



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Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u> </u>		•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.1	-	2.1	V
Drain-Source On-State Resistance	В	V <sub>GS</sub> =10V, I <sub>D</sub> =7A	-	18	23	mΩ
Dialii-Source Oii-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	-	25	40	11122
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =7A	-	15	-	S
Dynamic Characteristics (Note4)			•			•
Input Capacitance	C <sub>lss</sub>	\/ 45\/\\ 0\/	-	380	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	67	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVIHZ	-	41	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	5	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =15V, $R_L$ =2 $\Omega$	-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =3 $\Omega$	-	15	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	3	-	nS
Total Gate Charge	Qg	\/ -45\/  -74	-	7.2	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=15V,I_{D}=7A,$	-	1.3	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =4.5V	-	1.7	-	nC
Drain-Source Diode Characteristics	<u> </u>		•			•
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =7A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	7	Α

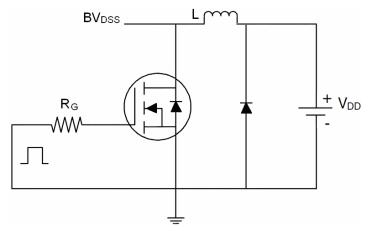
### Notes:

- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. The value of R  $_{\text{BJA}}$  is measured with the device mounted on 1in  $^2$  FR-4 board with 2oz. Copper, in a still air environment with T  $_A$ =25°C. The value in any given application depends on the user's specific board design. Surface Mounted on FR4 Board, t  $\leq$  10 sec. The current rating is based on the t  $\leq$  10s thermal resistance rating.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production.

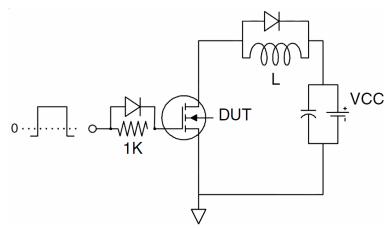


# **Test Circuit**

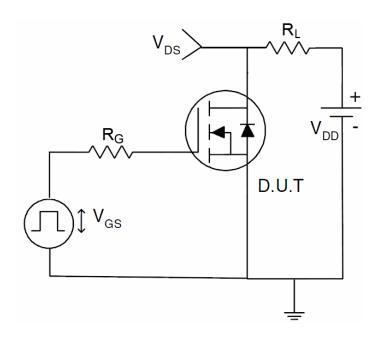
# 1) E<sub>AS</sub> Test Circuits



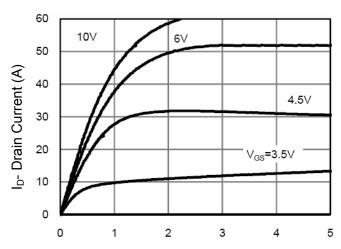
# 2) Gate Charge Test Circuit:



### 3) Switch Time Test Circuit:

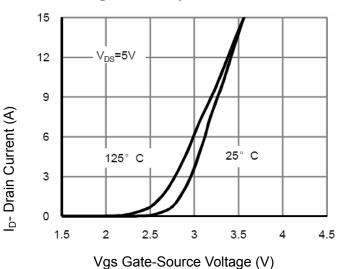


## **Typical Electrical and Thermal Characteristics (Curves)**



Vds Drain-Source Voltage (V)

**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

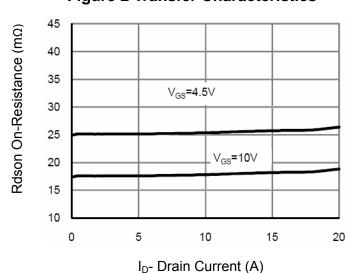


Figure 3 Rdson- Drain Current

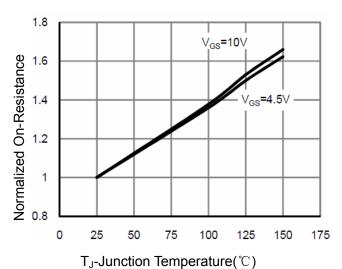
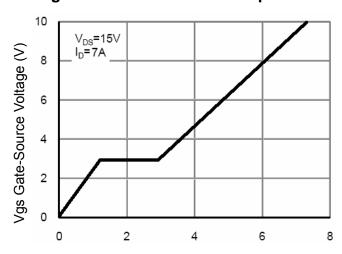
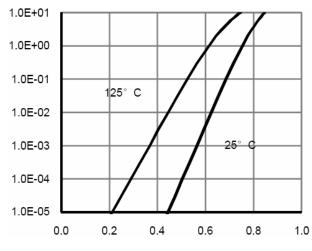


Figure 4 Rdson-JunctionTemperature



Qg Gate Charge (nC)
Figure 5 Gate Charge



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward



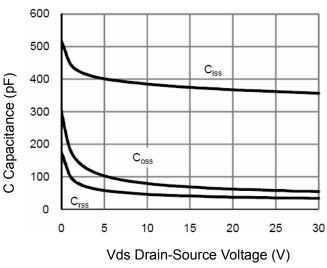


Figure 7 Capacitance vs Vds

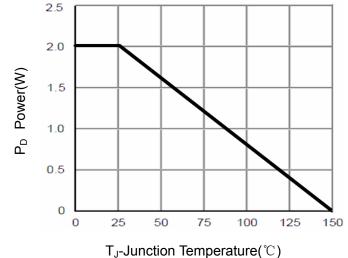
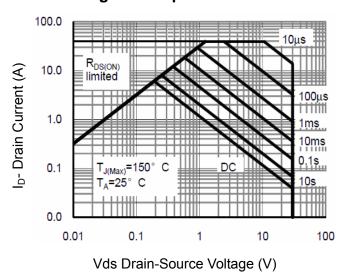


Figure 9 Power Dissipation



**Figure 8 Safe Operation Area** 

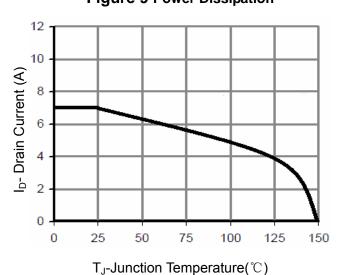
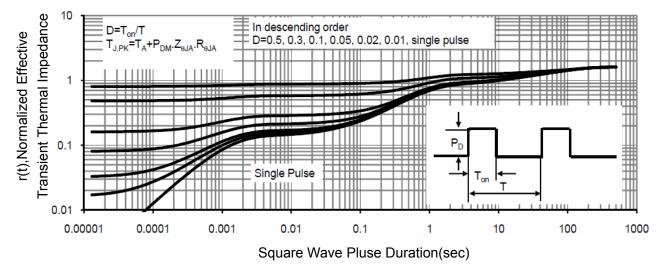


Figure 10 Current De-rating

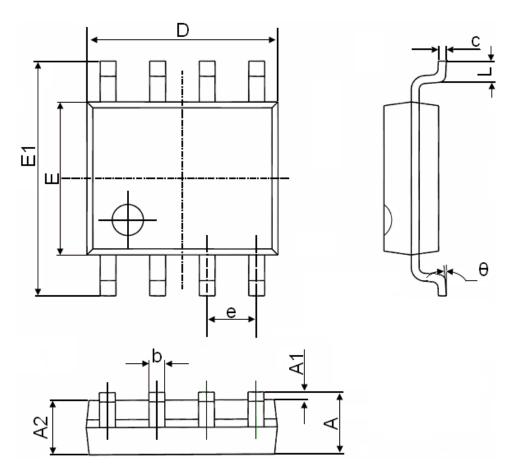


**Figure 11 Normalized Maximum Transient Thermal Impedance** 





# **SOP-8 Package Information**



Cumbal	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(I	BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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