

80mΩ ,Adjustable Fast Response Current-Limited Power-Distribution Switch

FEATURES

- Compliant to USB Specifications
- Integrated 80mΩ Power MOSFET
- Low Supply Current
 15μA Typical at Switch On State
 1μA Typical at Switch Off State
- Wide Input Voltage Range: 2.4V to 5.5V
- Fast Transient Response: < 2μs
- Reverse Current Flow Blocking
- Thermal Shutdown Protection
- Hot Plug-In Application (Soft-Start)
- Available in a 5-Pin SOT23-5 Package

APPLICATIONS

- USB Bus/Self Powered Hubs
- USB Peripherals
- Notebook Computers
- Battery-Charger Circuits
- Personal Communication Devices

GENERAL DESCRIPTION

The SD00HA is a cost-effective, low voltage, single P-MOSFET load switch, optimized for self-powered and bus-powered Universal Serial Bus (USB) applications. This switch operates with inputs ranging from 2.4V to 5.5V, making it ideal for both 3V and 5V systems. The switch's low $R_{DS(ON)}$, $80m\Omega$, meets USB voltage drop requirements. The SD00HA is also protected from thermal overload which limits power dissipation and junction temperatures. Current limit threshold programmed with a resistor from SET to ground. The quiescent supply current is typically 15μ A at switch on state. At switch off state the supply current decreases to less than 1μ A. The SD00HA is available in SOT23-5 package.

TYPICAL APPLICATION

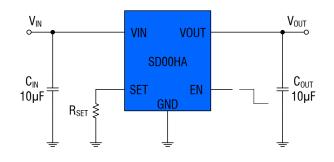
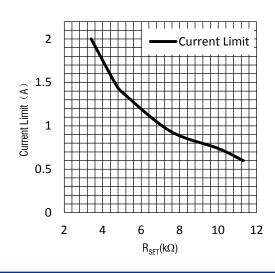


Figure 1. Basic Application Circuit





ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Supply Voltage	0.3V to 7V
EN Voltages	0.3V to $(V_{IN} + 0.3V)$
SET Voltage	0.3V to $(V_{IN} + 0.3V)$
Power Dissipation	0.4W
Thermal Resistance θ_{JC}	130°C/W
Thermal Resistance θ ₁ ,	250°C/W

Junction Temperature(Note2)	150°C
Operating Temperature Range40°C	to 85°C
Lead Temperature(Soldering, 10s)	300°C
Storage Temperature Range65°C to	o 150°C
ESD HBM(Human Body Mode)	2kV
ESD MM(Machine Mode)	200V

PACKAGE/ORDER INFORMATION

	Order Part Number	Package	Top Marking
TOP VIEW VOUT 1 GND 2 SET 3 4 EN 5-LEAD PLASTIC SOT-23 $T_{JIMAX} = 150^{\circ}\text{C}, \theta_{JA} = 250^{\circ}\text{C}/\text{W}, \theta_{JC} = 130^{\circ}\text{C/W}$	SD00HA	S0T23-5	D00HA <u>W</u>

PIN DESCRIPTION

Pin Name	Pin Number	Description	
VOUT	1	Power-switch output	
GND	2	Ground connection; connect externally to Power PAD	
SET	3	External resistor used to set current-limit threshold	
EN	4	Enable input, logic high turns on power switch	
VIN	5	Input voltage; connect a 10uF or greater ceramic capacitor from VIN to GND as close to the IC as possible	

Ver 1.0 www.sdw-tw.com 2



ELECTRICAL CHARACTERISTICS (Note 3)

 $(V_{IN}=5V, T_A=-40^{\circ}C \text{ to } 85^{\circ}C, \text{ unless otherwise noted.})$

PARAMETER	}	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	Range	V _{IN}		2.4		5.5	V
Curitab On Decistones	R _{DS(ON)}	$V_{IN} = 5V$		80	100	mΩ	
SWILCII OII N	Switch On Resistance		$V_{IN}=3V$		90	110	mΩ
Operation Quiceagent Current		I _Q	V_{IN} =5V,EN=Active,	15	25		
Operation Qu	Operation Quiescent Current		No load		าอ	20	μΑ
Off Supply C	urrent	I _{Q(OFF)}	$V_{IN} = 5.5V, EN = Inactive$			1	μ A
Off Switch C	urrent	I _{Q(SW_OFF)}	$V_{IN} = 5.5V, EN = Inactive$			1	μ A
Under-voltag	e Lockout	V _{UVLO}	V _{IN} Increasing		1.8	2.4	V
Under-voltag	e Lockout		V _{IN} decreasing		0.1		V
Hysteresis		ΔV_{UVL0}	V _{IN} decreasing		0.1		V
Current Limit	Threshold	I _{LIM}	$R_{SET} = 6.8k\Omega$		1		Α
EN	Logic-Low Voltage	V _{IL}	$V_{IN} = 2.5 V \text{ to } 5.5 V$			8.0	V
Threshold	Logic-High Voltage	V_{IH}	$V_{IN} = 2.5 V \text{ to } 5.5 V$	2			V
Output Lookogo Current	l I '	EN=Inactive,		0.5	10		
Output Leakage Current		$R_{LOAD} = 0\Omega$				μΑ	
Current Limit	: Response Time	T_{RESP}	$V_{IN} = 5V$		1		μs
Thermal Shu	tdown Protection	T_{SD}			150		°C
Thermal Shu	tdown Hysteresis	ΔT _{SD}			20		°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

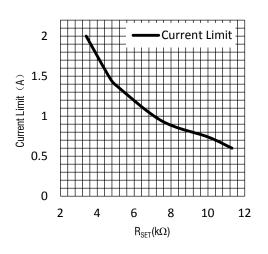
Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + (P_D) x$ (250°C/W).

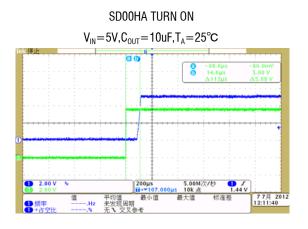
Note 3: 100% production test at 25°C. Specifications over the temperature range are guaranteed by design and characterization.

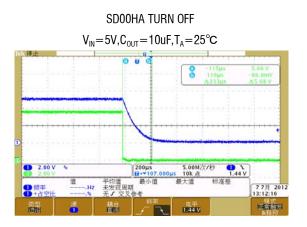
Ver 1.0 www.sdw-tw.com 3

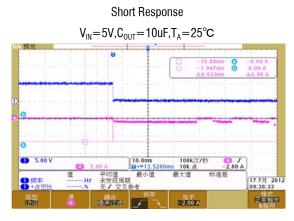


TYPICAL PERFORMANCE CHARACTERISTICS











FUNCTIONAL BLOCK DIAGRAM

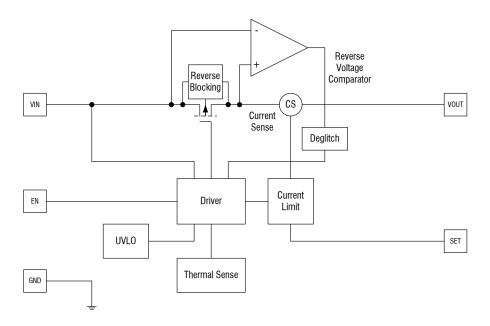


Figure 2. SD00HA Block Diagram

APPLICATIONS INFORMATION

The SD00HA is a single channel current limiting load switch that is intended to protect against short circuit and over current events by current limiting to a preset level. This device is optimized for self-powered and bus-powered Universal Serial Bus (USB) applications. The switch's low $R_{\text{DS}(\text{ON})},\ 80\text{m}\Omega$, meets USB voltage drop requirements; and a flag output is available to indicate fault conditions to the local USB controller.

Input and Output

 V_{IN} (input) is the power source connection to the internal circuitry and the source of the MOSFET. V_{OUT} (output) is the drain of the MOSFET. In a typical application, current flows through the switch from V_{IN} to V_{OUT} toward the load. If V_{OUT} is greater than V_{IN} , current will flow from V_{OUT} to V_{IN} since the MOSFET is bidirectional when on. The SD00HA's reverse current blocking feature

prevents current to flow from V_{OUT} to V_{IN} when the device is disabled.

Soft Start for Hot Plug-In Applications

In order to eliminate the upstream voltage droop caused by the large inrush current during hot-plug events,the "soft-start" feature effectively isolates the power source from extremely large capacitive loads,satisfying the USB voltage droop requirements.

Input capacitor

The input capacitor $C_{\rm IN}$ protects the power supply from current transients generated by the load attached to the SD00HA. When a short circuit is suddenly applied to the output of the SD00HA, a large current, limited only by the $R_{\rm DS(ON)}$ of the MOSFET, will flow for less than 2μ s before the current limit circuitry activates. In this event, a moderately sized $C_{\rm IN}$ will dramatically reduce the voltage transient seen by the power



supply and by other circuitry upstream from the SD00HA. The extremely fast short-circuit response time of the SD00HA reduces the size requirement for C_{IN} . C_{IN} should be located as close to the device V_{IN} pin as practically possible. Ceramic, tantalum, or aluminum electrolytic capacitors are appropriate for C_{IN}. There is no specific capacitor ESR requirement for C_{IN}. However, for higher current operation, ceramic capacitors are recommended for C_{IN} due to their inherent capability over tantalum capacitors to withstand input current surges from low impedance sources such as batteries in portable devices.

Output capacitor

A low-ESR 150μ F aluminum electrolytic or tantalum between V_{OUT} and GND is strongly recommended to meet the 330mV maximum droop requirement in the hub V_{RUS} (Per USB 2.0, output ports must have a minimum 120μ F of low-ESR bulk capacitance per hub). Standard bypass methods should be used to minimize inductance and resistance between the bypass capacitor and the downstream connector to reduce EMI and decouple voltage droop caused when downstream cables are hot-insertion transients. Ferrite beads in series with V_{BUS} , the ground line and the 0.1μ F bypass capacitors at the power connector pins are recommended for EMI and ESD protection. The bypass capacitor itself should have a low dissipation factor to allow decoupling at higher frequencies.

Thermal Considerations

Since the SD00HA has internal current limit and over temperature protection, junction temperature is rarely a concern. However, if the application requires large currents in a hot environment, it is possible that temperature, rather than current limit, will be the dominant regulating condition. In these applications, the maximum current available without risk of an over-temperature condition must be calculated. Power dissipation can be

calculated based on the output current and the $R_{\text{DS(ON)}}$ of switch as below.

$$P_{D} = R_{DS(ON)} \times I_{OUT}^{2}$$

Although the devices are rated for 2A(max) of output current, but the application may limit the amount of output current based on the total power dissipation and the ambient temperature. The final operating junction temperature for any set of conditions can be estimated by the following thermal equation:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_{A}}{\theta_{JA}}$$

Where $T_{J(MAX)}$ is the maximum operation junction temperature 150°C, T_A is the ambient temperature and the θ_{JA} is the junction to ambient thermal resistance. The junction to ambient thermal resistance θ_{JA} is layout dependent. For SOT23-5 and TSOT23-5 packages, the thermal resistance θ_{JA} is 250°C/W. The maximum power dissipation at $T_A = 25$ °C is 0.4W for SOT23-5 and TSOT23-5 Package.

Current limit threshold Setting

Current limit threshold is programmed with a resistor from SET to ground marked as R_{SET} . It can be estimated by the following equation:

$$I_{SET}(A) = \frac{6.8k\Omega}{R_{SET}(k\Omega)}$$

Such as the following table.

$R_{SET}(k\Omega)$
11.3
8.45
6.8
4.53
3.4

Ver 1.0 www.sdw-tw.com 6



PCB Layout Recommendations

When laying out the printed circuit board, the following checking should be used to ensure proper operation of the SD00HA. Check the following in your layout:

- Does the (+) plates of C_{IN} connect to VIN as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
- \blacktriangleright Keep the (-) plates of C_{IN} and C_{OUT} as close as possible

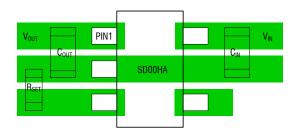
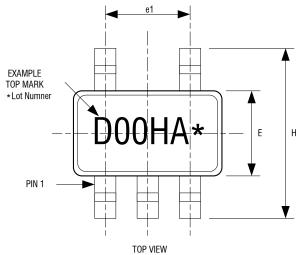


Figure 3. SD00HA Suggested Layout

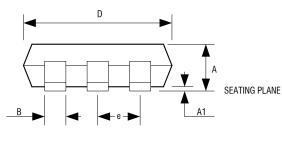
PACKAGE DESCRIPTION

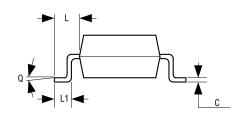
S0T23-5



5LD SOT-23 PACKAGE OUTLINE DIMENSIONS

Dimension	Min.	Max.	
Α	1.05	1.35	
A1	0.04	0.15	
В	0.3	0.5	
С	0.09	0.2	
D	2.8	3.0	
Н	2.5	3.1	
E	1.5	1.7	
е	0.95 REF.		
e1	1.90 REF.		
L1	0.2	0.55	
L	0.35	0.8	
Q	0° 10°		





NOTE:

- 1.DIMENSIONS ARE IN MILLIMETERS
- 2.DRAWING NOT TO SCALE
- 3.DIMENSIONS ARE INCLUSIVE OF PLATING

FRONT VIEW

4.DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR