

## N-Channel Super Junction Power MOSFET

### Description

SM180R65C is power MOSFET using advanced super junction technology that can realize very low on-resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of low EMI to designers as well as low switching loss.

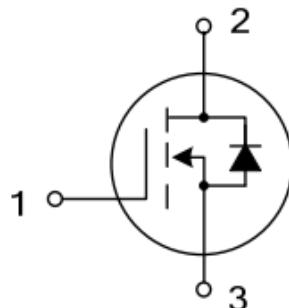
$I_D$	20A
$V_{DSS}$	650V
$R_{dson\ (max.)}$	$0.18\Omega(V_{GS}=10V, I_D=10A)$
$Q_g$	36nC

### General Features

- 20A, 650V,  $R_{DS(on)(max)} = 0.18\Omega$  @  $V_{GS} = 10V$
- Low Gate charge
- Low  $C_{rss}$
- Fast Switching
- Improved dv/dt Capability

### Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



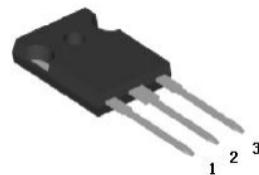
1.Gate 2.Drain 3.Source



TO-220



TO-220F



TO-247

## Order Information

Order Information	Marking ID	Package	Packing Type Supplied As
SM180R65CT2TL	180R65C	TO220F-3L	1000 units on Box, 5000 units on Carton
SM180R65CT1TL	180R65C	TO220-3L	1000 units on Box, 5000 units on Carton
SM180R65CT8TL	180R65C	TO247-3L	450 units on Box, 2250 units on Carton

## Absolute Maximum Ratings Ta=25 °C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source Voltage	V <sub>DS</sub>	650	V
Gate-source Voltage	V <sub>GS</sub>	±30	V
Continuous Drain Current(Ta=25°C)	I <sub>D</sub>	20	A
Drain Current-Pulsed	I <sub>DM</sub>	62	A
Total Dissipation (Ta=25°C)	TO247	P <sub>D</sub>	W
	TO220		
	TO220F		
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-65 to 150	°C
Single Pulse Avalanche Energy	E <sub>AS</sub>	485	mJ
ESD HBM(Human Body Mode)		≥2000	V
ESD MM(Machine Mode)		≥200	V

## Electrical Characteristics Ta = 25°C

PARAMETER	Symbol	Test Condition	MIN	Typ	MAX	UNIT
Drain-source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650			V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
Drain-source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			1	uA
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.5	V
Gate-body Leakage Current (V <sub>DS</sub> = 0)	I <sub>GSS</sub>	V <sub>GS</sub> =±30V			±100	nA
Forward Transconductance	G <sub>FS</sub>	V <sub>DS</sub> =10V I <sub>D</sub> =20A	6			S
Static Drain-source On Resistance	R <sub>DSS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A		0.15	0.18	Ω

**Note:** Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant in temperature etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings

## Thermal Characteristics Ta=25 °C

PARAMETER		Symbol	TYP	MAX	UNIT
Maximum Junction-to-case	TO220F,TO220	$R_{QJC}$		3.57	°C/W
	TO-247			0.3	
Maximum Junction-to-Ambient	TO220F,TO220	$R_{QJA}$		80	°C/W
	TO247			40	

**Note1:** Ensure that the channel temperature does not exceed 150°C

**Note2:**  $V_{DD}=50V$ ,  $T_{ch}=25$  °C(initial),  $I_{AS}=20A$ ,  $R_g=25\Omega$

**Note3:** This transistor is sensitive to electrostatic and should be handled with care

## Dynamic Characteristics Ta = 25 °C

PARAMETER	Symbol	Test Condition	MIN	TYP	MAX	UNIT
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$		1540		pF
output Capacitance	$C_{oss}$			1480		pF
Reverse Transfer Capacitance	$C_{rss}$			32		pF

## Switching Characteristics Ta=25 °C

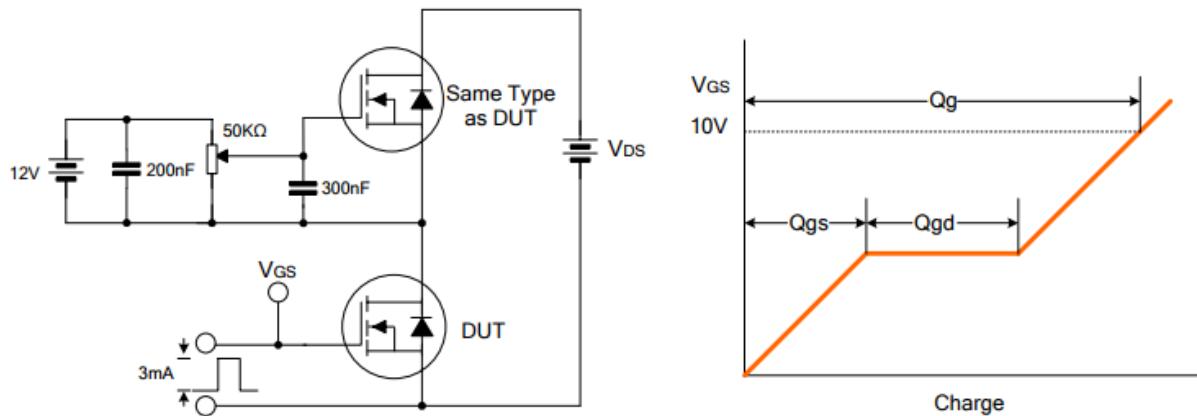
PARAMETER	Symbol	Test Condition	MIN	TYP	MAX	UNIT
Turn-On Delay Time	$T_{d(on)}$	$V_{DS}=400V, I_D=20A, V_{GS}=10V, R_G=25\Omega$		38		nS
Turn-On Rise Time	$T_r$			39		nS
Turn-Off Delay Time	$T_{d(off)}$			170		nS
Turn-Off Rise Time	$T_f$			45		nS
Total Gate Charge	$Q_g$	$V_{DS}=480V, I_D=20A, V_{GS}=10V$		36		nC
Gate-Source Charge	$Q_{gs}$			9		nC
Gate-Drain Charge	$Q_{gd}$			13		nC

## Drain-Source Diode Maximum Ratings and Characteristics Ta=25 °C

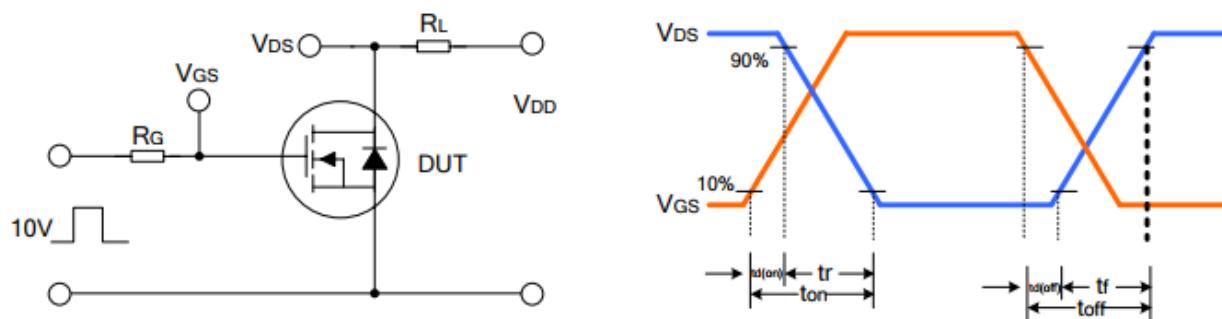
PARAMETER	Symbol	Test Condition	MIN	TYP	MAX	UNIT
Max. Diode Forward Current	$I_s$	Integral Reverse P-N Junction Diode in the MOSFET			20	A
Pulsed Source Current	$I_{sm}$				62	
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_s=20A$		0.9	1.5	V
Reverse Recovery Time	$T_{rr}$	$V_{GS}=0V, I_s=20A, dI/dt=100A/\mu s$		320		nS
Reverse Recovery Charge	$Q_{rr}$			5.5		$\mu C$

## Test Circuit

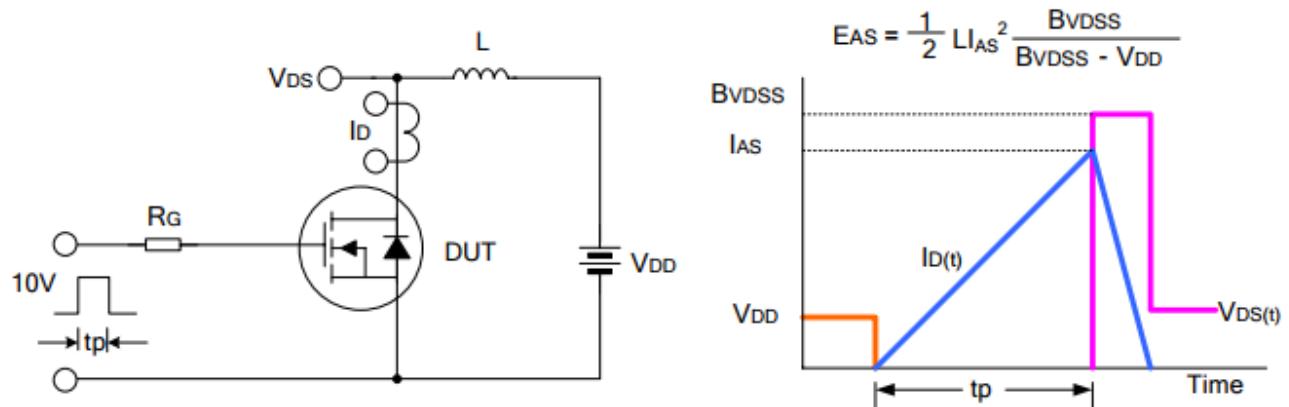
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform



## Typical Characteristics Curve

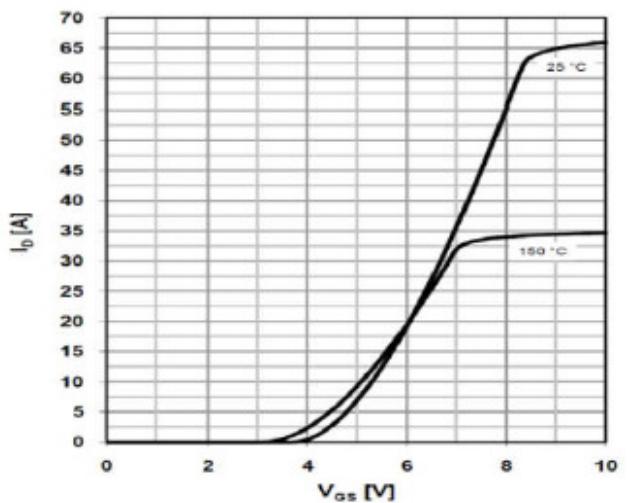
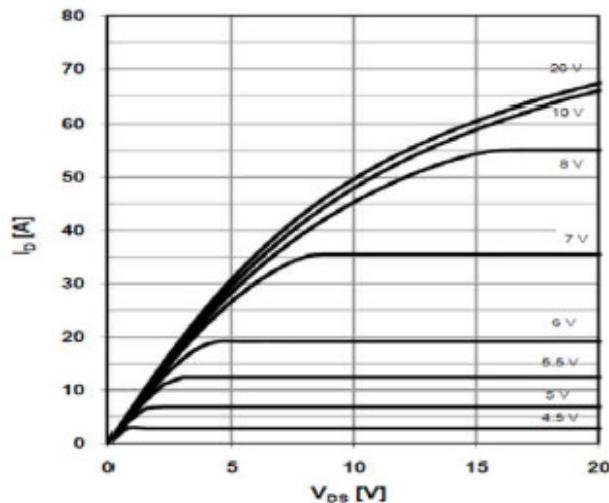


Figure 1: Output Characteristics

Figure 2: Transfer Characteristics

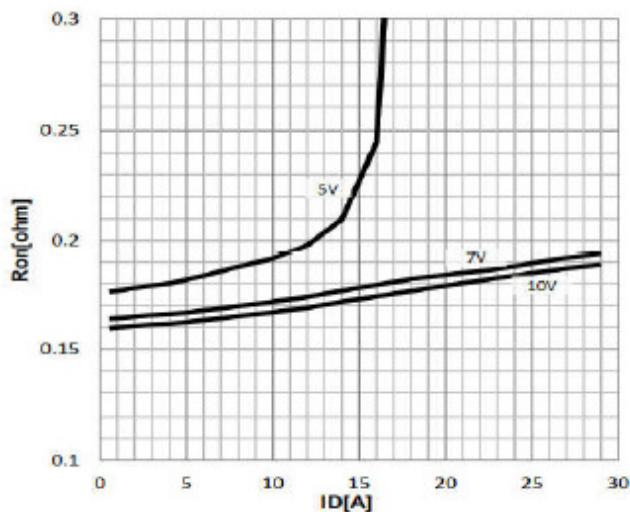


Figure 3: On Resistance Vs Drain Current

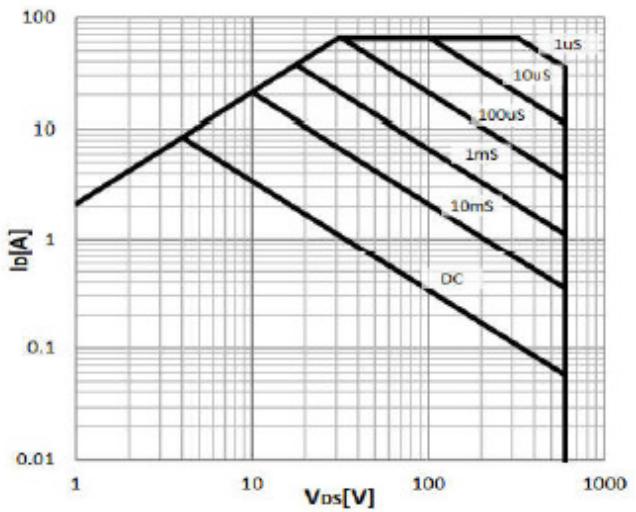


Figure 4: Safe

Operating Area

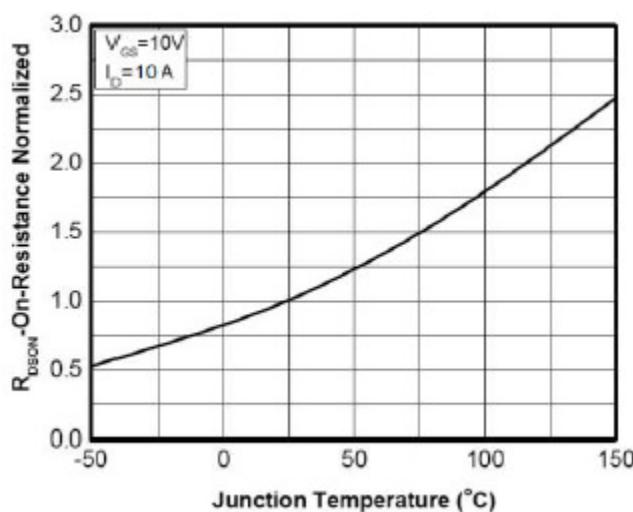


Figure5: On Resistance Vs Junction Temperature Characteristics

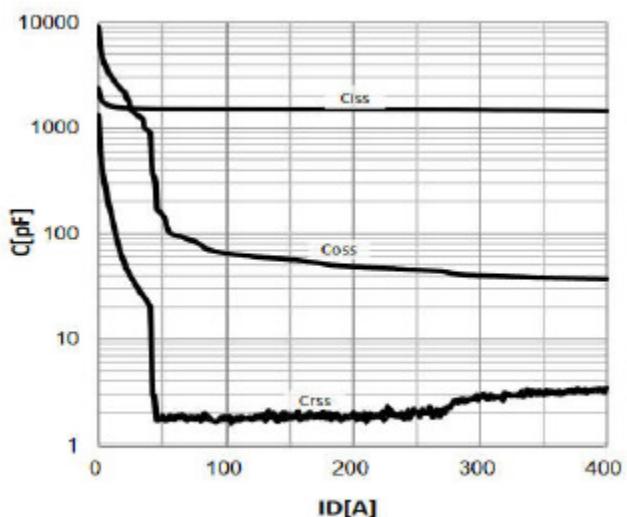


Figure6: Capacitance

## Typical Characteristics Curve

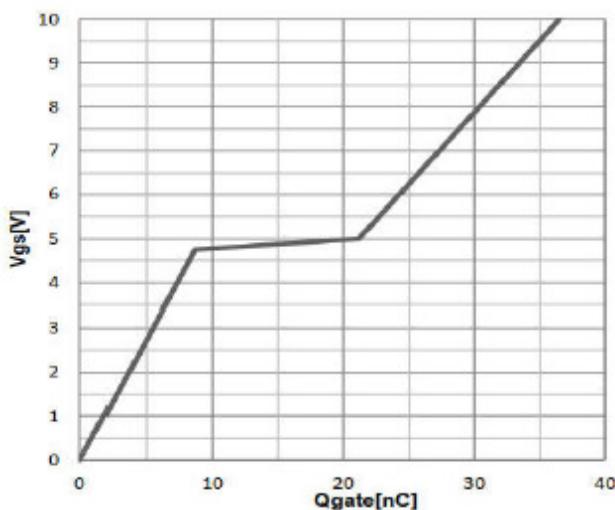


Figure7: Gate Charge Waveform  
Diode Forward Voltage

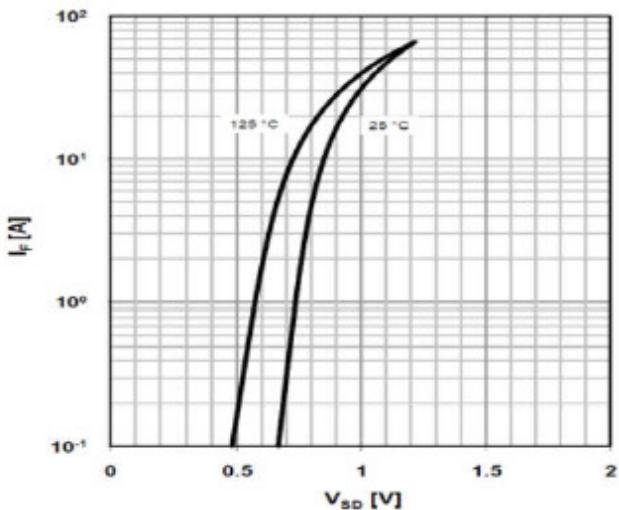
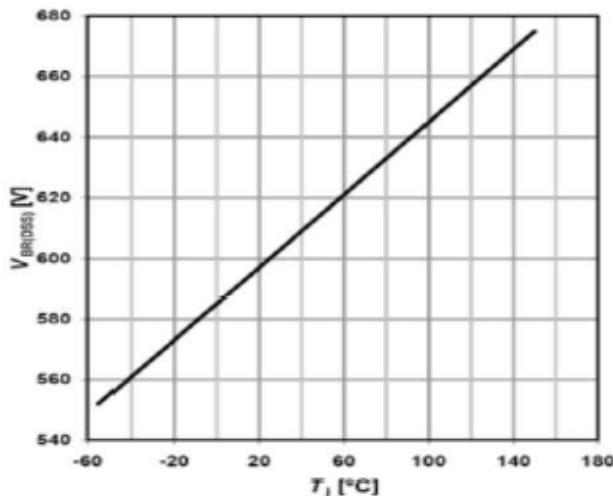
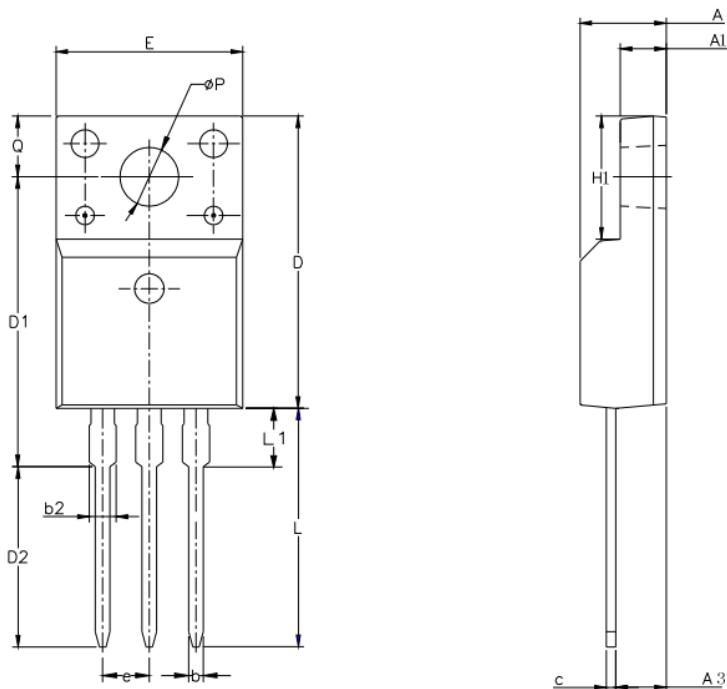


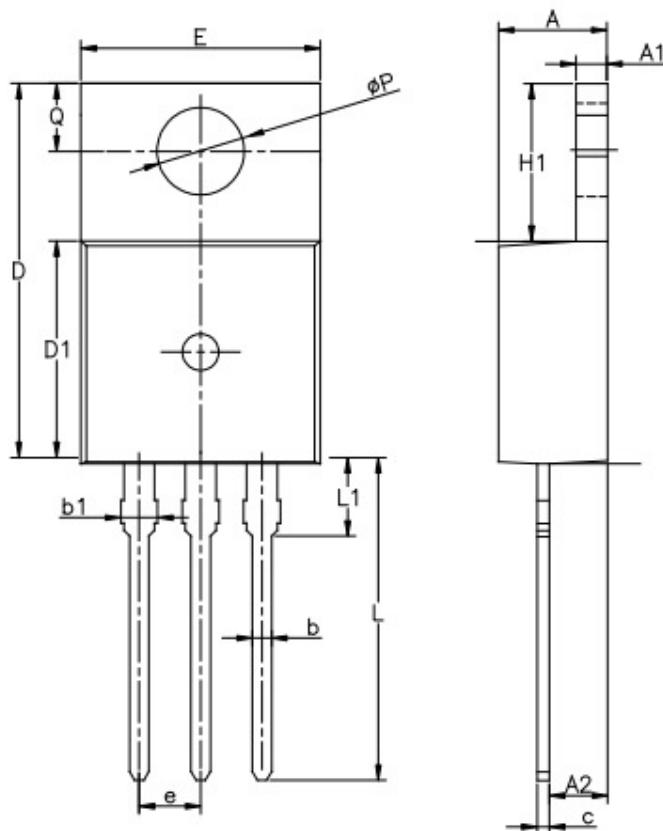
Figure8: Source-Drain





SYMBOL	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BCS		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	/	/	3.50
ØP	3.00	3.18	3.40
Q	3.05	3.30	3.55

## Outline Information (TO220-3L)



SYMBOL	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	1.00	1.30	1.50
A2	1.80	2.40	2.80
b	0.60	0.80	1.00
b1	1.00	—	1.60
c	0.30	—	0.70
D	15.10	15.70	16.10
D1	8.10	9.20	10.00
E	9.60	9.90	10.40
e	2.54BSC		
H1	6.10	6.50	7.00
L	12.60	13.08	13.60
L1	—	—	3.95
φP	3.40	3.70	3.90
Q	2.60	—	3.20

## Outline Information (TO247-3L)

UNIT: mm							
SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
A	4.60		5.15	A1	1.30		1.60
b	2.86		3.26	b1	1.86		2.26
b2		1.20		C		0.50	
D	19.00		21.00	E	15.45		15.75
E1	12.00		13.06	e		5.45	
L	14.00		14.60	L1	5.20		5.88
L2	24.00		24.40	L3	10.00		10.60
P		3.50		Q	2.30		2.70

