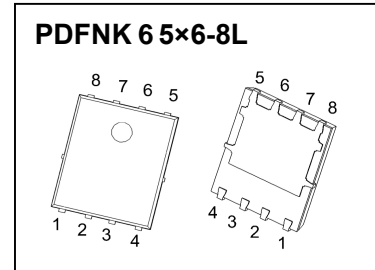


**CJAC50P03 P-Channel Power MOSFET**

<b>V<sub>(BR)DSS</sub></b>	<b>R<sub>DS(on)MAX</sub></b>	<b>I<sub>D</sub></b>
-30V	7mΩ@-10V	-50A



**DESCRIPTION**

The CJAC50P03 uses advanced trench technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. It can be used in a wide variety of applications

**FEATURES**

- High density cell design for ultra low R<sub>DS(ON)</sub>
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

**APPLICATIONS**

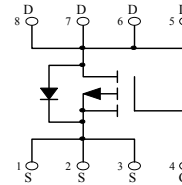
- Battery and loading switching

**MARKING**



CJAC50P03 = Part No.  
 Solid dot=Pin1 indicator  
 XXX=Date Code

**EQUIVALENT CIRCUIT**



**MAXIMUM RATINGS ( T<sub>a</sub>=25°C unless otherwise noted )**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub> <sup>(1)</sup>	-50	A
Pulsed Drain Current	I <sub>DM</sub>	-200	A
Single Pulsed Avalanche Energy	E <sub>AS</sub> <sup>(2)</sup>	300	mJ
Power Dissipation	P <sub>D</sub>	2	W
Thermal Resistance from Junction to Ambient	R <sub>θJA</sub> <sup>(1)</sup>	62.5	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55 ~+150	°C
Lead Temperature for Soldering Purposes(1/8" from case for 10s)	T <sub>L</sub>	260	°C

(1).Mounted on a glass epoxy board of 25.4 mm x 25.4 mm x 0.8 mm

(2).E<sub>AS</sub> condition: V<sub>DD</sub>=15V,L=0.5mH, R<sub>G</sub>=25Ω, Starting T<sub>J</sub> = 25°C

# MOSFET ELECTRICAL CHARACTERISTICS

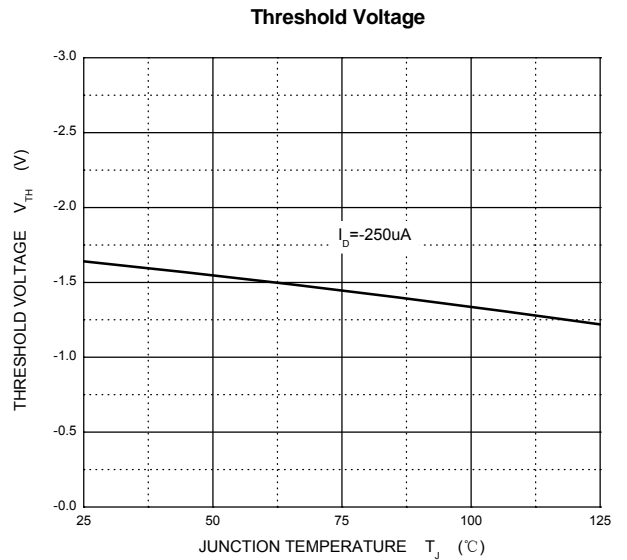
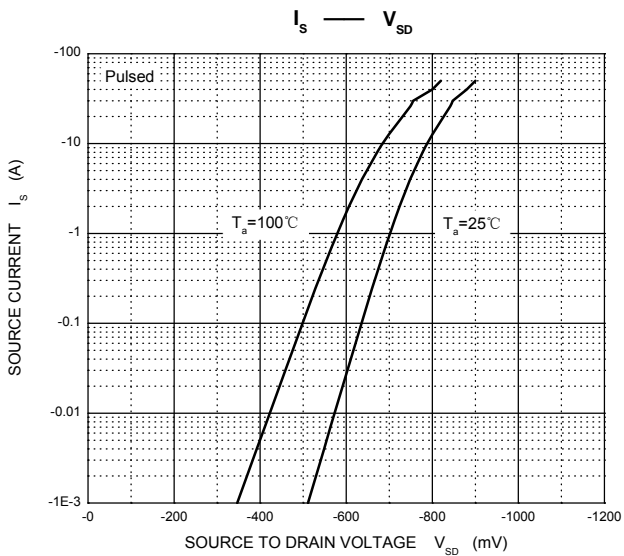
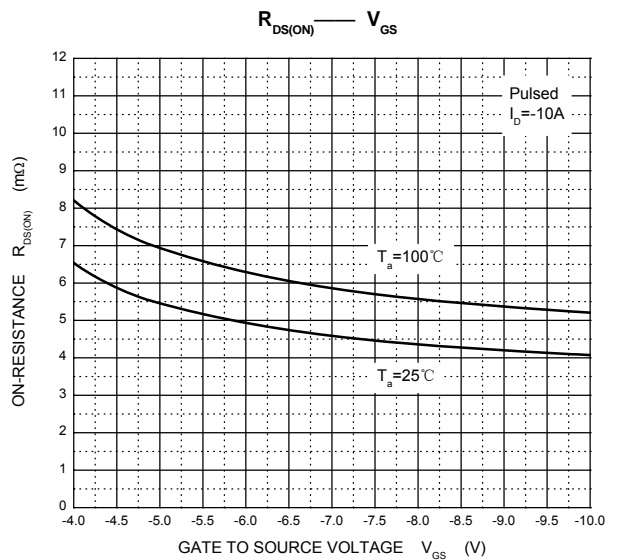
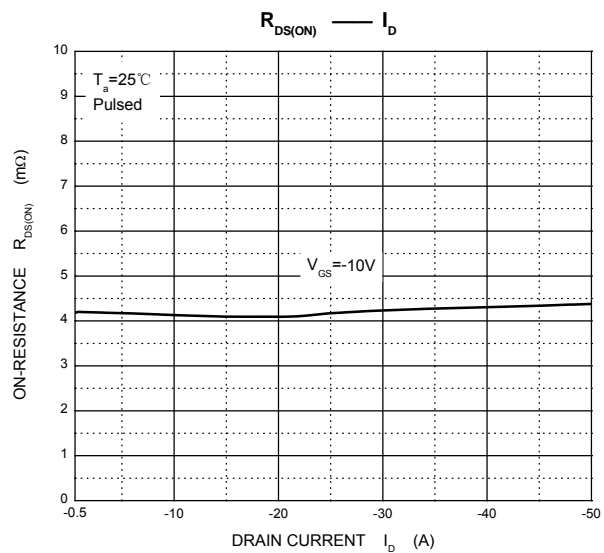
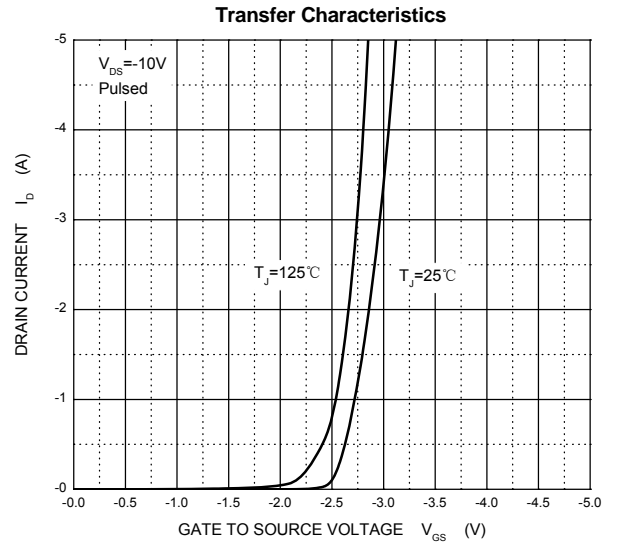
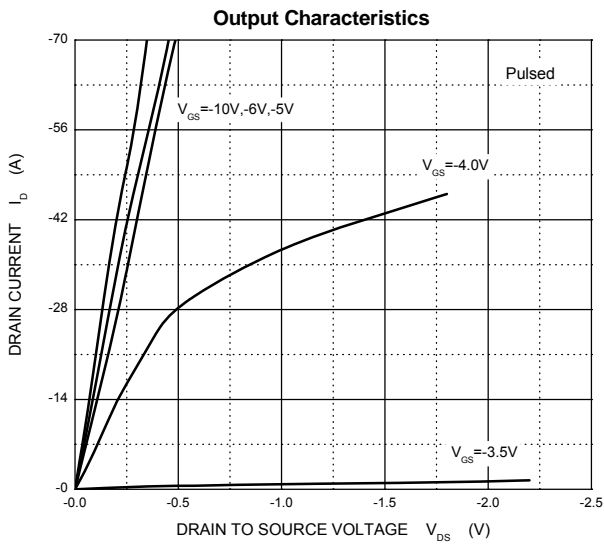
$T_a=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Off characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = -30V, V_{GS} = 0V$			-1	$\mu A$
Gate-body leakage current	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
<b>On characteristics (note1)</b>						
Gate-threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.5	-2.5	V
Static drain-source on-state resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -10A$		4.4	7	m $\Omega$
Forward transconductance	$g_{fs}$	$V_{DS} = -10V, I_D = -15A$		20		S
<b>Dynamic characteristics (note 2)</b>						
Input capacitance	$C_{iss}$	$V_{DS} = -15V, V_{GS} = 0V,$ $f = 1MHz$		3590		pF
Output capacitance	$C_{oss}$			695		
Reverse transfer capacitance	$C_{rss}$			665		
<b>Switching characteristics (note 2)</b>						
Total gate charge	$Q_g$	$V_{DS} = -15V, V_{GS} = -10V,$ $I_D = -10A$		84		nC
Gate-source charge	$Q_{gs}$			11.7		
Gate-drain charge	$Q_{gd}$			25		
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15V, I_D = -10A,$ $V_{GS} = -10V, R_G = 6\Omega$		13		ns
Turn-on rise time	$t_r$			12		
Turn-off delay time	$t_{d(off)}$			50		
Turn-off fall time	$t_f$			14		
<b>Drain-Source Diode Characteristics</b>						
Drain-source diode forward voltage(note1)	$V_{SD}$	$V_{GS} = 0V, I_S = -10A$		-0.85	-1.2	V
Continuous drain-source diode forward current(note3)	$I_S$				-50	A
Pulsed drain-source diode forward current	$I_{SM}$				-70	A
Reverse Recovery Time	$t_{rr}$	$T_J = 25^\circ C, I_F = -10A$			45	ns
Reverse Recovery Charge	$Q_{rr}$	$di/dt = 100A/\mu s$ (Note1)			43	nC

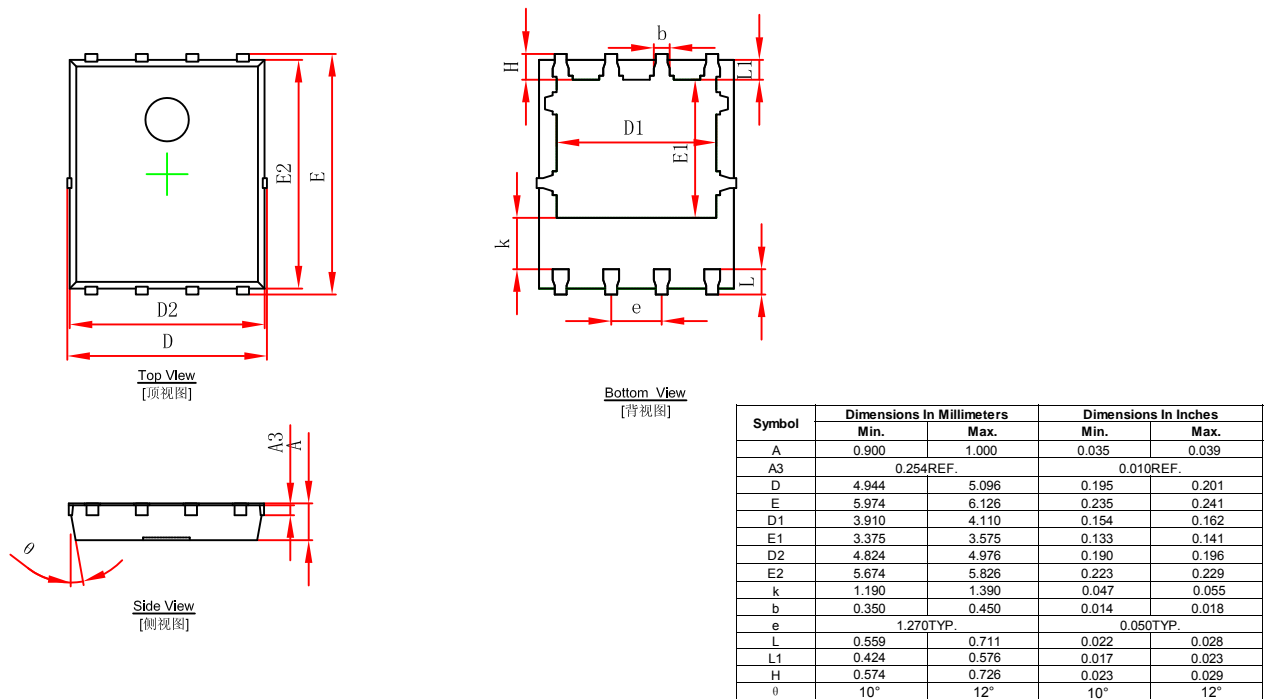
Notes:

1. Pulse Test : Pulse Width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
2. Guaranteed by design, not subject to production.
3. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

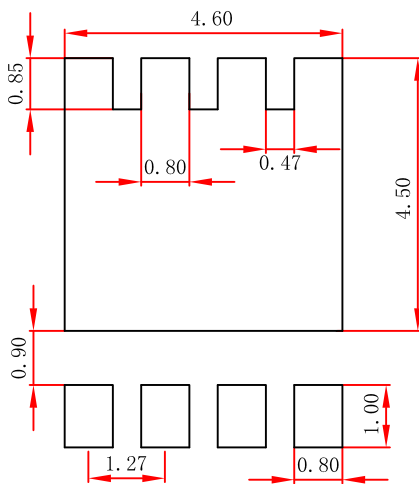
# Typical Characteristics



## PDFNWB5x6-8L Package Outline Dimensions



## PDFNWB5x6-8L Suggested Pad Layout



**Note:**

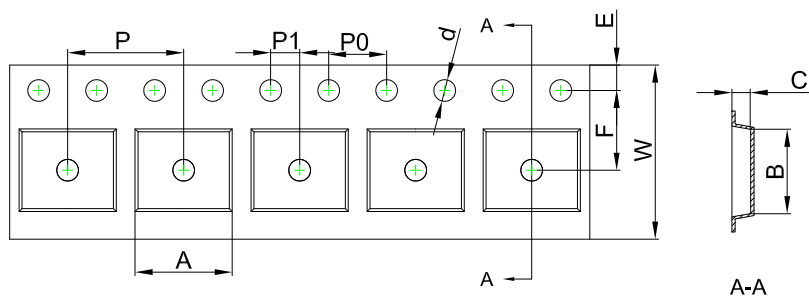
1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.

**NOTICE**

JSCJ reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. JSCJ does not assume any liability arising out of the application or use of any product described herein.

# PDFNWB5×6 Tape and Reel

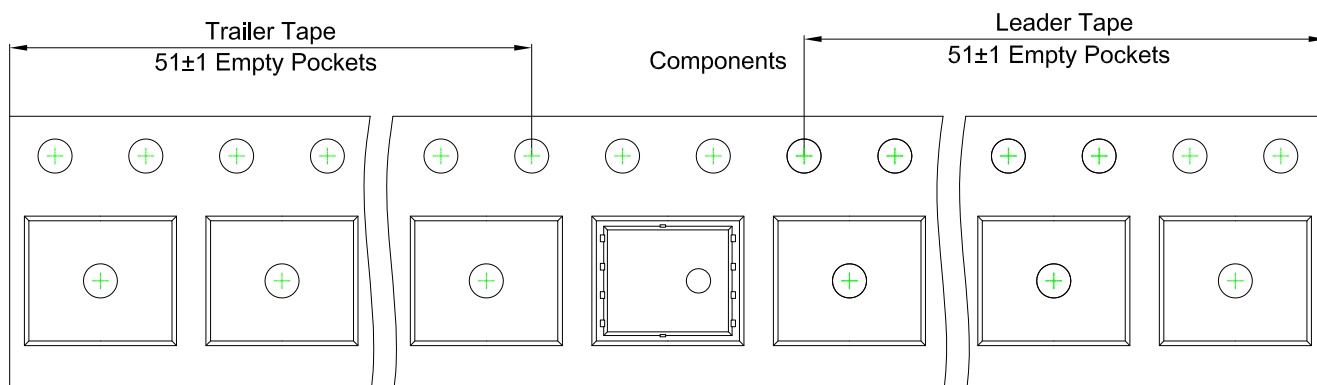
## PDFNWB5×6-8L Embossed Carrier Tape



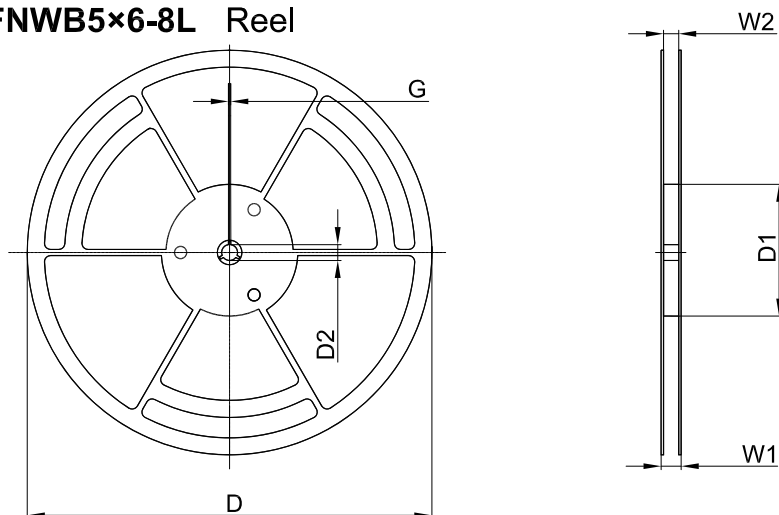
**Packaging Description:**  
**PDFNWB5×6-8L** parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 5,000 units per 13" or 33.0 cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
PDFNWB5×6-8L	6.30	5.30	1.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

## PDFNWB5×6-8L Tape Leader and Trailer



## PDFNWB5×6-8L Reel



Dimensions are in millimeter						
Reel Option	D	D1	D2	G	W1	W2
13"Dia	Ø330.00	100.00	13.00	1.90	17.60	12.40

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)
5,000 pcs	13 inch	5,000 pcs	340×336×29	50,000 pcs	353×346×365