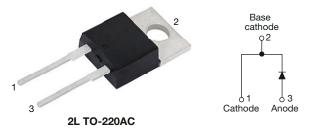
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Ultrafast Rectifier, 8 A FRED Pt®



PRIMARY CHARACTERISTICS						
I _{F(AV)} 8 A						
V _R	200 V					
V _F at I _F	0.895 V					
t _{rr} typ.	See Recovery table					
T _J max.	175 °C					
Package	2L TO-220AC					
Circuit configuration	Single					

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

VS-MUR820 is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Peak repetitive reverse voltage	V _{RRM}		200	V				
Average rectified forward current	I _{F(AV)}	Total device, rated V_R , $T_C = 150 \ ^\circ C$	8					
Non-repetitive peak surge current	I _{FSM}		100	А				
Peak repetitive forward current	I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 150 $^{\circ}$ C	16					
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
Forward valtage		I _F = 8 A	-	-	0.975	V
Forward voltage	V _F	I _F = 8 A, T _J = 150 °C	-	-	0.895	
Reverse leakage current		$V_{R} = V_{R}$ rated	-	-	5	
neverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA
Junction capacitance	CT	V _R = 200 V	-	25	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

Revision: 23-Nov-17 1 Document Number: 96186 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>





DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
		I _F = 1.0 A, dI _F /dt =	50 A/µs, V _R = 30 V	-	-	35			
Reverse recovery time	+	I _F = 0.5 A, I _R = 1.0 A, I _{REC} = 0.25 A		-	-	25	ns		
neverse recovery time	t _{rr}	T _J = 25 °C		-	20	-	115		
		T _J = 125 °C		-	34	-			
Peak recovery current	1	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs	-	1.7	-	А		
Peak recovery current	IRRM	T _J = 125 °C	$V_{\rm B} = 160 {\rm V}$	-	4.2	-	~		
Reverse recovery charge	0	T _J = 25 °C		-	23	-	'nC		
	Q _{rr}	T _J = 125 °C		-	75	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to case	R _{thJC}		-	-	3.0		
Thermal resistance, junction to ambient	R _{thJA}		-	-	50	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-		
Weight			-	2.0	-	g	
weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style 2L TO-220AC		MUF	R820		

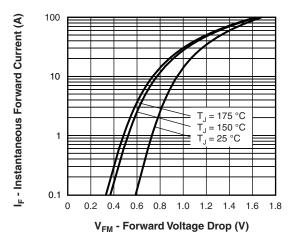
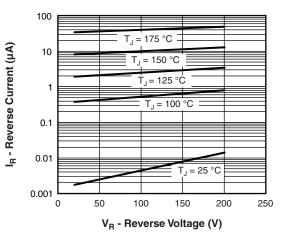
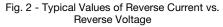


Fig. 1 - Maximum Forward Voltage Drop Characteristics





VS-MUR820-M3

Vishay Semiconductors



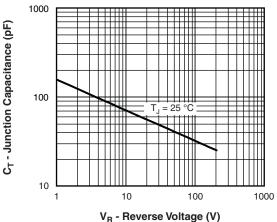


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

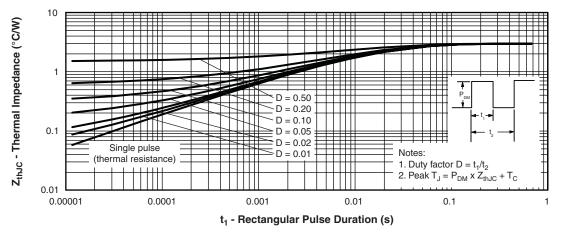
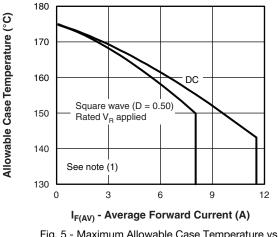
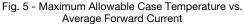


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics





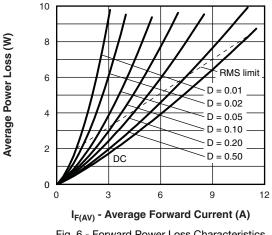


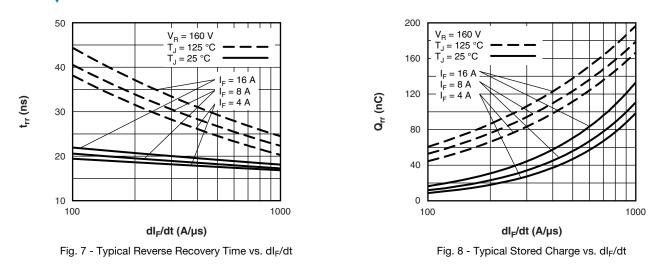
Fig. 6 - Forward Power Loss Characteristics

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Note

SHA

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

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 $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{rated } \mbox{V}_{R} \end{array}$

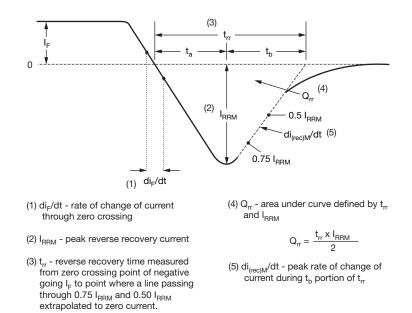


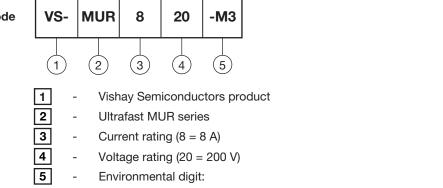
Fig. 9 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code



-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

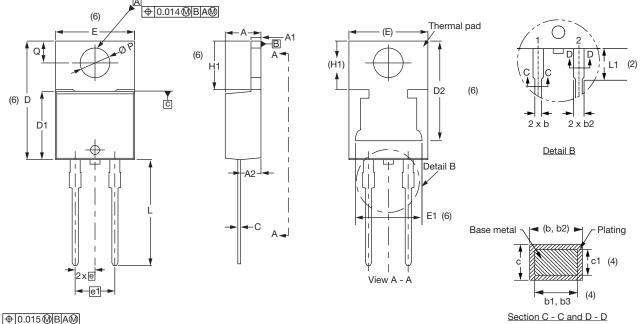
ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-MUR820-M3	50	1000	Antistatic plastic tube					

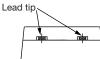
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96156				
Part marking information	www.vishay.com/doc?95391				



2L TO-220AC

DIMENSIONS in millimeters and inches





SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.50	2.92	0.098	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.35	0.585	0.604	3	
D1	8.38	9.02	0.330	0.355		

Conforms to JEDEC®	outline	TO-220AC
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SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

⁽⁴⁾ Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2, and E1
- ⁽⁷⁾ Outline conforms to JEDEC[®] TO-220, except D2

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⁽³⁾ Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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