

Vishay Semiconductors

High Voltage Input Rectifier Diode, 60 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	60 A			
V _R	1600 V			
V _F at I _F	1.15 V			
IFSM	950 A			
T _J max.	150 °C			
Package	TO-247AC 2L			
Circuit configuration	Single			

FEATURES

- · Very low forward voltage drop
- 150 °C max. operating junction temperature
- Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47





APPLICATIONS

- · Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Sinusoidal waveform	60	Α				
V _{RRM}		1600	V				
I _{FSM}		950	Α				
V _F	60 A, T _J = 25 °C	1.15	V				
T _J		-40 to +150	°C				

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-60EPS16-M3	1600	1700	1

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	T _C = 118 °C, 180° conduction half sine wave	60		
Maximum peak one cycle non-repetitive surge current		10 ms sine pulse, rated V _{RRM} applied	800	Α	
	IFSM	10 ms sine pulse, no voltage reapplied	950	*	
Maximum I ² t for fusing I ² t	10 ms sine pulse, rated V _{RRM} applied	3200	A ² s		
	1-1	10 ms sine pulse, no voltage reapplied	4525	A-S	
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	45 250	A ² √s	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum forward voltage drap	V	30 A, T _J = 25 °C		1.0	V
Maximum forward voltage drop	V_{FM}	60 A, T _J = 25 °C		1.15	V
Forward slope resistance	r _t		3.96	mΩ	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = 150 ^{\circ}{\rm C}$		0.74	V
Maximum reverse leakage current I _{RN}		T _J = 25 °C	V _R = Rated V _{RRM}	0.1	mA
	IRM	T _J = 150 °C		1.0] IIIA

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature	re range	T_J , T_{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.35	
Maximum thermal resistance, junction to ambient		R_{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2	
Approximate weight				6	g
				0.21	oz.
Mounting torque max	ninimum			6.0 (5)	kgf · cm
	naximu m			12 (10)	(lbf · in)
Marking device			Case style TO-247AC 2L	60EF	PS16

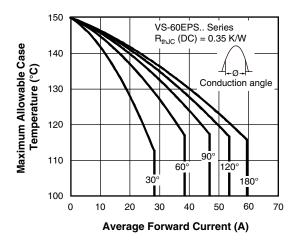


Fig. 1 - Current Rating Characteristics

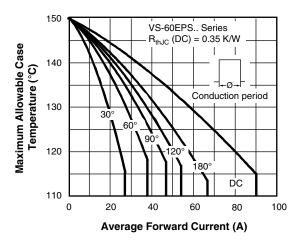


Fig. 2 - Current Rating Characteristics



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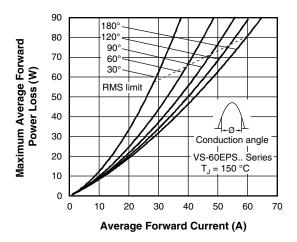


Fig. 3 - Forward Power Loss Characteristics

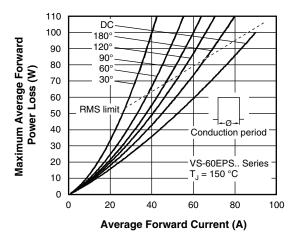


Fig. 4 - Forward Power Loss Characteristics

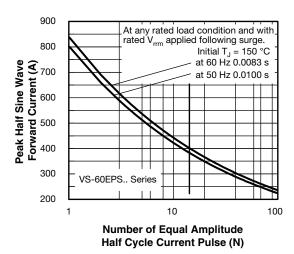


Fig. 5 - Maximum Non-Repetitive Surge Current

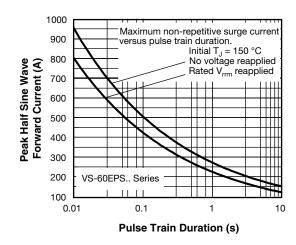


Fig. 6 - Maximum Non-Repetitive Surge Current

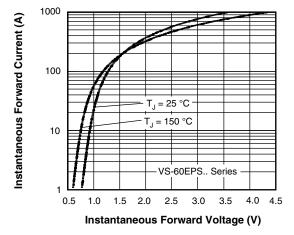


Fig. 7 - Forward Voltage Drop Characteristics

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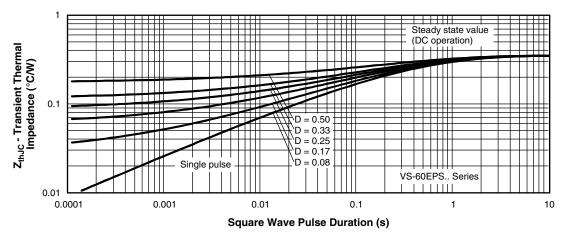
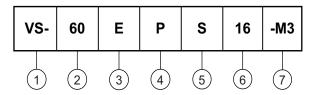


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (60 = 60 A)
- 3 Circuit configuration:

E = single diode

4 - Package:

P = TO-247AC 2L

5 - Type of silicon:

S = standard recovery rectifier

| 6 | - Voltage rating (16 = 1600 V)

7 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-60EPS16-M3	25	500	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96144</u>				
Part marking information	www.vishay.com/doc?95648			



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