



NCE P-Channel Enhancement Mode Power MOSFET

Description

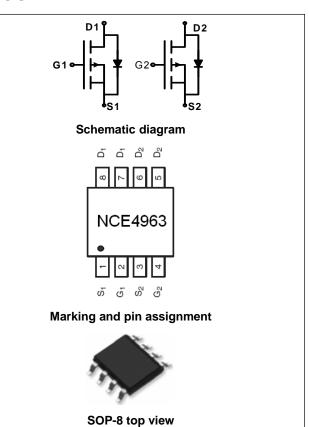
The NCE4963 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

- V_{DS} = -20V,I_D = -7A
 R_{DS(ON)} < 27mΩ @ V_{GS}=-4.5V
 R_{DS(ON)} < 39mΩ @ V_{GS}=-2.5V
- High power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- Motor drive
- Load switch
- Power management



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4963	NCE4963	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-20	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	ID	-7	А
Drain Current-Pulsed (Note 1)	I _{DM}	-40	А
Maximum Power Dissipation	PD	3.0	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	42	°C /W

Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-20	-	-	V





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Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA	-0.6	-0.8	-1.4	V
	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-6.5A	-	21	27	mΩ
Drain-Source On-State Resistance		V _{GS} =-2.5V, I _D =-5A	-	29	39	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =3A	-	10	-	S
Dynamic Characteristics (Note4)	·					
Input Capacitance	Clss		-	1210	-	PF
Output Capacitance	Coss	V _{DS} =-10V,V _{GS} =0V,	-	310	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	290	-	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t _{d(on)}		-	25	-	nS
Turn-on Rise Time	tr	V _{DD} =-10V, ID=-1A,	-	30	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-4.5V,R _{GEN} =6 Ω	-	70	-	nS
Turn-Off Fall Time	t _f		-	50	-	nS
Total Gate Charge	Qg		-	10	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-10V,I _D =-6.5A,V _{GS} =-4.5V	-	1.5	-	nC
Gate-Drain Charge	Q _{gd}		-	3	-	nC
Drain-Source Diode Characteristics	L.			1		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-7A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production





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Typical Electrical and Thermal Characteristics

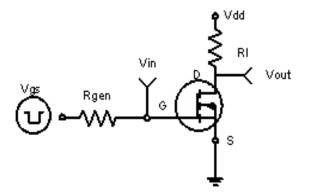
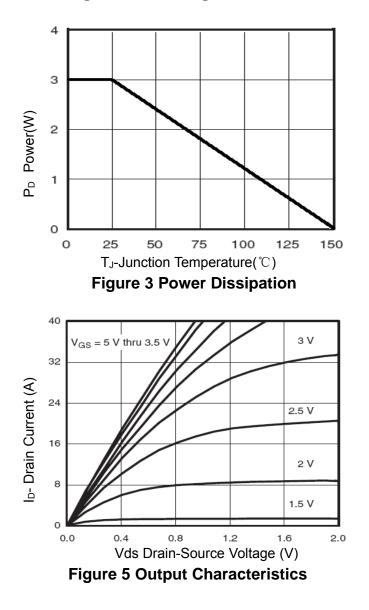
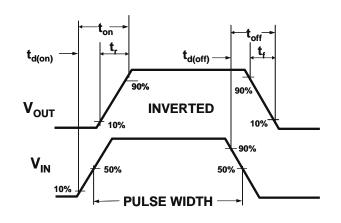


Figure 1 Switching Test Circuit







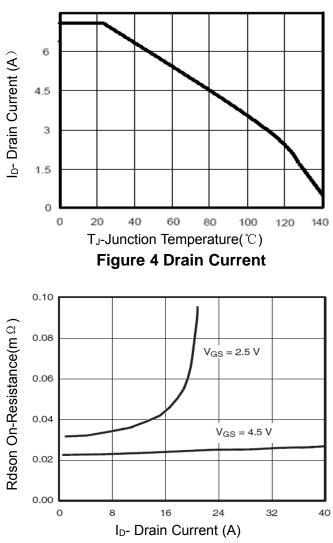
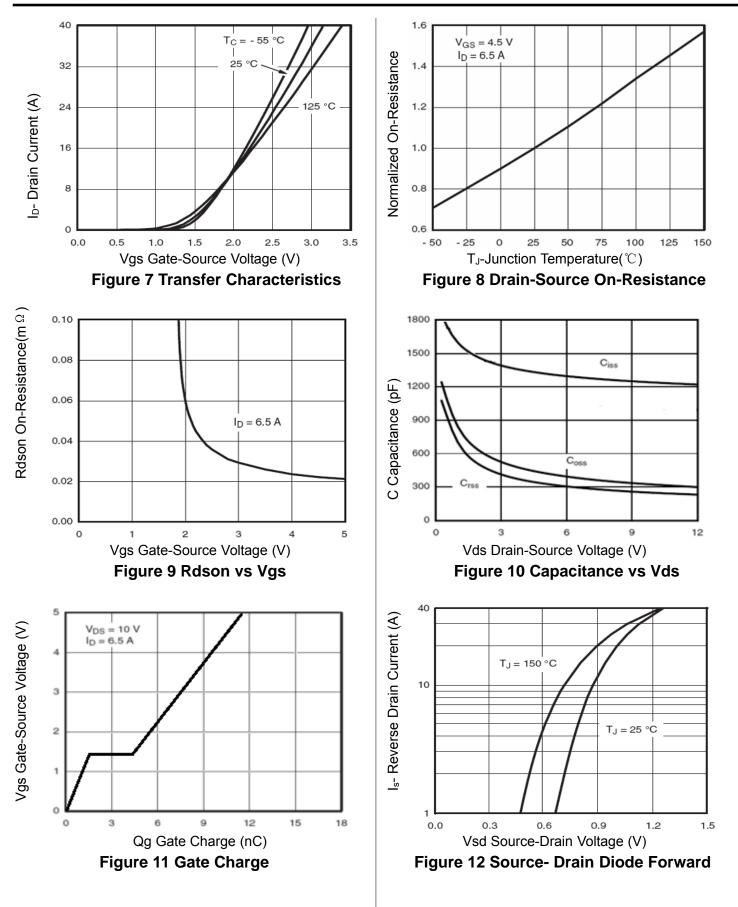


Figure 6 Drain-Source On-Resistance



Pb Free Product

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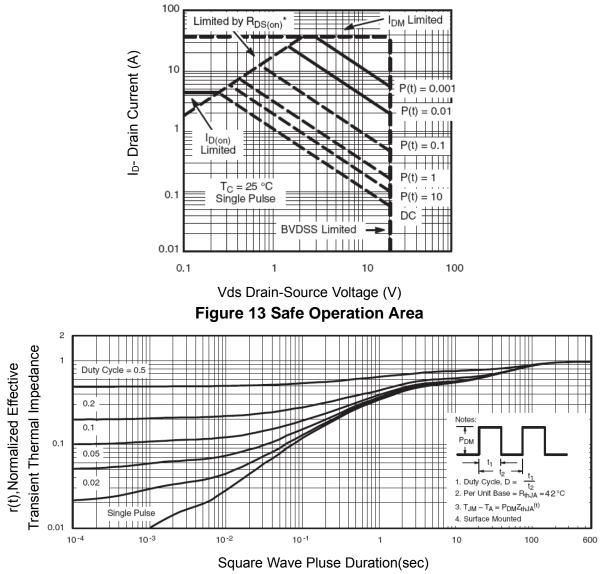


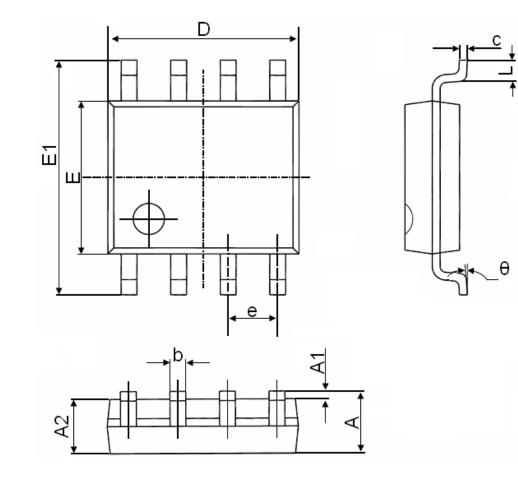
Figure 14 Normalized Maximum Transient Thermal Impedance







SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
e	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8 °	0 °	8°	







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