



NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE01H10D uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

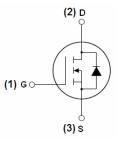
- $V_{DS} = 100V, I_D = 100A$ $R_{DS(ON)} < 13mΩ @ V_{GS} = 10V$ (Typ:9.9mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-263-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE01H10D	NCE01H10D	TO-263-2L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage	100	V
V _G s	Gate-Source Voltage	±20	V
I _D	Drain Current-Continuous	100	Α
I _D (100℃)	Drain Current-Continuous(TC=100°C)	80	Α
I _{DM}	Pulsed Drain Current	380	Α
P _D	Maximum Power Dissipation	200	W
	Derating factor	1.33	W/℃
E _{AS}	Single pulse avalanche energy (Note 5)	800	mJ
T_{J}, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 175	$^{\circ}$ C

NCE01H10D

Thermal Characteristic

Electrical Characteristics (T_C=25°C unless otherwise noted)

Symbol		Parameter	Condition	Min	Тур	Max	Unit
Off Characteris	stics						
BV _{DSS}	Drain-Source Breakdo	own Voltage	V _{GS} =0V I _D =250μA	100	110	-	V
I _{DSS}	Zero Gate Voltage Dr	Zero Gate Voltage Drain Current		-	-	1	μΑ
I _{GSS}	Gate-Body Leakag	Gate-Body Leakage Current		-	-	±100	nA
On Characteris	stics (Note 3)						
$V_{GS(th)}$	Gate Threshold \	/oltage	$V_{DS}=V_{GS},I_{D}=250\mu A$	2	3	4	V
R _{DS(ON)}	Drain-Source On-State	e Resistance	V _{GS} =10V, I _D =40A	-	9.9	13	mΩ
g _{FS}	Forward Transcond	Forward Transconductance		V _{DS} =10V,I _D =20A 50		-	S
Dynamic Chara	acteristics (Note4)						
C _{lss}	Input Capacita	ance	\/ -50\/\/ -0\/	-	4800	-	PF
C _{oss}	Output Capacit	ance	V_{DS} =50V, V_{GS} =0V, F=1.0MHz	-	340	-	PF
C _{rss}	Reverse Transfer Ca	apacitance	F=1.UIVID2	-	150	-	PF
Switching Cha	racteristics (Note 4)						
t _{d(on)}	Turn-on Delay	Time		-	15	-	nS
t _r	Turn-on Rise	Гіте	V _{DD} =50V,I _D =40A	-	50	-	nS
t _{d(off)}	Turn-Off Delay	Time	V_{GS} =10V, R_{GEN} =2.5 Ω	-	40	-	nS
t _f	Turn-Off Fall 1	Time		-	55	-	nS
Qg	Total Gate Ch	arge	\/ -90\/ -404	-	85	-	nC
Q _{gs}	Gate-Source C	harge	$V_{DS}=80V,I_{D}=40A,$ $V_{GS}=10V$	-	18	-	nC
Q_{gd}	Gate-Drain Ch	arge	VGS-10V	-	28	-	nC
Drain-Source D	Diode Characteristics						
V _{SD}	Diode Forward Voltage	ge (Note 3)	V _{GS} =0V,I _S =40A	-	-	1.2	V
Is	Diode Forward Curre	ent (Note 2)	-	-	-	57	Α
t _{rr}	Reverse Recover	y Time	TJ = 25°C, IF = 40A	-	38	80	nS
Qrr	Reverse Recovery	Charge	di/dt = 100A/μs(Note3)	-	53	100	nC
t _{on}	Forward Turn-On	Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				y LS+LD)

Notes:

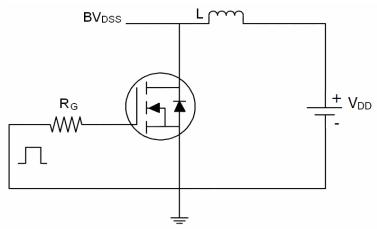
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition:Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V,L=0.5mH,Rg=25 Ω



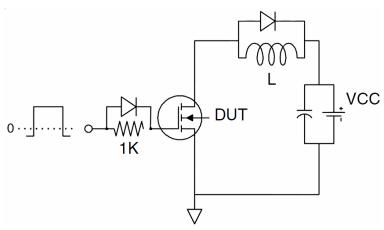
NCE01H10D

Test Circuit

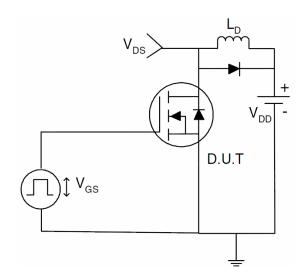
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Pb Free Product



NCE01H10D

Typical Electrical and Thermal Characteristics (Curves)

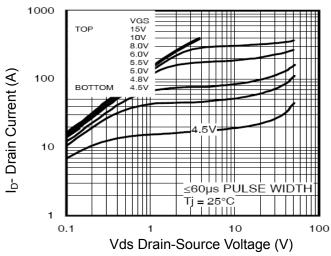


Figure 1 Output Characteristics

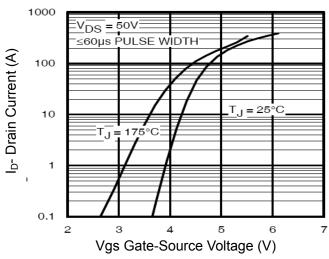
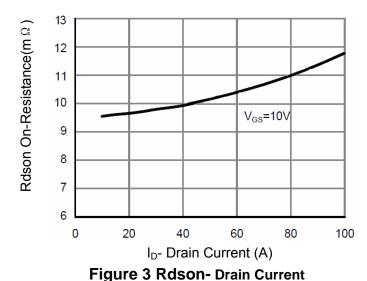


Figure 2 Transfer Characteristics



Normalized On-Resistance 2.5 I_D = 40A $V_{GS} = 10V$ 2.0 1.5 1.0 0.5 -60 -40 -20 0 20 40 60 80 100120140160180 T_J -Junction Temperature($^{\circ}$ C)

Figure 4 Rdson-JunctionTemperature

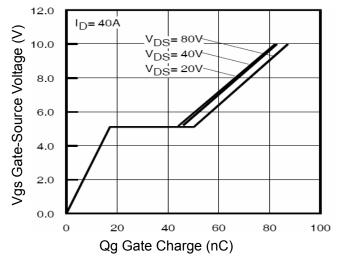


Figure 5 Gate Charge

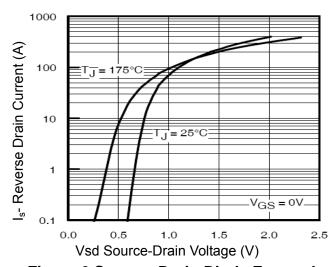


Figure 6 Source- Drain Diode Forward



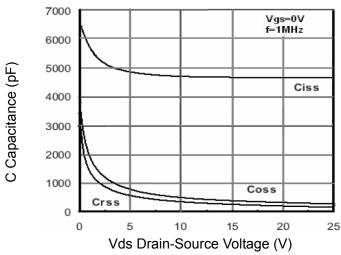


Figure 7 Capacitance vs Vds

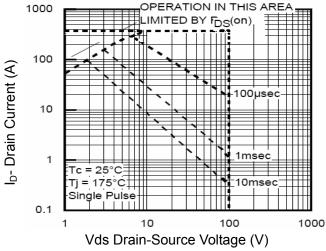


Figure 8 Safe Operation Area

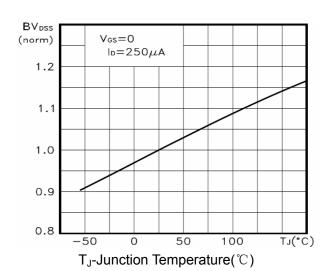


Figure 9 BV_{DSS} vs Junction Temperature

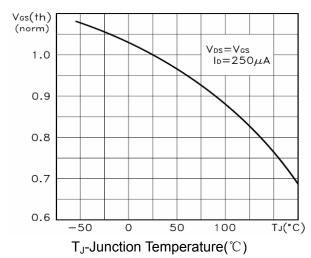


Figure 10 V_{GS(th)} vs Junction Temperature

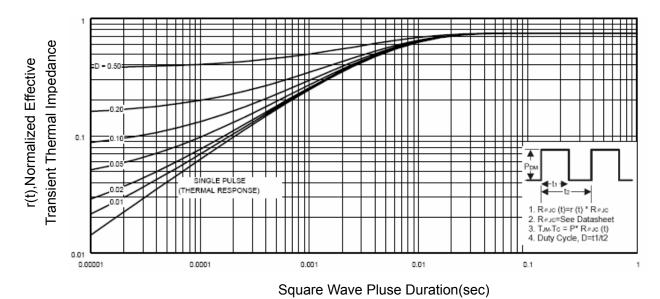
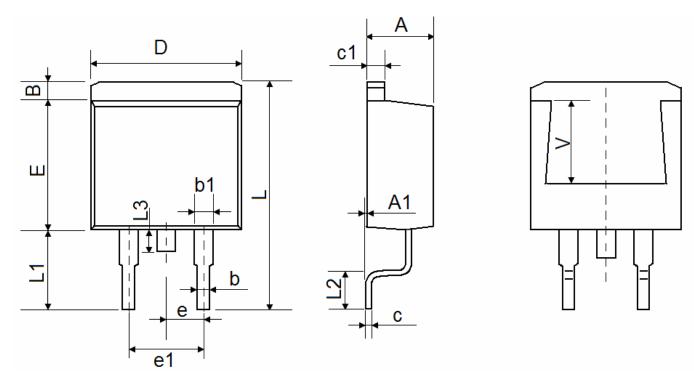


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-263-2L Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.600	REF	0.220 REF		



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