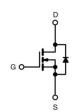
# **Sinai Power Technologies**

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# **N-channel Power MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V) at T <sub>J</sub> max.	700			
R <sub>DS(on)</sub> max. at 25°C (Ω)	V <sub>GS</sub> =10V	2.4		
Q <sub>g</sub> max. (nC)	2	4		
Q <sub>gs</sub> (nC)	4	ļ		
Q <sub>gd</sub> (nC)	3	3		
Configuration	single			





TO-220F

Schematic diagram

### **Features**

- I<sub>D</sub>=4A(Vgs=10V)
- Ultra Low Gate Charge
- Improved dv/dt Capability
- 100% Avalanche Tested
- RoHS compliant

# **Applications**

- Switching Mode Power Supplies (SMPS)
- PWM Motor Controls
- DC to DC Converters
- LED Lighting
- Bridge Circuits

ORDERING INFORMATION				
Device	SPC4N65G			
Device Package	TO-220F			
Marking	4N65G			

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C, unless otherwise noted)					
Parameter	Symbol	Limit	Unit		
Drain to Source Voltage	V <sub>DSS</sub>	650	V		
Continuous Drain Current (@T <sub>C</sub> =25°C)		4 (1)	Α		
Continuous Drain Current (@T <sub>C</sub> =100°C)	I <sub>D</sub>	2.5 (1)	А		
Drain current pulsed (2)	I <sub>DM</sub>	16 <sup>(1)</sup>	А		
Gate to Source Voltage	V <sub>GS</sub>	30	V		
Single pulsed Avalanche Energy (3)	E <sub>AS</sub>	160	mJ		
Peak diode Recovery dv/dt (4)	dv/dt	6	V/ns		
Total power dissipation (@T <sub>C</sub> =25°C)		22.7	W		
Derating Factor above 25°C	$\neg P_{D}$	0.18	W/ºC		
Operating Junction Temperature & Storage Temperature	T <sub>STG</sub> , T <sub>J</sub>	-55 to + 150	°C		
Maximum lead temperature for soldering purpose	TL	260	°C		
Mounting torque (5)		0.4~0.6	N.m		

#### Notes

- 1. Drain current is limited by maximum junction temperature.
- 2. Repetitive rating : pulse width limited by junction temperature.
- 3. L = 20mH,  $I_{AS}$  = 4A,  $V_{DD}$  = 50V,  $R_{G}$ =25 $\Omega$ , Starting at  $T_{J}$  = 25 $^{\circ}$ C
- 4.  $I_{SD} \le 4A$ , di/dt = 100A/us,  $V_{DD} \le BV_{DSS}$ , Starting at  $T_J = 25$ °C
- Mounting consideration for TO220 Fullpack:
  M3 screw plus flat washer is suggested, free of burr between devices and contact area,
  the devices are to be mounted to a hole not larger than 3.6mm in contact diameter (chamfer included).



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THERMAL CHARACTERISTICS				
Parameter	Symbol	Value	Unit	
Thermal resistance, Junction to case	R <sub>thjc</sub>	5.5	°C/W	
Thermal resistance, Junction to ambient	R <sub>thja</sub>	50.5	°C/W	

ELECTRICAL CHARACTERISTICS (Tc = 25°C unless otherwise specified)							
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit	
Off Characteristics							
Drain to source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	650			V	
Breakdown voltage temperature coefficient	ΔBV <sub>DSS</sub> / ΔTJ	I <sub>D</sub> =250uA, referenced to 25°C		0.52		V/°C	
Drain to source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			1	uA	
Drain to source leakage current		V <sub>DS</sub> =520V, T <sub>C</sub> =125°C			50	uA	
Gate to source leakage current, forward	I <sub>GSS</sub>	V <sub>GS</sub> =30V, V <sub>DS</sub> =0V			100	nA	
Gate to source leakage current, reverse	IGSS	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	nA	
On Characteristics							
Gate threshold voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=250uA$	2		4	V	
Drain to source on state resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2A		1.95	2.4	Ω	
Forward Transconductance	Gfs	$V_{DS} = 30 \text{ V}, I_{D} = 2 \text{ A}$		3.8		S	
Dynamic Characteristics							
Input capacitance	C <sub>iss</sub>			610			
Output capacitance	Coss	$V_{GS}$ =0V, $V_{DS}$ =25V, f=1MHz		62		pF	
Reverse transfer capacitance	$C_{rss}$			48			
Turn on delay time	t <sub>d(on)</sub>			13			
Rising time	tr	$V_{DS}$ =380V, $I_D$ =4A, $R_G$ =25 $\Omega$		30		ns	
Turn off delay time	t <sub>d(off)</sub>			45		115	
Fall time	<b>t</b> f			33			
Total gate charge	Qg	V <sub>DS</sub> =520V, V <sub>GS</sub> =10V, I <sub>D</sub> =4A		20			
Gate-source charge	Q <sub>gs</sub>			4		nC	
Gate-drain charge	$Q_{gd}$			8			

SOURCE TO DRAIN DIODE RATINGS CHARACTERISTICS							
Parameter	Symbol	Test conditions Min. Typ		Тур.	Max.	Unit	
Continuous source current	Is	Integral reverse p-n Junction diode in the MOSFET			4	Α	
Pulsed source current	I <sub>SM</sub>				16	Α	
Diode forward voltage drop.	V <sub>SD</sub>	I <sub>S</sub> =4A, V <sub>GS</sub> =0V			1.2	V	
Reverse recovery time	T <sub>rr</sub>	I <sub>S</sub> =4A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/us		230		ns	
Reverse recovery Charge	Qrr			1.5		uC	



## Fig1. Output characteristics

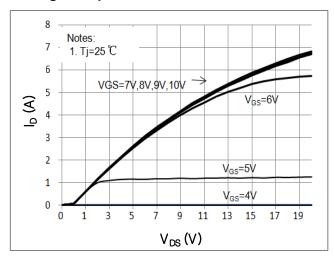


Fig3. Gate charge characteristics

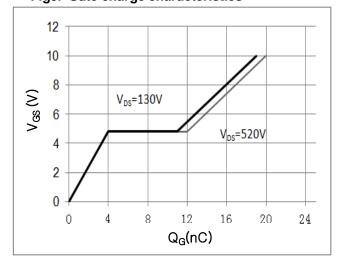


Fig 5. Rds(ON) vs junction temperature

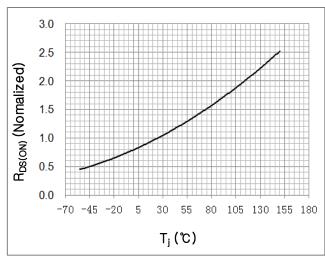


Fig2. Drain-source on-state resistance

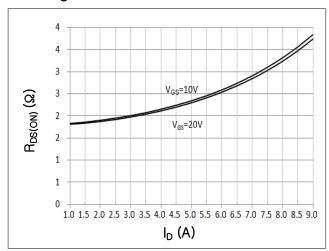


Fig 4. Capacitance Characteristics

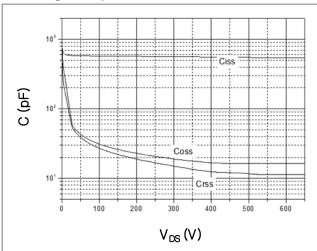
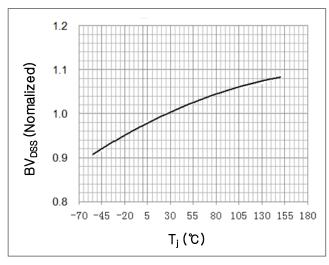


Fig 6. BVpss vs junction temperature



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Fig 7. Safe operating area

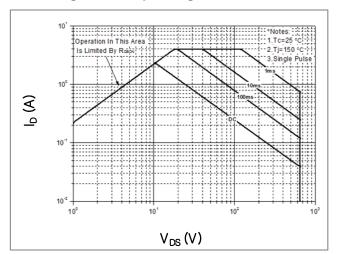


Fig 8. Transient thermal impedance

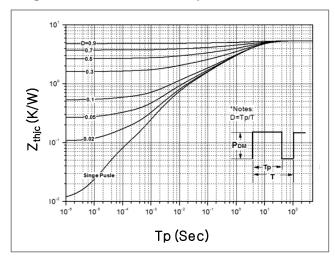


Fig 9. Forward characteristics of reverse diode

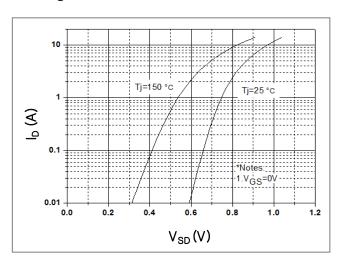
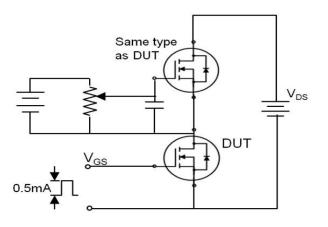
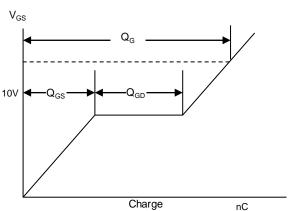


Fig 10. Gate charge test circuit & waveform







## Fig 11. Switching time test circuit & waveform

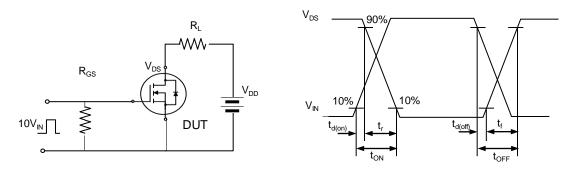


Fig 12. Unclamped Inductive switching test circuit & waveform

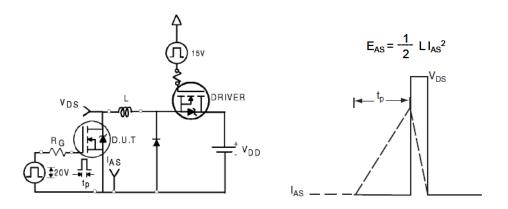
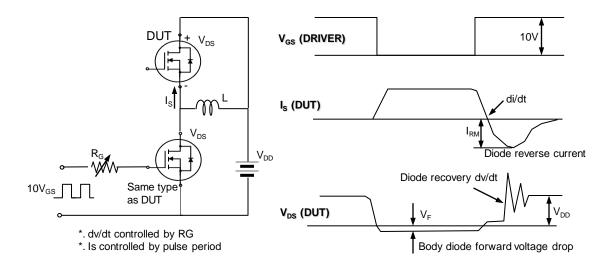


Fig 13. Peak diode recovery dv/dt test circuit & waveform





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