74HC132-Q100; 74HCT132-Q100

Quad 2-input NAND Schmitt trigger Rev. 4 — 12 June 2018

Product data sheet

General description

The 74HC132-Q100; 74HCT132-Q100 is a quad 2-input NAND gate with Schmitt-trigger inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}. Schmitt trigger inputs transform slowly changing input signals into sharply defined jitter-free output signals.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- · Complies with JEDEC standard no. 7A
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- · Multiple package options

Applications

- · Wave and pulse shapers
- · Astable multivibrators
- · Monostable multivibrators

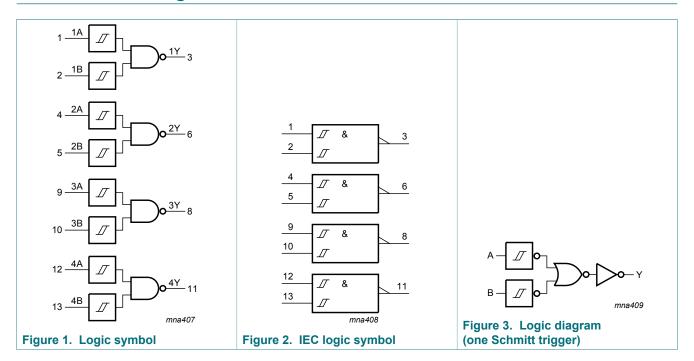
Ordering information

Table 1. Ordering information

- Crucing in	7								
Type number	Package								
	Temperature range	Name	Description	Version					
74HC132D-Q100	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads;	SOT108-1					
74HCT132D-Q100			body width 3.9 mm						
74HC132PW-Q100	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads;	SOT402-1					
74HCT132PW-Q100	40 °C to +125 °C TSSOP12		body width 4.4 mm						
74HC132BQ-Q100	-40 °C to +125 °C	DHVQFN14	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm	SOT762-1					

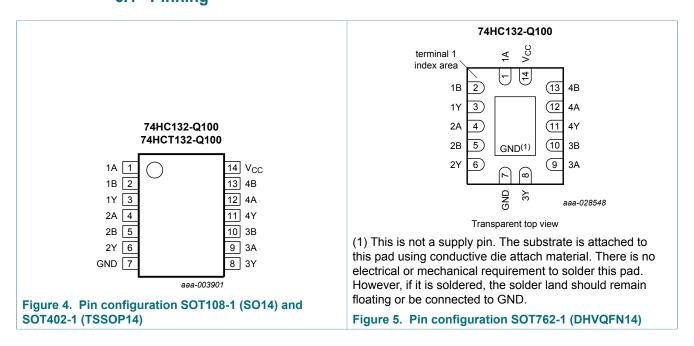


5 Functional diagram



6 Pinning information

6.1 Pinning



6.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1A to 4A	1, 4, 9, 12	data input
1B to 4B	2, 5, 10, 13	data input
1Y to 4Y	3, 6, 8, 11	data output
GND	7	ground (0 V)
V _{CC}	14	supply voltage

7 Functional description

Table 3. Function table ^[1]

Input		Output
nA	nB	nY
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

^[1] H = HIGH voltage level; L = LOW voltage level; X = don't care.

8 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I _{OK}	output clamping current	$V_{\rm O} < -0.5 \text{V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{V}$ [1]	-	±20	mA
I _O	output current	-0.5 V < V _O < V _{CC} + 0.5 V	-	±25	mA
I _{CC}	supply current		-	50	mA
I_{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	SO14, TSSOP14 and DHVQFN14 packages [2]	-	500	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

^[2] For SO14 package: P_{tot} derates linearly with 8 mW/K above 70 °C. For TSSOP14 packages: P_{tot} derates linearly with 5.5 mW/K above 60 °C. For DHVQFN14 packages: P_{tot} derates linearly with 4.5 mW/K above 60 °C.

9 Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	74HC132-Q100			74F	Unit		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C

10 Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C			°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74HC13	2-Q100		<u> </u>						'	
V _{OH}	HIGH-level output	$V_I = V_{T+}$ or V_{T-}								
	voltage	I_{O} = -20 μ A; V_{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -20 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I_{O} = -20 μ A; V_{CC} = 6.0 V	5.9	6.0	-	5.9	-	5.9	-	V
	I_{O} = -4.0 mA; V_{CC} = 4.5 V	3.98	4.32	-	3.84	-	3.7	-	V	
		I_{O} = -5.2 mA; V_{CC} = 6.0 V	5.48	5.81	-	5.34	-	5.2	-	V
V _{OL}	LOW-level output	$V_I = V_{T+}$ or V_{T-}								
	voltage	I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		I_{O} = 5.2 mA; V_{CC} = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0 \text{ V}$	-	-	2.0	-	20	-	40	μΑ
C _I	input capacitance		-	3.5	-	-	-	-	-	pF

Symbol	Parameter	Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Тур	Max	Min	Max	Min	Max	
74HCT1	32-Q100		<u> </u>		<u> </u>	ı	'		'	
V _{OH}		$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	voltage	Ι _Ο = -20 μΑ	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -4.0 mA	3.98	4.32	-	3.84	-	3.7	-	V
	· ·	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	voltage	I _O = 20 μA;	_	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA;	-	0.15	0.26	-	0.33	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2.0	-	20	-	40	μA
ΔI _{CC}	additional supply current	per input pin; $V_I = V_{CC} - 2.1 \text{ V}$; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 4.5 \text{ V}$ to 5.5 V	-	30	108	-	135	-	147	μА
Cı	input capacitance		-	3.5	-	-	-	-	-	pF

11 Dynamic characteristics

Table 7. Dynamic characteristics

 $GND = 0 \ V; \ C_L = 50 \ pF;$ for test circuit see Figure 7.

Symbol	Parameter	Conditions		25 °C		-40 °C to	Unit	
			Min	Тур	Max	Max (85 °C)	Max (125 °C)	
74HC13	2-Q100							
t _{pd} propagation delay	propagation delay	nA, nB to nY; see Figure 6 [1]						
	V _{CC} = 2.0 V	-	36	125	155	190	ns	
	V _{CC} = 4.5 V	-	13	25	31	38	ns	
		V _{CC} = 5.0 V; C _L = 15 pF	-	11	-	-	-	ns
		V _{CC} = 6.0 V	-	10	21	26	32	ns
t _t	transition time	see Figure 6 [2]						
		V _{CC} = 2.0 V	-	19	75	95	110	ns
		V _{CC} = 4.5 V	-	7	15	19	22	ns
		V _{CC} = 6.0 V	-	6	13	16	19	ns
C _{PD}	power dissipation capacitance	per package; $V_1 = GND$ to V_{CC} [3]	-	24	-	-	-	pF

Symbol	Parameter	Conditions			25 °C		-40 °C to	Unit	
				Min	Тур	Max	Max (85 °C)	Max (125 °C)	
74HCT1	32-Q100								
t _{pd}	propagation delay	nA, nB to nY; see Figure 6	[1]						
		V _{CC} = 4.5 V		-	20	33	41	50	ns
		V_{CC} = 5.0 V; C_L = 15 pF		-	17	-	-	-	ns
t _t	transition time	V _{CC} = 4.5 V; see <u>Figure 6</u>	[2]	-	7	15	19	22	ns
C _{PD}	power dissipation capacitance	per package; V _I = GND to V _{CC} - 1.5 V	[3]	-	20	-	-	-	pF

 $\begin{tabular}{lll} t_{pd} is the same as t_{PHL} and t_{PLH}. \\ [2] t_{t} is the same as t_{THL} and t_{TLH}. \\ [3] C_{PD} is used to determine the dynamic power dissipation (P_{D} in μW): \end{tabular}$

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

 f_o = output frequency in MHz;

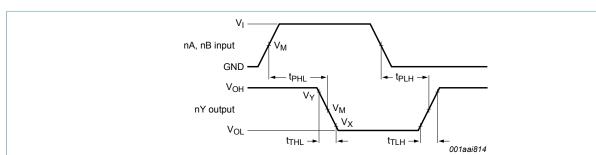
C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0) = \text{sum of outputs.}$

11.1 Waveforms and test circuit



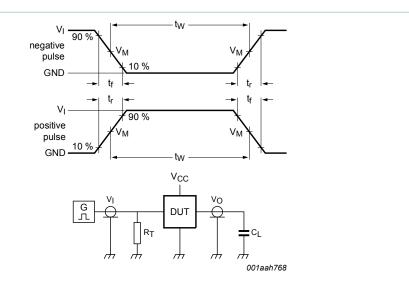
Measurement points are given in Table 8.

V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Figure 6. Input to output propagation delays

Table 8. Measurement points

Туре	Input	Output	Dutput							
	V _M	V_{M}	V _X	V _Y						
74HC132-Q100	0.5V _{CC}	0.5V _{CC}	0.1V _{CC}	0.9V _{CC}						
74HCT132-Q100	1.3 V	1.3 V	0.1V _{CC}	0.9V _{CC}						



Test data is given in Table 9.

Definitions test circuit:

 R_T = termination resistance should be equal to output impedance Z_o of the pulse generator.

 C_L = load capacitance including jig and probe capacitance.

Figure 7. Test circuit for measuring switching times

Table 9. Test data

Туре	Input L		Load	Test	
	V _I	t _r , t _f C _L			
74HC132-Q100	V _{CC}	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}	
74HCT132-Q100	3.0 V	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}	

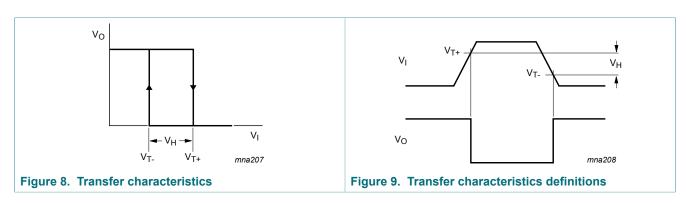
12 Transfer characteristics

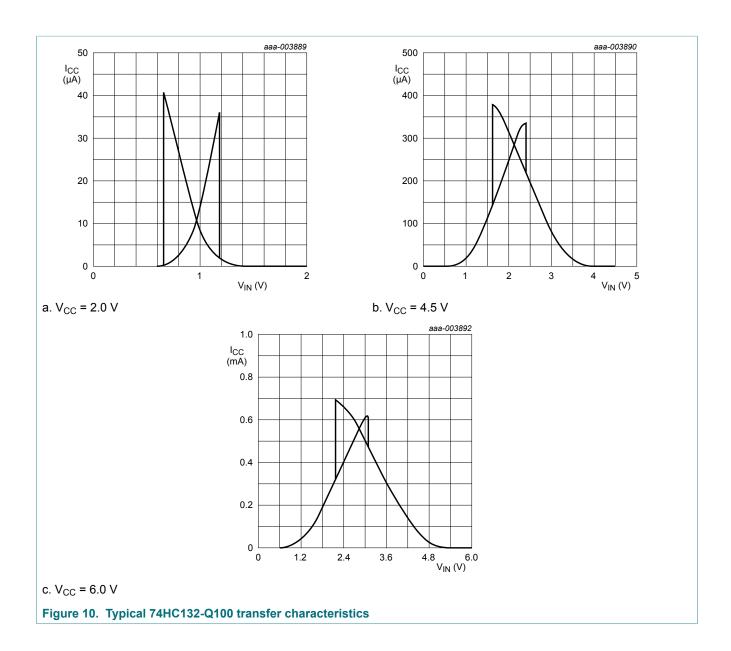
Table 10. Transfer characteristics

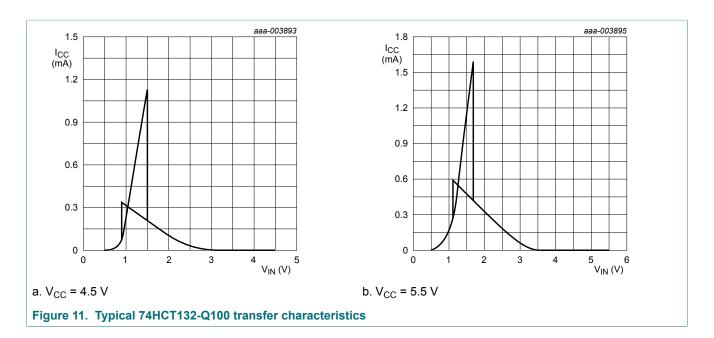
At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for waveforms see Figure 8 to Figure 11.

Symbol	Parameter	Conditions	T,	_{amb} = 25 °	°C	T _{amb} = -40 °C to +85 °C		T _{amb} = −40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
74HC13	2-Q100				-	1		1		
V _{T+}	positive-going threshold	V _{CC} = 2.0 V	0.7	1.18	1.5	0.7	1.5	0.7	1.5	V
	voltage	V _{CC} = 4.5 V	1.7	2.38	3.15	1.7	3.15	1.7	3.15	V
		V _{CC} = 6.0 V	2.1	3.14	4.2	2.1	4.2	2.1	4.2	V
	negative-going threshold	V _{CC} = 2.0 V	0.3	0.63	1.0	0.3	1.0	0.3	1.0	V
	voltage	V _{CC} = 4.5 V	0.9	1.67	2.2	0.9	2.2	0.9	2.2	V
		V _{CC} = 6.0 V	1.2	2.26	3.0	1.2	3.0	1.2	3.0	V
V _H	hysteresis voltage	V _{CC} = 2.0 V	0.2	0.55	1.0	0.2	1.0	0.2	1.0	V
		V _{CC} = 4.5 V	0.4	0.71	1.4	0.4	1.4	0.4	1.4	V
		V _{CC} = 6.0 V	0.6	0.88	1.6	0.6	1.6	0.6	1.6	V
74HCT1	32-Q100		1	·	·		<u> </u>	J.	·	
V _{T+}	positive-going threshold	V _{CC} = 4.5 V	1.2	1.41	1.9	1.2	1.9	1.2	1.9	V
	voltage	V _{CC} = 5.5 V	1.4	1.59	2.1	1.4	2.1	1.4	2.1	V
V _{T-}	negative-going threshold	V _{CC} = 4.5 V	0.5	0.85	1.2	0.5	1.2	0.5	1.2	V
	voltage	V _{CC} = 5.5 V	0.6	0.99	1.4	0.6	1.4	0.6	1.4	V
V _H	hysteresis voltage	V _{CC} = 4.5 V	0.4	0.56	-	0.4	-	0.4	-	V
		V _{CC} = 5.5 V	0.4	0.60	-	0.4	-	0.4	-	V

12.1 Transfer characteristics waveforms







13 Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$ where:

 P_{add} = additional power dissipation (μW);

 f_i = input frequency (MHz);

 t_r = rise time (ns); 10 % to 90 %;

 $\Delta I_{CC(AV)}$ = average additional supply current (μA).

 t_f = fall time (ns); 90 % to 10 %;

Average $\Delta I_{CC(AV)}$ differs with positive or negative input transitions, as shown in <u>Figure 12</u> and <u>Figure 13</u>.

An example of a relaxation circuit using the 74HC132-Q100; 74HCT132-Q100 is shown in Figure 14.

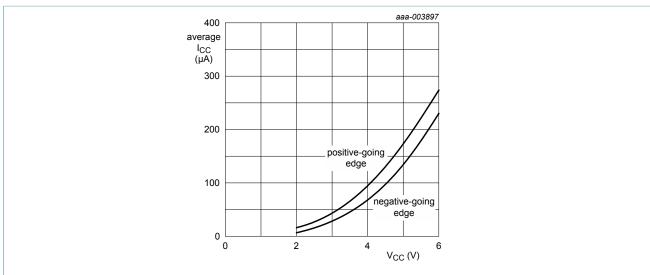


Figure 12. Average additional supply current as a function of V_{CC} for 74HC132-Q100; linear change of V_{I} between $0.1V_{CC}$ to $0.9V_{CC}$.

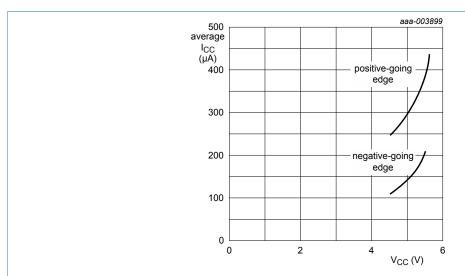
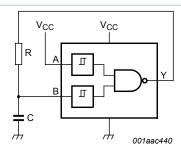


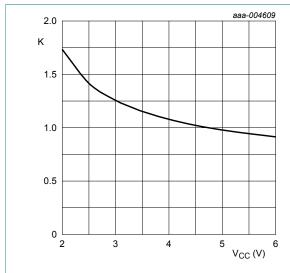
Figure 13. Average additional supply current as a function of V_{CC} for 74HCT132-Q100; linear change of V_I between $0.1V_{CC}$ to $0.9V_{CC}$.



For 74HC132-Q100 and 74HCT132-Q100: $f = \frac{1}{T} \approx \frac{1}{K \times RC}$

Typical K-factor for relaxation oscillator, see Figure 15 and Figure 16

Figure 14. Relaxation oscillator



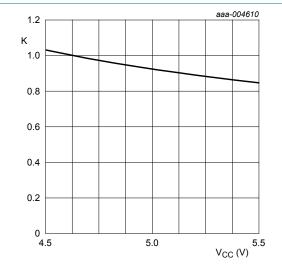
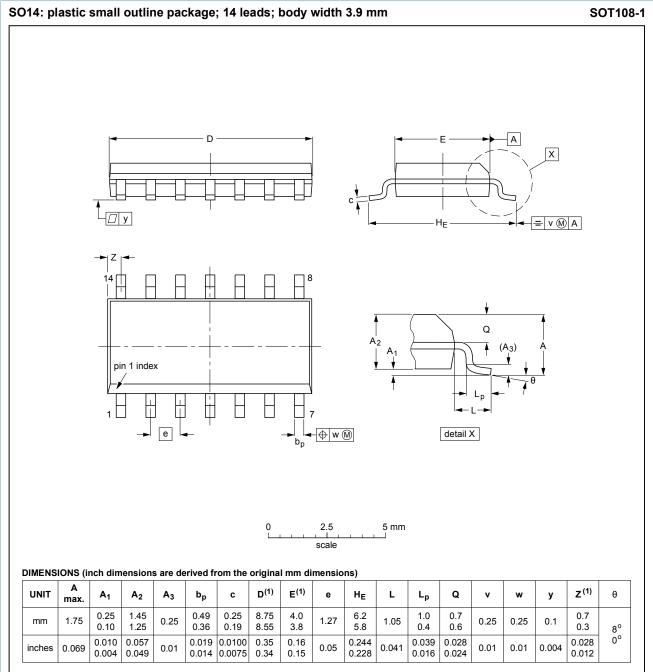


Figure 15. K-factor for 74HC132-Q100

Figure 16. K-factor for 74HCT132-Q100

14 Package outline



Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

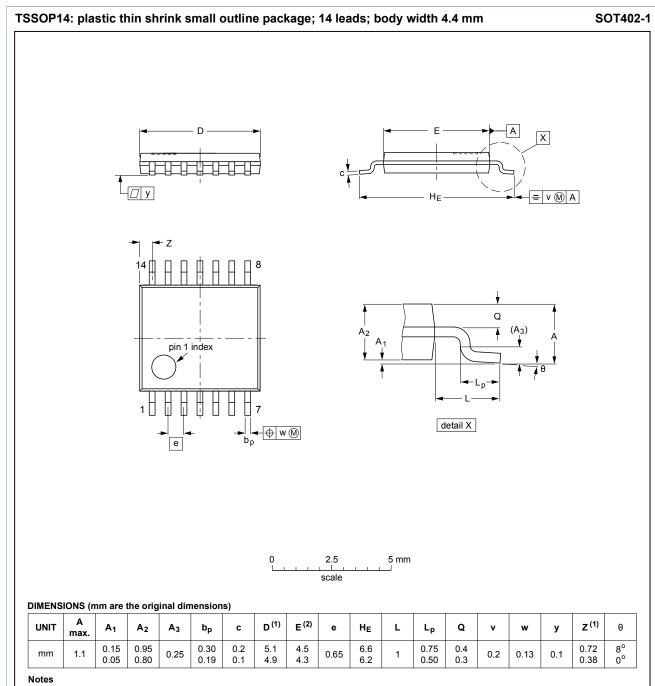
OUTLINE VERSION	REFERENCES			EUROPEAN	ISSUE DATE	
	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT108-1	076E06	MS-012				99-12-27 03-02-19

Figure 17. Package outline SOT108-1 (SO14)

74HC_HCT132_Q100

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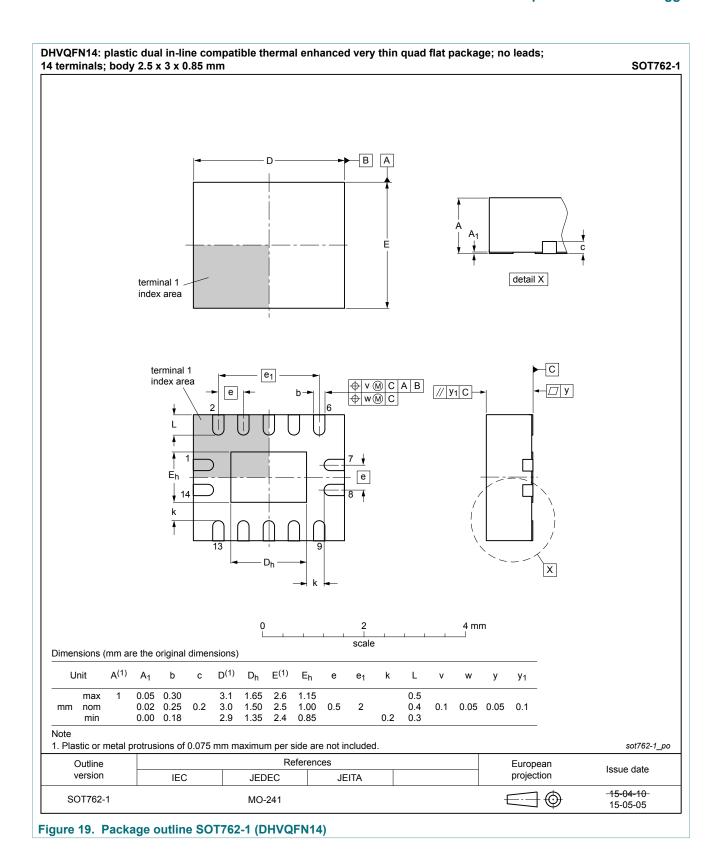
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- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC JEDEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT402-1		MO-153				99-12-27 03-02-18

Figure 18. Package outline SOT402-1 (TSSOP14)



15 Abbreviations

Table 11. Abbreviations

Acronym	Description
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model

16 Revision history

Table 12. Revision history

Table 12. Revision metery					
Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC_HCT132_Q100 v.4	20180612	Product data sheet	-	74HC_HCT132_Q100 v.3	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Added type number 74HC132BQ (SOT762-1) 				
74HC_HCT132_Q100 v.3	20151201	Product data sheet	-	74HC_HCT132_Q100 v.2	
Modifications:	General description changed.				
74HC_HCT132_Q100 v.2	20120813	Product data sheet	-	74HC_HCT132_Q100 v.1	
Modifications:	Figure 15 and Figure 16 added (typical K-factor for relaxation oscillator).				
74HC_HCT132_Q100 v.1	20120712	Product data sheet	-	-	

17 Legal information

17.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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