AUTOMOTIVE

RoHS

COMPLIANT

GREEN (5-2008)\*\*



## Vishay Semiconductors

# Silicon Phototransistor in 0805 Package



#### DESCRIPTION

TEMT7000X01 is a high speed silicon NPN epitaxial planar phototransistor in a miniature 0805 package for surface mounting on printed boards. The device is sensitive to visible and near infrared radiation.

#### **FEATURES**

• Package type: surface mount



• Dimensions (L x W x H in mm): 2 x 1.25 x 0.85

AEC-Q101 qualified

High photo sensitivity

· High radiant sensitivity

• Suitable for visible and near infrared radiation

Fast response times

• Angle of half sensitivity:  $\varphi = \pm 60^{\circ}$ 

Package matched with IR emitter series VSMB1940X01

• Floor life: 168 h, MSL 3, acc. J-STD-020

· Lead (Pb)-free reflow soldering

 Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### Note

\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

#### **APPLICATIONS**

- Detector in automotive applications
- Light sensors
- Radiation sensors

PRODUCT SUMMARY			
COMPONENT	I <sub>caE</sub> (μΑ)	φ (deg)	λ <sub>0.1</sub> (nm)
TEMT7000X01	225 to 675	± 60	470 to 1090

#### Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
TEMT7000X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

#### Note

MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		I <sub>C</sub>	20	mA	
Power power dissipation	T <sub>amb</sub> ≤ 55 °C	P <sub>V</sub>	100	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	Acc. reflow profile fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R <sub>thJA</sub>	270	K/W	



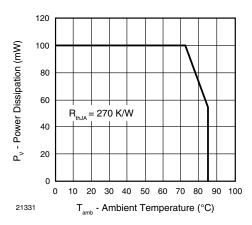


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I <sub>C</sub> = 0.1 mA	$V_{CEO}$	20			V
Collector dark current	V <sub>CE</sub> = 5 V, E = 0	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz, E = 0$	C <sub>CEO</sub>		25		pF
Collector light current	$E_e$ = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, $V_{CE}$ = 5 V	I <sub>CA</sub>	225	450	675	μA
Angle of half sensitivity		φ		± 60		deg
Wavelength of peak sensitivity		$\lambda_{p}$		850		nm
Range of spectral bandwidth		λ <sub>0.1</sub>		470 to 1090		nm
Collector emitter saturation voltage	$I_{C} = 0.05 \text{ mA}$	V <sub>CEsat</sub>			0.4	V
Temperature coefficient of I <sub>ca</sub>	$E_e$ = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, $V_{CE}$ = 5 V	Tk <sub>lca</sub>		1.1		%/K

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

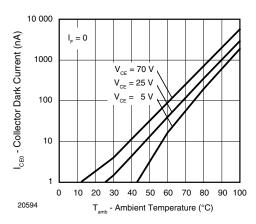


Fig. 2 - Collector Dark Current vs. Ambient Temperature

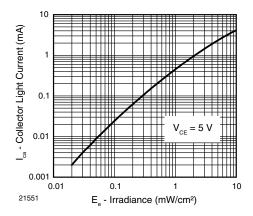


Fig. 3 - Collector Light Current vs. Irradiance

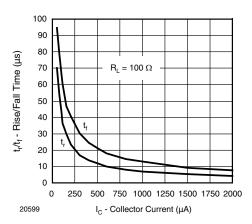


Fig. 4 - Rise/Fall Time vs. Collector Current

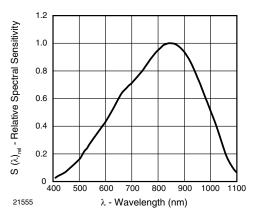


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

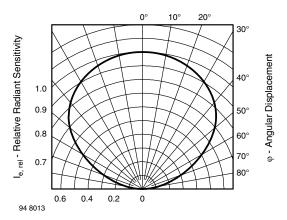


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

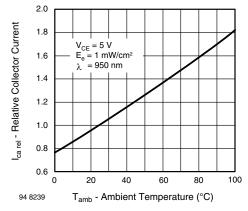


Fig. 7 - Relative Collector Current vs. Ambient Temperature

#### **REFLOW SOLDER PROFILE**

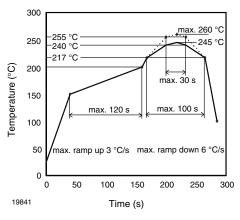


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

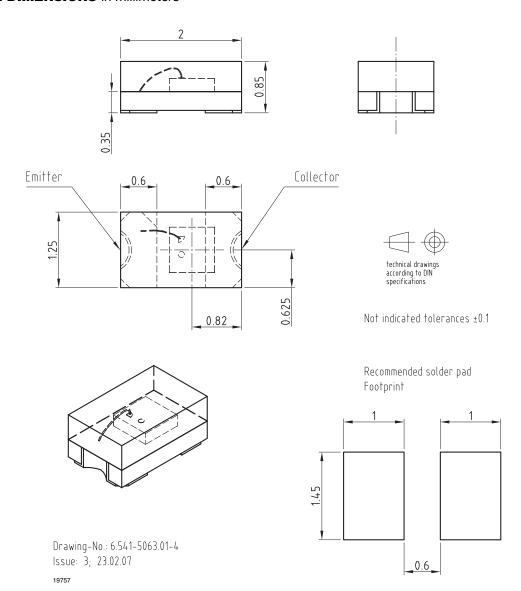
Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

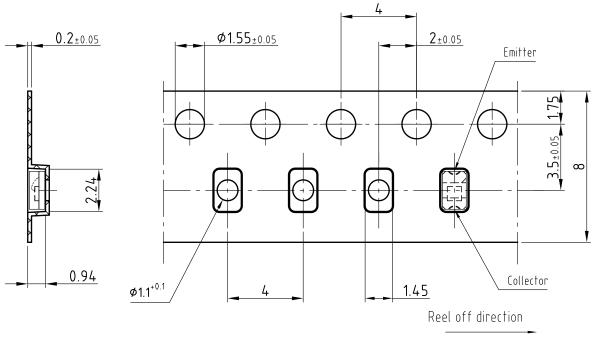
#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.

### **PACKAGE DIMENSIONS** in millimeters



#### **BLISTER TAPE DIMENSIONS** in millimeters



Drawing-No.: 9.700-5310.01-4

Issue: 2; 14.08.07

20690

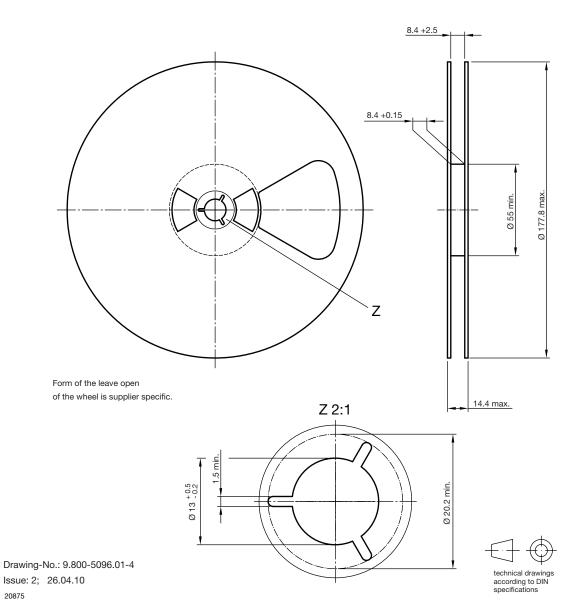
Not indicated tolerances ±0.1

Quantity per reel: 3000 pcs



technical drawings according to DIN specifications

### **REEL DIMENSIONS** in millimeters





## **Legal Disclaimer Notice**

Vishay

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