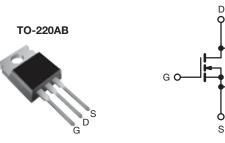


Vishay Siliconix

Power MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	900				
R _{DS(on)} (Ω)	V _{GS} = 10 V 3.7				
Q _g (Max.) (nC)	78				
Q _{gs} (nC)	10				
Q _{gd} (nC)	42				
Configuration	Single				



N-Channel MOSFET

FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- · Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

Third generation MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universially preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION		
Package	TO-220AB	
Lead (Pb)-free	IRFBF30PbF	
	SiHFBF30-E3	
SnPb	IRFBF30	
	SiHFBF30	

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMITE	UNIT
Drain-Source Voltage			V _{DS}	900	V
Gate-Source Voltage			V _{GS}	± 20	v
Continuous Drain Current	Continuous Drain Current $V_{co.}$ at 10 V $T_c = 25 \text{ °C}$			3.6	
Continuous Drain Current	V _{GS} at 10 V	T _C = 100 °C	I _D	2.3	A
Pulsed Drain Current ^a			I _{DM}	14	
Linear Derating Factor				1.0	W/°C
Single Pulse Avalanche Energy ^b			E _{AS}	250	mJ
Repetitive Avalanche Current ^a			I _{AR}	3.6	А
Repetitive Avalanche Energy ^a			E _{AR}	13	mJ
Maximum Power Dissipation $T_{C} = 25 \text{ °C}$			PD	125	W
Peak Diode Recovery dV/dt ^c			dV/dt	1.5	V/ns
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature) for 10 s				300 ^d	U
Mounting Torque				10	lbf ∙ in
Mounting Torque	6-32 or M3 screw			1.1	N·m

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. V_{DD} = 50 V, starting T_J = 25 °C, L = 36 mH, R_g = 25 Ω , I_{AS} = 3.6 A (see fig. 12).

c. $I_{SD} \le 3.6$ A, dI/dt ≤ 70 A/µs, $V_{DD} \le 600$, $T_{J} \le 150$ °C.

d. 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

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THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	62	
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50	-	°C/W
Maximum Junction-to-Case (Drain)	R _{thJC}	-	1.0	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static				I			
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} :	= 0 V, I _D = 250 μΑ	900	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference	e to 25 °C, I _D = 1 mA	-	1.1	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	- V _{GS} , I _D = 250 μΑ	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 20 V	-	-	± 100	nA
		V _{DS} = 900 V, V _{GS} = 0 V		-	-	100	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 720 V	∕, V _{GS} = 0 V, T _J = 125 °C	-	-	500	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 2.2 A ^b	-	-	3.7	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	100 V, I _D = 2.2 A ^b	2.3	-	-	S
Dynamic						•	
Input Capacitance	C _{iss}		$V_{GS} = 0 V$,	-	1200	-	
Output Capacitance	C _{oss}		$V_{DS} = 25 V$,	-	320	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1	.0 MHz, see fig. 5	-	200	-	1
Total Gate Charge	Qg			-	-	78	nC
Gate-Source Charge	Q_gs	V _{GS} = 10 V	$I_D = 3.6 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13^{b}	-	-	10	
Gate-Drain Charge	Q _{gd}			-	-	42	
Turn-On Delay Time	t _{d(on)}				14	-	- ns
Rise Time	t _r	V_{DD} = 450 V, I _D = 3.6 A, R _g = 12 Ω, R _D = 120 Ω, see fig. 10 ^b		-	25	-	
Turn-Off Delay Time	t _{d(off)}			-	90	-	
Fall Time	t _f			-	30	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	
Internal Source Inductance	L _S			-	7.5	-	nH
Drain-Source Body Diode Characteristic	s						•
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	3.6	А
Pulsed Diode Forward Current ^a	I _{SM}			-	-	14	
Body Diode Voltage	V_{SD}	$T_J = 25 \ ^{\circ}C, \ I_S = 3.6 \ A, \ V_{GS} = 0 \ V^b$		-	-	1.8	V
Body Diode Reverse Recovery Time	t _{rr}	$T_J = 25 \text{ °C}, I_F = 3.6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}^b$		-	430	650	ns
Body Diode Reverse Recovery Charge	Q _{rr}			-	1.4	2.1	μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L_S and L_D)				L _D)	

Notes

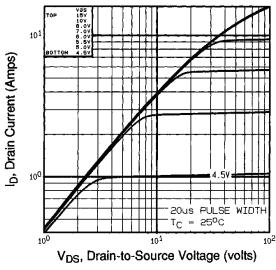
a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width $\leq 300~\mu s;$ duty cycle $\leq 2~\%.$

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



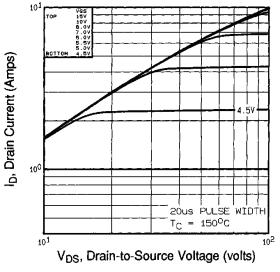


Fig. 2 -Typical Output Characteristics, T_C = 150 °C

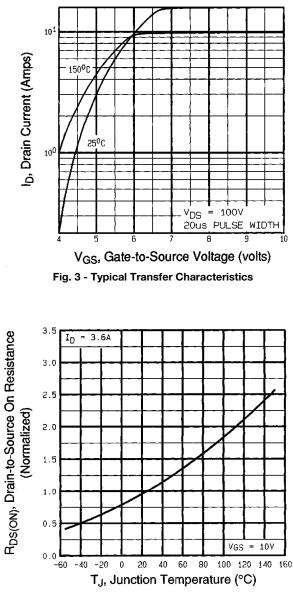


Fig. 4 - Normalized On-Resistance vs. Temperature

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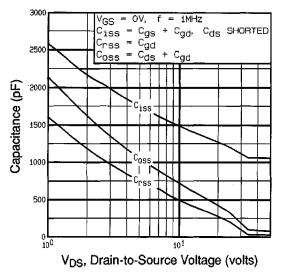


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

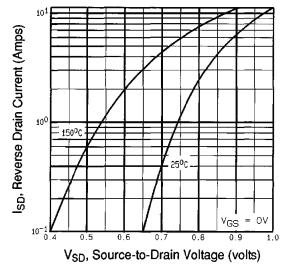


Fig. 7 - Typical Source-Drain Diode Forward Voltage

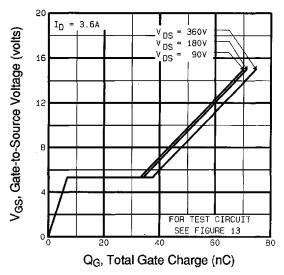
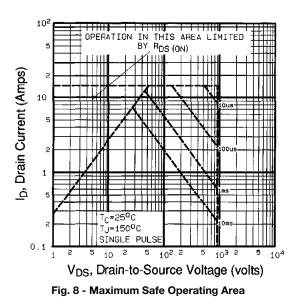


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



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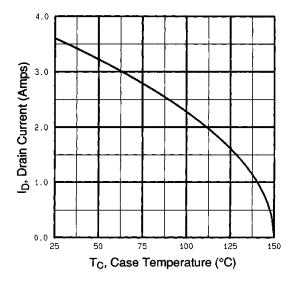


Fig. 9 - Maximum Drain Current vs. Case Temperature

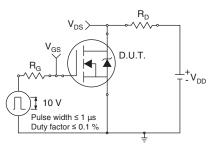


Fig. 10a - Switching Time Test Circuit

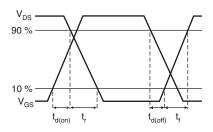
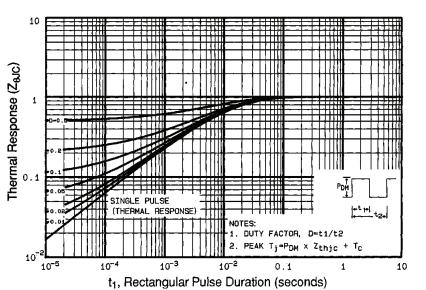
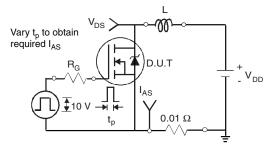
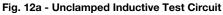


Fig. 10b - Switching Time Waveforms









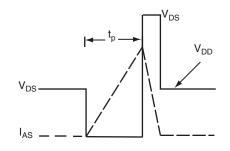


Fig. 12b - Unclamped Inductive Waveforms

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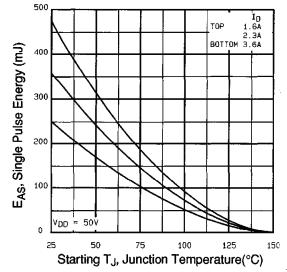


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

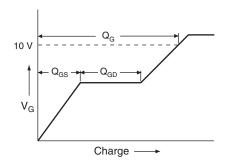


Fig. 13a - Basic Gate Charge Waveform

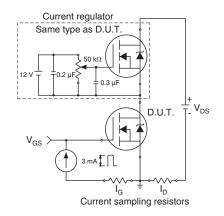
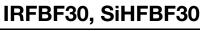


Fig. 13b - Gate Charge Test Circuit

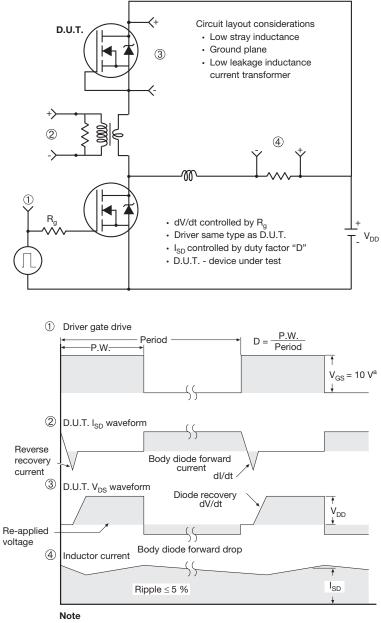
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a. $V_{GS} = 5 V$ for logic level devices

Fig. 14 - For N-Channel

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TO-220-1



DIM.	MILLIN	MILLIMETERS		INCHES		
DIN.	MIN.	MAX.	MIN.	MAX.		
А	4.24	4.65	0.167	0.183		
b	0.69	1.02	0.027	0.040		
b(1)	1.14	1.78	0.045	0.070		
С	0.36	0.61	0.014	0.024		
D	14.33	15.85	0.564	0.624		
E	9.96	10.52	0.392	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.10	6.71	0.240	0.264		
J(1)	2.41	2.92	0.095	0.115		
L	13.36	14.40	0.526	0.567		
L(1)	3.33	4.04	0.131	0.159		
ØР	3.53	3.94	0.139	0.155		
Q	2.54	3.00	0.100	0.118		
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031						

Note

- M^{\star} = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM

Package Picture				
ASE		Xi	'an	
		IRF 9510 744K AB		

Revison: 14-Dec-15

1 For technical questions, contact: <u>hvm@vishay.com</u> Document Number: 66542

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